Academic Programme Guide

of

Bachelor of Engineering
Electronics and Communication Engineering

Based on Choice Based Credit System (CBCS) /

Elective Course System (ECS)

CHITKARA
UNIVERSITY
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NAAC ACCREDITED

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1. General Information

Bachelor of Engineering Programme in Electronics and Communication Engineering prepares the students for the ever-expanding field of Electronics and Communication Engineering. The curriculum is directed towards the major applications such as wireless communications, embedded systems and Internet of things (IoT), Robotics, and Very large scale Integration (VLSI). We believe that, many creative opportunities exist at the boundaries of Computer Science engineering and Electronics and Communication Engineering, so accordingly cross-training schedule for the students across disciplinary boundaries is planned. The normal duration of course is four years. Initially in the curriculum of Electronics and Communication Engineering few courses are in common with the other engineering programs. Thereafter, for 3rd and 4^{th}year the programme is structured into different verticals to allow customization by individual students based on their own personal perspectives. The Programme Educational Objectives (PEOs) and Programme Outcomes of Electronics and Communication Engineering are summarized as below:

1.1 Programme Educational Objectives (PEOs):

PEO1: The graduating students would be able to make choice to go for a professional career in core technical domain or to pursue higher studies in the field of Electronics and Communication Engineering and other related areas and succeed in their academic and research careers.
PEO2: The graduating students would be able to solve socially relevant engineering problems by designing/developing the products with the help of acquired multidisciplinary knowledge.
PEO3: The graduating students would exhibit a good command over interpersonal communication skills, leadership and team work, and possess ethical values in their chosen professional careers.
PEO4: The graduating students would be ready to serve the society at local, national and international level with the help of life-long learning accomplished for professional development through practical training, courses by international faculty and specialized certifications.

1.2 Programme Outcomes (POs):

The graduating students would be able to

PO1: Possess an ability to apply the knowledge of mathematics science, engineering fundamentals, and engineering specialization to the solution of complex engineering problems.
PO2: Possess an ability to identify, formulate, review research literature, and analyze complex engineering problems using first principles of mathematics, natural sciences, and engineering sciences.
PO3: Possess an ability to design solutions for complex engineering problems and design system components or processes to meet the specific needs with appropriate consideration of the public health and safety, the cultural, societal, and environmental sustainability.
PO4: Possess an ability to use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions
PO5: Possess an ability to create, elect, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
PO6: Possess an ability to apply reasoning informed by the contextual knowledge of societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practices.

PO7: To understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge and need for sustainable development.

PO8: Possess an ability to apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.

PO9: Possess an ability to function effectively as an individual and as a member or leader in diverse teams and in multidisciplinary settings**.**

PO10: Possess an ability to communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.

PO11: Possess an ability to demonstrate knowledge and understanding of the engineering, management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.

PO12: Possess an ability to recognize the need and have ability to engage in independent and lifelong learning in the broadest context of technological change.

1.3 PEOs - Mission mapping

University Vision:
To be a globally recognized organization promoting academic excellence through interdisciplinary applied research and to expand realms of knowledge through innovation.

University Mission:
M1: To carry out the academic processes in accordance with global standards through active teacher-student-industry participation.

M2: To promote research, innovation and entrepreneurship in collaboration with industry, research laboratories and academic institutions of global repute.

M3: To inculcate high moral, ethical and professional values amongst our students, faculty & staff.

M4: To contribute in building skillful society.

In B.E. Electronics and Computer Engineering Programme, the programme educational objectives (PEOs) are well-designed on the mission of providing the graduating students with knowledge and expertise required for professional practices in engineering and the necessary technical skills for working in corporate industries.

The graduating students are prepared for participation in a global environment, where number of opportunities exists for students to connect with one another across the world. Each year, professors from different universities across the globe visits Chitkara University to provide international exposure to students. During the Global Week (GW), cross-cultural competence and knowledge sharing between the students and faculties on both sides are infused, which also facilitates the social cultural immersion programs, helping students in their international careers. Engineering Projects in Community Service–(EPICS) course is offered to students which involves service learning, and reflecting upon an organized activity to benefit their communities, in order to deepen their knowledge of a topic or perspective they have learned about in the classroom. Aiming at developing student's personality through community service, NSS activities are offered to students to instill the idea of social welfare and to provide service to society without bias. To enrich student's interpersonal skills, variety of extracurricular activities have been inculcated in the course curriculum in the form of national level technical and cultural festivals such as EXPLORE and Rangrez respectively on a yearly basis. A vital role is played by the department for overall grooming of the student through organizing industrial visits, workshops and technical quizzes/debates and project showcase competitions by technical societies (IETE, and IEI) and department cultural club (E-Buzz). The students are offered to participate or organize such events. These value-added activities have been designed taken into account various Programme Objectives (POs) such as PO3, PO4, PO7, PO8, PO9, and PO10, and have been in accordance with all the mentioned Programme Educational Objectives (PEOs). By offering sports related activities, the overall purpose of service-learning is achieved with an emphasis on good health and well-being.

The programme also aims at achieving the sustainable development goals set up by the United Nations. PO5, and PO10 promote development for sustainable society, which depends on three aspects: Economic Forces, Public policy changes, Changes in Life-style. An engineer can contribute to sustainable development; as the role of technology in the transition to a sustainable society is a central one. Present day technologies include Cloud Computing, Internet of Things (IoT), Artificial Intelligence, Augmented Reality (AR), Virtual Reality (VR), and Robotic Process Automation. The Programme of Electronics and Computer Engineering is designed to build innovators, entrepreneurs, leaders and responsible citizens with the above-mentioned skills and knowledge that will help them contribute to achieving the UN 2030 agenda.

PEOs and POs are designed and oriented to meet the mission of university. The PEOs ensure that the graduating students are well equipped with technical knowledge, command over communication skills, leadership qualities, accomplishment of life-long learning to apply for solving the relevant engineering problems in community at local, national and international level, thereby helping establish a balanced social and professional environment. Thus, the objective of the programme is to produce high quality analytic and creative minded electronics engineers to transform the society into knowledgeable, avant-grade and sustainable society.

1.4 Programme Constitution:

- The courses offered in first year are applied basic engineering subjects.
- Programme in second year have core and elective electronics subjects and is structured keeping in mind the requirements of exams like GATE, UPSC, and IES.
- In year 3 and 4, the programme is structured in form of specialization track. Verticals are offered in the specialization track that allows students to customize their preferences for subjects, based on individual interest and carrier options. The verticals include Robotics and Automation, VLSI Design and Verification, and Core Full Stack. These help the students to gain deeper knowledge and skills in the selected area.
- We follow outcome based education and programme outcomes are mapped with course outcomes. For details see the appendix A of mapping report.

1.5 Placement Opportunities:

Electronics and Communication Graduates have tremendous employment opportunities in design, development, research, marketing, Customer support, Sales and testing areas in industries of: Optical, Mobile, Wireless communication, Satellite, Computing, Bio medical instrumentation, Health engineering, Embedded System design, VLSI design, Software development and testing, Hardware design and testing.

2. Eligibility for Admission

The student seeking admission in BE programme should have minimum 60% marks in 12th grade or equivalent exam as declared by JEE, with Mathematics, and Physics as compulsory subjects. He / She should have appeared in JEE. The admission is based purely on merit. During admission process, the university follows reservation policy as decided by the State.

3.Programme Duration

The duration of the BE programme is four years - divided into 8 semesters. The maximum duration permissible for completion of B.E. programme is shown in table 1:

Table 1: Duration of the Programme

Normal duration of the degree programme	Maximum time allowed for completion of programme
4 years	4 + 2 years

4.Pedagogical Aspects

The structural layout of the programme and its courses requires that each course be divided in lecture, tutorial and practical sessions. Duration of each session of the course is 55minutes.
Lecture sessions: Lectures are delivered by traditional – Chalk& Talk method, supplemented by modern Information Communication Technology (ICT) methods. The students are encouraged to ask questions and involve in group discussion to the extent allowed by the faculty. In some

subjects where case study based methodology is adopted, the lectures are supplemented by discussions on case studies.

Tutorial Sessions: The tutorial sessions are small groups of students interacting with the faculty, solving application oriented and analytical problems. The tutorial sessions are very interactive and inculcate problem solving skills in the students.

Lab / Practical Sessions: During lab / practical sessions, the students work on prescribed list of experiments and do what they have learnt in the Lecture / Tutorial sessions.

Projects: The students identify their team mates (maximum 4 students per team) and work on a unique project allotted to them by faculty / group of faculty members. The projects are allotted to them either at the start of semester or at a later stage (but not later than Sessional test I). Projects are designed by considering real world challenges. Thus, the project statements are made in such a way that the students while working on these projects apply the concepts learned so far and the deliverables are multi-faceted.

5.Programme Structure

The various courses of Electronics and Communication Engineering programme are categorized in terms of their academic affinity or their functional objectives as Basic Science course (BSC), Engineering Science course (ESC) Programme Core Courses (PCC), Programme Elective Course (PEC) Open elective course (OEC), Mandatory course (MC), and Project work (PW), Generic course (GC) Specialization Courses, and Special Courses. PCC are compulsory set of courses. There are specified number of elective courses classified as PEC or OEC. The students are offered a pool of different elective courses (Based on different verticals) out of which they will choose the course/courses as per their interest.

Special Courses

Integrated Project: Every year, the students identify their team mates (at the most 4 students per team) and work on a unique integrated project allotted to them by faculty / group of faculty members. The projects are allotted to them either at the start of each semester or at a later stage (but not later than Sessional test I) in the semester. Integrated projects are designed by the faculty keeping in mind the courses which the students have studied so far and are currently studying. Thus, the project statements are made in such a way that the students while working on these projects apply the concepts learned so far and the deliverables are multi-faceted. The students work on the Integrated Project during their lab hours.

Special Courses

Special courses comprised of IOHE (Industry Oriented Hands-on Experience) and also GW (Global Week). Industry Oriented Hands-on Experience is a 6 months industrial training which is being offered in the final year of the degree. Student's get the real industry experience through this course. Students are free to choose his/her IOHE with the due approval of the dean while Global Week is a one-week programme where student's get the international exposure.

IOHE: IOHE is a real experience at the industry. This may or may not be in a specific skill set. Head of the Department and the Office of External affairs (optional) has the authority to assign IOHEs, at appropriate industries. The students are given freedom to choose his/her own IOHE, but the decision of Head is final while allotment.

GW: Global week is one week programme where students are provided international exposure. Faculty from different part of world teaches the students for one week as per their expertise.

Table 2: Course Scheme

Year 1, Semester – 1

Code	Subject	Hours (L-T-P)	Credit
AML5101	Engineering Mathematics- I	4-1-0	5
PYL5101	Engineering Physics	4-1-0	5
EEL4103	Basics of Electrical Engineering	4-0-0	4
EEP1103	Basics of Electrical Engineering Lab	0-0-2	1
CHL4101	Engineering Chemistry	4-0-0	4
CHP1101	Engineering Chemistry Lab	0-0-2	1
PYP1101	Engineering Physics Lab	0-0-2	1
HUL2101	Disaster Management	2-0-0	2
GEL4101	Environmental Sciences	4-0-0	4
	Total		27

Year 1, Semester – 2

Semester 2nd			
Code	Subject	Hours (L-T-P)	Credit
AML5102	Engineering Mathematics- II	5-0-0	5
ECL5101	Basics of Electronics Engineering	4-1-0	5
ECP1101	Basics of Electronics Engineering Lab	0-0-2	1
CSL4103	Introduction to Programming Logic	4-0-0	4
MEL4102	Engineering Graphics	4-0-0	4
MEP1102	Engineering Graphics Lab	0-0-2	1
MEW2101	Manufacturing Practice Lab	0-0-4	2
CSP1103	Introduction to Programming Logic Lab	0-0-2	1
ASP3101	Integrated Project I	3-0-0	3
	Total		26

Technology Innovation (AST3101) (Credit 3)

ECE
Credits : 53 + 3 = 56

Year 2, Semester – 3

Course Code	Courses	L-T-P	Credits
ECL4214	Analog Electronics	3-1-0	4
ECL4204	Signals & Systems	3-1-0	4
ECL4208	Control Systems	3-1-0	4
ECL4207	Digital Electronics & Logic Design	3-1-0	4
ECP1214	Analog Electronics Lab	0-0-2	1
ECP1207	Digital Electronics & Logic Design Lab	0-0-2	1
ECP2203	Measurement & Virtual Instrumentation Lab	0-0-4	2
CSL4102	Problem Solving Techniques using C	2-0-8	6
CSP2102	Problem Solving Techniques using C Lab		
	Total		26

Year 2, Semester – 4

Course Code	Courses	L-T-P	Credits
ECL4205	Network Analysis & Synthesis	3-1-0	4
ECL4317	Digital Signal Processing	3-1-0	4
ECL4314	Linear Integrated Circuits	3-1-0	4
ECL4215	Analog Communication System	3-1-0	4
ECP1305	Digital Signal Processing Lab	0-0-2	1
ECP1314	Linear Integrated Circuits Lab	0-0-2	1
CSL3201	Object Oriented Programming	2-0-8	6
CSP2201	Object Oriented Programming lab		
ECP3201	Integrated Project II	0-0-4	2
	Total		26

Year 3, Semester – 5

Course Code	Courses	L-T-P	Credits
ECL4315	Microprocessor & Microcontroller	3-1-0	4
ECL4322	Electromagnetic Waves & Antennas	3-1-0	4
ECL4321	Digital Communication Systems	3-1-0	4
ECL4303	Microelectronic Circuits	3-1-0	4
ECP1206	Analog & Digital Communication Lab	0-0-2	1
ECP2303	Microelectronic Circuits Lab	0-0-4	2
ECP1315	Microprocessor & Microcontroller Lab	0-0-2	1
CSL5210	Data Structures	4-1-0	5
CSP2210	Data Structures Lab	0-0-4	2
GEW	GEW Elective	2-0-0	2
	Total		29

GEW Elective

(GEW1615)	Introduction to Power Electronics
(GEW1810)	Introduction to Microcomputer Technology

Year 3, Semester – 6

Course Code	Courses	L-T-P	Credits
	Department Elective-I	3-1-0	4
	Department Elective-II	3-0-0	3
ECL4311	VLSI Design	3-1-4	6
ECP2313	VLSI Design Lab		
GTI4301	Numerical Ability & Logical Reasoning	3-1-0	4
	Open Elective I/Lab	3-1-2	5
CLP2305	Industry Interface	0-0-4	2
HUL2401	Cyber Security	2-0-0	2
ECP3204	Integrated Project IV	0-0-4	2
	Total		28

Open Elective Courses

Course Code	Courses	L-T-P	Credits
CSL4203	Computer Networks	3-1-2	5
CSA4120	Introduction to CCNA Routing and Switching	3-1-2	5
CSA3211	Data Analytics	3-1-2	5
CSL5349	Essential Programming Concepts	3-1-2	5
CSL4318	Advanced Programming Concepts	3-1-2	5
CSL4320	Advanced Object-Oriented Programming	3-1-2	5

Department Elective I

Course Code	Courses	L-T-P	Credits
ECL4316	Wireless & Mobile communication	3-1-0	4
CSL4207	Operating System	3-1-0	4
ECL4409	Audio and Speech Processing	3-1-0	4
CLL3301	Life Skills I	3-1-0	4

Department Elective II

Course Code	Courses	L-T-P	Credits
ECL4414	Analog Layout Design	3-0-0	3
ECL4413	Digital Design using FPGA	3-0-0	3
ECL3312	IOT& Embedded Systems	3-0-0	3
ECL3313	VLSI design and Verification	3-0-0	3
HUL3303	Project Management	3-0-0	3
HUL3302	Digital Marketing and Entrepreneurship	3-0-0	3

Integrated Project

Course Code	Course Name	L-T-P	Credits
ECP3204	Integrated Project	0-0-4	2
ECP3204-1	Integrated Project in Analog Layout Design	0-0-4	2
ECP3204-2	Integrated Project in Digital Design using FPGA	0-0-4	2
ECP3204-5	Integrated Project in IOT & Embedded	0-0-4	2
ECP3204-6	Integrated Project in VLSI Design and Verification	0-0-4	2
ECP3204-3	Integrated Project in Networking	0-0-4	2
ECP3204-4	Integrated Project in Advance Wireless Communication	0-0-4	2

Year 4, Semester – 7

Course Code	Course Name	L-T-P	Credits
	Department Elective III	3-0-0	3
	Department Elective IV	3-0-0	3
	Department Elective V	3-0-0	3
	Department Elective V Lab	0-0-2	1
ECL4401	Embedded System Design	3-0-0	3
ECP2401	Embedded System Design Lab	0-0-4	2
ECP6401	Major Project	0-0-6	3
HUL3301	Human Rights & Values	3-0-0	3
	Total		21

Department Elective III

Course Code	Course Name	L-T-P	Credits
CSL3308	Artificial Intelligence and Expert System	3-0-0	3
ECL4404	Digital Image Processing	3-0-2	4
ECP1404	Digital Image Processing Lab		
ECL4407	Optical Communication	3-0-0	3

Department Elective IV

Course Code	Course Name	L-T-P	Credits
ECL4319	Information Theory and Coding	3-0-0	3
ECL4406	Mechatronics	2-0-2	3
ECP1406	Mechatronics lab		
ECL4411	Wireless Sensor Networks	3-0-0	3

Department Elective V

Course Code	Course Name	L-T-P	Credits
ECL4403	Microwave and Satellite Communication	3-0-2	4
ECP1403	Microwave and Satellite Communication Lab		
ECL4412	Advance Wireless Communication	3-0-2	4
ECP1412	Advance Wireless Communication Lab		

For 1-year Internship

Course Code	Title of the Course	Credits
ECT9411	CO-OP Project At Industry: Module- I	23

Year 4, Semester – 8

For 1-year Internship

Course Code	Title of the Course	Credits
ECT9412	CO-OP Project At Industry: Module- II	23

For 6-months Training

Course Code	Title of the Course	Credits
ECT9401	Industry Oriented Hands On Experience	25

* The student have a choice to opt for department elective III (3 credits) and department elective IV (3 credits) or to choose entrepreneurial skill development /startup activity (6 credits).

Course Code	Course Name	L-T-P, Credits
ER101	CEED Acceleration Program (CAP) Cohort-II-Module I	(0-0-4), 3 credits
ER102	CEED Acceleration Program (CAP) Cohort-II-Module II	(0-0-4), 3 credits
GC	Generic Course Offered by the University	Max credits to be earned 6***

*** GC This is applicable to the students who opt for generic courses.

6. Assessment and Evaluation

The evaluation will be continuous and the weightage of various components are as given in Table 3 (For Theory courses) and in Table 4 (for Practical Courses), Table 5 (for Integrated Projects) and Table 6 (for Programming courses).

Table 3: Evaluation components for Theory Courses

For Theory Courses	
Quizzes /Assignments/ Presentation/Class Test/ Open Book Test/ Case Study	10
Sessional Tests (STs)	30
End Term Examination	60
Total	**100**

There are three Sessional Tests (STs) for all theory papers, the average of best two are considered. The policy on the evaluation component – 'Quizzes / Tutorials / Assignments' is decided by the course coordinator and Dean and is announced separately for each course. The End Term examination for practical courses includes conduct of experiment and an oral examination (viva voce).

Table 4: Evaluation Components for Practical Courses

For Lab Courses	
Lab Performance / File work	40
Internal Viva – Voce	20
End Term	40
Total	**100**

Table 5: Evaluation Components for Integrated Project

For Integrated Projects	
Performance / Presentation / Project report	40
Internal Viva – Voce	20
End Term – Project Display	40
Total	**100**

Table 6: Evaluation Components for Programming Courses

For Programming Courses	
Internal Assessment	50
End term	50
Total	**100**

The medium of examination is English.

Criteria to Pass Examination: Based on the marks obtained by the student in a particular course as described in tables above, the grade in that course is obtained, in accordance with the table 7:

7. Rules for Attendance

Students are expected to be regular in attending the classes. 75% attendance (of all held sessions – lectures, tutorials, lab) is compulsory in a course in order to be eligible for appearing in end term comprehensive examination. 10% concession in this mandatory requirement is possible only in extreme circumstances and at the sole discretion of the Vice Chancellor. 5% concession is possible only in case of extreme circumstances and at the sole discretion of the Head of the Department. Students are encouraged for participating in co-curricular activities conducted by prestigious institutions at national/International level. Such students would be eligible for grant of special Duty Leaves (limited by a cap decided by the Vice Chancellor) to make up for the attendance, in case any class work is missed during this period. This privilege extended to the students will not be termed as right and is limited to just the attendance benefit.

8. Grading System

The list of letter and non-letter grades, their applicability and connotation are given below:
a) Letter Grades

Table 7: Grading scheme

% Marks Range of Total	Grade	Qualitative Meaning	Grade Point
80 – 100	O	Outstanding	10
70 – 79	A+	Excellent	9
60 – 69	A	Very Good	8
55 – 59	B+	Good	7
50 – 54	B	Above Average	6
45 – 49	C	Average	5
40 – 44	P	Pass	4
0 – 39	F	Fail	0
	I	Incomplete	0

If a student obtains grade P or above, he is declared pass in that subject. The grade F is equivalent to being fail in that subject, in the later case, the student has to reappear in the end term examination of that subject, whenever its exam is conducted again with the regular examination, after payment of appropriate examination fee.

If the student is detained from appearing in the end term examination because of shortage of attendance in the regular semester or is absent in the end term exam, his grade in that subject is 'I', till he/she appears again in the end term examination and obtains a new grade.

b) Non-letter Grades: Audit Courses will be graded as Excellent, Good, Fair or Poor.
The grade I (Incomplete) may be awarded in the following conditions:

(i)   Where a case of unfair means is pending, a 'Grade I' is awarded till the case is finalized.
(ii) Where a case of indiscipline is pending, a 'Grade I' is awarded till the case is finalized.
(iii) In cases of unfair means and indiscipline where the results for a particular examination are declared can be declared as null and void.
(iv) In cases, where the student does not complete his course work because of some reason viz, shortage of attendance / is absent in the end term examination.

In case the grades are not received by the University as per the time schedule the, the Dean of department may make a specific authorization for the Course coordinator to report GA (Grade Awaited). The dean of department will also simultaneously advise Dean Examination about the estimated time by which the grades will be received. Whenever the report GA appears in the grade

sheet, permission for further registration of such a student will be decided by Dean of School.

The Cumulative Grade Point Average (CGPA) denotes the overall performance of a student in all courses in which he is awarded letter grades. It is the weighted average of the grade points of all the letter grades received by the student from the time of his entry into the University.

Calculation of CGPA:

The CGPA (calculated on a 10-point scale) would be used to describe the overall performance of a student (from the semester of admission till the point of reckoning) in all courses for which LETTER GRADES will be awarded. SGPA will indicate the performance of student for any particular semester. Formulas for calculation of SGPA and CGPA have been provided as below:

$$CGPA = \frac{\sum_{i=1}^{N}\left(GPA_i * \sum_{j=1}^{n} C_{ij}\right)}{\sum_{i=1}^{N}\left(\sum_{j=1}^{n} C_{ij}\right)}$$

$$SGPA_i = \frac{\sum_{j=1}^{n} C_{ij} G_j}{\sum_{j=1}^{n} C_{ij}}$$

Where n = number of subjects in the semester; N = number of semesters; GPA_i = GPA for the ith semester; C_{ij} = number of credits for the jth course in ith semester; and G_j = Grade point corresponding to the grade obtained in the jth course.

Example to Understand the Calculation

Suppose a student is registered in four courses 'W', 'X', 'Y' and 'Z' in a particular semester as mentioned below in the Column - I of the table. Column - II in the table 6depicts the number of credits, which those courses carried. At the end of the semester, student was awarded with the grades as mentioned in Column – III in the table given below. Column – IV indicates the corresponding grade weight. Column – V and Column – VI indicate essentially the Credit value and Grade Points for every course completed by a student in that particular semester.

Table 8: Number of credits and courses

Courses in which student registered (Col. I)	Credits (Col. II)	Letter Grade (Col. III)	Grade Value (Col. IV)	Credit Value (Col. V)	Grade Points (Col. VI)
Course W	3	B	6	3 x 6	18
Course X	3	A	8	3 x 8	24
Course Y	3	O	10	3 x 10	30
Course Z	2	O	10	2 x 10	20
Total	11			Total	92

Thus, the total GPA of the student would be =

$$SGPA = \frac{Total\ grade\ pts.}{Total\ no.\ of\ credits} = \frac{92}{11} = 8.36$$

Suppose the GPA of the student in two successive terms is 7.0 and 8.0 with respective course credits being 12 and 11, then the

$$CGPA = \frac{7x12 + 8x11}{12 + 11} = \frac{84 + 88}{23} = 7.48$$

9. Promotion and Registration

Any bonafide student, who appears for the examination conducted by the University, shall be promoted to the next higher semester and shall carry forward all course(s) / subject(s) in which he/she is declared fail. The student shall have to pass all papers within stipulated maximum duration to qualify for the award of degree.

All students are eligible to register for next semester irrespective of number of backlogs
unless if:
(i)   He/She has dues outstanding to the University, hostel, or any   recognized authority or body of the University.
(ii)   His/Her grade sheet in his immediately preceding term is withheld.
(iii)   He/She has been specifically debarred or asked to stay away from that term.
Late registration may be granted in case a student fails to register on the stipulated date. Student failing to register on the specified day of registration will be allowed to register only after permission from Dean of the Department and after paying the stipulated late fee. Any student who has not registered will not be allowed to attend classes. The registration of the student may be cancelled, if at the later stage, it is found that the student is not eligible for registration due to following reasons:
(a) If the registration of a student in a course is not found to be in accordance with the regulations, his/her registration in that course will be cancelled and the grade obtained, if any, will be rejected.
(b) The registration of a student in a course or complete set of courses in   a term can be cancelled by the concerned authority when he is found guilty in cases of unfair means, breach of discipline, etc. or when he/she persistently and deliberately does not pay his dues.
(c) Absence for a period of four or more weeks at a stretch during a term shall result in automatic cancellation of the registration of a student from all the courses in that term.

10. Migration/Credit Transfer Policy
The following procedures will be followed for credit transfer for student under migration, studied in other Universities in India and Abroad:

"The credits earned by the student from the other universities in India or abroad shall be transferred as such. The Degree shall only be awarded to candidate subject to the condition that student earned the minimum no. of credit defined by Academic Regulation/APG of the Programme run by the Chitkara University."

In case a student undergoes international exchange programme or internship for 1 semester/ 1 year, then the courses, credits and grades earned by the student in abroad during that period should be reflected on the grade card issued by the Chitkara University. The courses will be marked as (*) on the grade card/transcript. The description of the (*) will be "credits and grades as adopted university/institute name . . . . . during international exchange programme.

11. Eligibility to Award the Degree

A student is deemed to have fulfilled the requirement of graduation for a degree (single or dual degree) or a higher level degree when he has:
  (i)   Cleared all Courses prescribed for the program
  (ii)   Earned the minimum credits required for the program as described in the "APG"
  (iii) Obtained the minimum CGPA 4.5 for the award of degree in the UG programs
  (iv)   Satisfied all requirements of these regulations.
         The minimum credits to be earned are given in table 9

Table 9: Minimum credits to be earned for award of degree in BE

Course / Year	BE in Electronics and Communication Engineering
Year I	56
Year II	53
Year III	58
Year IV	47
Total	**213**

It is mandatory for the student to earn minimum 213 credits by clearing mandatory core and elective courses. The student can choose electives of his interest from the list of electives attached in the scheme. The maximum credits that can be earned by student are 219 if he avails opportunity offered by university in the form of Generic Courses (GC) that falls in category of extracurricular activities/NCC/NSS/vocational courses/sports etc.

A student is deemed to have become eligible for the degree if, in addition to satisfying the above requirements, he has:
  (i)   Satisfied all rules of evaluation
  (ii)   No case of indiscipline or unfair means is pending against him.
However, in case of a student having outstanding dues against him to be paid to the University, Hostel or any other recognized organ of the University, his degree will be withheld until the said dues are cleared under extreme circumstances where gross violation of graduation is detected at a later stage, the Academic Council may recommend the Governing Body to recall the degree already awarded.

12. Programme Overview
This undergraduate program in Electronics and Communication Engineering prepares the students for the ever-expanding field of Electronics and Communication Engineering. The curriculum is directed towards the major applications such as wireless communications, embedded systems and IoT, Robotics, and VLSI. We believe that, many creative opportunities exist at the boundaries of CSE and ECE, so accordingly we planned the cross-training schedule for the students across disciplinary boundaries. Initially in the curriculum of ECE there are many courses in common

with the CSE program. Thereafter, the foundation program for the 3 & 4 year is structured into different verticals to allow customization by individual students based on their own personal perspectives

AML5101	Engineering Mathematics- I	(4-1-0), 5 Credits

Course Learning Outcomes (CLO):

CLO1:   Define vector space, subspace, basis, dimension and spanning set of a vector space.

CLO2:   Compute eigen values, eigenvectors, inverse and rank of matrices.

CLO3:   Explain geometry of a complex plane and state properties of analytic functions.

CLO4:   Skilled to calculate the Taylor and Laurent series of a function of complex variable about a given point.

Differentiation: Geometrical interpretation of derivative, Indefinite and definite (integration by substitution, by parts, by partial fraction), Reduction formulae sine and cosine (with limit $0 – \pi/2$). Matrices: Review of matrices and determinants, Elementary operations, rank, Inverse of matrix(using rank), Normal form(using rank), Cayley Hamilton theorem(without proof), Solution of a system of linear equations by using rank, Characteristics equations, Eigen values and vectors. Partial Differentiation & its Applications: Introduction to Partial Derivatives: Function of several variables, Limit and continuity Partial Differentiation, Euler's Theorem, Total derivatives, Partial Derivative of Composite Functions, Implicit Functions, Jacobians, Taylor's Series Expansion, Maclaurin's Series (one and two variables). Application: Maxima and Minima of functions of two and three variables, Lagrange's method of Undetermined Multipliers. Multiple Integration and its Applications: Introduction to Double Integration using Cartesian & polar coordinate, Change of order in double integration, Introduction to Triple Integration ,Change of variables in Polar, Cylindrical and Spherical Coordinates , Applications of multiple integral to find Area enclosed by Plane curves ,Applications of multiple integral to find Volume , Improper integrals of first and second kind , Special Functions: Beta and Gamma functions. Introduction to Scalars and Vector: Vector Function (Derivative and integral), tangent to the curve, Unit tangent, Scalar and Vector Field, Gradient and its Physical Interpretations, Directional Derivatives. Divergence and its Physical Interpretations, Curl and its Physical Interpretations , Properties of Gradient, Divergence and Curl , Line Integrals, Surface & Volume Integral , Green's Theorem in the Plane (without proof) and applications , Stokes's Theorem (without proof) and applications , Gauss Divergence Theorem (without proof) and applications.

Recommended Books
1. "The Engineering Mathematics", 2nd Edition, Chitkara University Publication, Vol. I.
2. Srimanta Pal &Subodh C. Bhunia , "Engineering Mathematics", Edition 2015, Oxford University.
3. B V Ramana, "Higher Engineering Mathematics", , Edition 2009, McGraw Hill.
4. Dr. H. C. Taneja, "Advanced Engineering Mathematics" (Vol. I & Vol. II), I. K. International.

PYL5101	Engineering Physics	(4-1-0) , 5 Credits

Course Learning Outcomes (CLO):

CLO1: Apply the knowledge of physics through fundamental concepts together with analytical tools in everyday life.

CLO2: Analyze a physical problem, and suggest appropriate possible solution based on the physics concepts.

CLO3: Explore physical systems by setting up experiments, collecting and analyzing data, identifying sources of uncertainty, and interpreting their results in terms of the fundamental principles and concepts of physics

CLO4: Skilled to evaluate and analyze scientific measurement and error analysis.

CLO5: Apply the fundamental concepts of physics to related engineering problems.

Laser: Introduction, characteristics laser action, stimulated absorption, spontaneous emission, stimulated emission, Population inversion and pumping, Einstein's coefficient (no derivation), various level lasers, two level, three level, four level, Ruby laser, Helium-Neon laser, Carbon dioxide laser, Semiconductor laser, concepts of Holography. Fiber Optics: Basic principle of optical fibre, step index and graded index fibers, parameters of optical fibers, acceptance angle, acceptance cone, numerical aperture, Normalized Frequency, No. of modes, Attenuation in optical fibers, intermodal and intramodal dispersion (no derivation), optical fibers in communication. Electrodynamics: Vector and scalar fields, Gradient, divergence, curl and their physical interpretation, Gauss's theorem and Stoke's theorem (Statement only), Equation of continuity, Maxwell's equations in free space, Propagation of electromagnetic waves in free space. Special theory of relativity: Frames of reference, postulates of special theory of relativity, Galilean transformation equations. Lorentz's transformation equations, inverse Lorentz's transformation equations (no derivation), length contraction, time dilation Relativistic velocity addition formula, Variation of mass with velocities (concept only), Mass energy relation. Quantum Mechanics: Introduction to Quantum Mechanics, Group velocity and phase velocity (No relation), De-broglie waves, Uncertainty principle (statement only), Wave function and its significance, Normalised wave function, Schrodinger wave equations (Time dependent and Time Independent), Particle in a one dimensional box. Electronic Properties of Solids: Free electron theory (quantum theory) density of states, Fermi energy, Fermi Dirac function, Band theory of solids (introduction): metals, semiconductors, insulator, doping Intrinsic and extrinsic semiconductors, carrier concentration of semiconductors (no derivation), Hall effect (Quantitative idea). Magnetic Materials: Magnetic materials, terminology and classification, Magnetic moments of an atom; orbital, spin and total, Lande's g-factor, Ferromagnetism and related phenomena, the domain structure, the hysteresis loop,Types of magnetic materials: soft magnetic materials, hard magnetic materials.

Recommended Books

1. 'The Engineering Physics' second edition by Chitkara University Publication.
2. H. K. Malik and A. K. Singh, Engineering Physics by , Tata McGraw Hill Publishing company.
3. Arthur Beiser, Concepts of Modern Physics by, Tata McGraw Hill Publishing company.
4. D.K. Bhattacharya and P. Tandon , Engineering Physics by, Oxford University Press.

EEL4103	Basics of Electrical Engineering	(4-0-0) , 4 Credits

Course Learning Outcomes (CLO):

CLO1: Students would know the basics of DC circuits, Series and parallel connections, Kirchhoff's current and voltage laws, mesh and nodal analysis. They would be able to compute various electrical engineering concepts based on real time applications.

CLO2: Students would possess an ability to analyze and characterize the RL, RC & RLC circuits and have basic understanding of their implementation and also able to compute parameters related to these circuits like impedance and power. They would also learn phenomenon like resonance

CLO3: Students would be skilled to apply and clarify fundamental principles of magnetic effects, magnetism and their functionality for electrical equipment's.

CLO4: Students would possess the skill to conduct experiments, understand the principle, construction and working of Transformers, DC motors and Induction motors.

DC Circuits: Introduction to DC Circuits and related terminology, Series and Parallel combination of resistances, Kirchhoff's Laws: KVL and KCL, Mesh or loop Analysis and Nodal Analysis. Magnetic Circuits**:** Definitions of Magnetic quantities, Magnetic Circuit, Comparison between Electric and Magnetic Circuits Magnetic Effect of Electric Current, Current carrying conductor in magnetic field, Law of EMI, Induced EMF: self-inductance, mutual inductance, Coupling Coefficient between two magnetically coupled circuits. Representation of alternating quantities in rectangular and polar forms, RL, RC, RLC series circuits and its Power calculations. Resonance in series AC circuits. Three Phase AC circuits: Star Connections, Delta connections. Measurements of power in 3 phase circuits**.** Electrical Machines Transformer: Principle, Construction, Working. DC Motor: Principle, Construction, Working. Three Phase Induction Motors: Principle, Construction, Working. Electrical measuring instruments and transducers**:** Electrical Measuring instruments: Classification of instruments, Basic principles of indicating instruments, Electrical Transducers Introduction, Types of transducer: LVDT, RTD. Thermocouple, Thermistor, Piezoelectric transducer, Photoelectric transducer.

Recommended Books

1. D.C, Kulshreshtha, 'Basic Electrical Engineering' TMH, 2014.
2. T.K Nagsarkar & M.S Sukhija, 'Basic Electrical Engineering', Oxford2012.
3. R. Muthusubramanian, S Salivahanan, 'Basic Electrical and Electronics Engineering', McGraw Hill,2009
4. B.R. Patil, 'Basic Electrical and Electronics Engineering' Oxford Higher Education Revised Second Edition, 2013.

EEP1103	Basics of Electrical Engineering Lab	(0-0-2) , 1 Credits

Course Learning Outcomes (CLO):

CLO1: After completing the course, students would know the basic components of electrical elements, equipment's and their functionality with applications. With the knowledge of the basic components, students would be able to make basic electrical projects

CLO2: They would possess an ability to analyze and characterize the electrical equipment's and instrument's basics for their implementation.

CLO3: They would be skilled to measure power and power factor of ac circuits and understand three-phase star and delta connections with and without applying loads to calculate 3-phase power.

CLO4: Possess skill to perceive the concept of Fuse/MCB characteristics for different fault currents. Students will be familiarized with appearance and functioning of the MCB and fuse used in their homes.

To study the use of multi-meter and testing of various components. Verification of Kirchoff's current law in DC circuits, Verification of Kirchoff's voltage law in DC circuits.Analysis of AC circuits: To find voltage, current relationship and power factor in single phase series R-L-C circuits.. Measurement of self-inductance, mutual inductance and coupling coefficient of windings. To perform open- circuit and short circuit test on a transformer and determine Efficiency, Voltage ratio. To study speed control of the D.C. shunt motor by Armature control method and Field control method, to connect, start and reverse the
direction of rotation of a 3- phase induction motor,

Recommended Books:
1. D.C, Kulshreshtha, 'Basic Electrical Engineering' TMH, 2014.
2. T.K Nagsarkar & M.S Sukhija, 'Basic Electrical Engineering', Oxford2012.
3. R. Muthusubramanian, S Salivahanan, 'Basic Electrical and Electronics Engineering', McGraw Hill,2009
4. B.R. Patil, 'Basic Electrical and Electronics Engineering' Oxford Higher Education Revised Second Edition, 2013.

CHL4101	Engineering Chemistry	(4-0-0), 4 Credits

Course Learning Outcomes (CLO):

CLO1: Develop innovative methods to produce soft water for industrial use and potable water at cheaper cost.

CLO2: Substitute metals with conducting polymers and also produce cheaper biodegradable polymers to reduce environmental pollution.

CLO3: Design economically and new methods of synthesis nano materials.

CLO4: Skilled to apply their knowledge for protection of different metals from corrosion.

CLO5: Have the knowledge of converting solar energy into most needy electrical energy efficiently and economically to reduce the environmental pollution.

Water Technology : Introduction, Sources of water, Impurities in water, Hard and soft water, Units of hardness (Numericals included), Specification for boiler feed water, Boiler problems: Scale and sludge formation, Caustic embrittlement, Priming and Foaming, Boiler corrosion due to oxygen and carbon dioxide, External treatment: Lime –soda process (Numericals included), Zeolite process, Ion exchange process, Internal treatment (different types of conditioning), Specification of water for domestic use, Treatment for domestic use(break point chlorination); Treatment of brackish water(reverse osmosis, electrodialysis using ion selective membrane). Water quality parameters: Acidity, alkalinity, BOD, COD, Dissolved oxygen, Conductivity, pH.Polymer Sciences: Elementary ideas about polymers, Classification of polymers, Types of polymerization, Chemical reaction for the synthesis of polymers: Teflon, Polyester (Dacron), Nylon6,6; PMMA ,Novolac, Phenol formaldehyde resin(Bakelite), PC(Polycarbonate). Synthesis and Application of rubbers – Buna-N, Buna-S and Poly isoprene. Introduction and Application of Electro active , Conducting and Biopolymers. Battery Technology: Introduction to electrolytic and electrochemical cell, EMF of cell, Relationship between emf and thermodynamic properties (ΔH, ΔS, ΔG). Numerical based on EMF of cells and thermodynamic parameters. Electroless plating, Preparation of PCB (Printed circuit board), Dry cell, lead storage batteries. Applications and function of batteries used in Laptops: Lithium ion battery and Nano batteries. Batteries used in rockets & submarine: Fuel cell (hydrogen-oxygen alkaline fuel cell, molten carbonate fuel cell, Phosphoric acid fuel cell). Batteries used in electronic devices :Solar cell. Liquid Crystal Technology: Introduction, Characteristics of liquid crystals: Director and Effect of temperature on order parameter. Classification of liquid crystal: Smectic liquid crystal (Smectic A,B,C), Nematic liquid crystal, Cholesteric liquid crystal, Molecular arrangement in various types of liquid crystals. Principle of liquid crystal Display (LCD) , Application of polymer dispersed liquid crystals (PDLCs). Phase Equilibrium: Introduction, Gibbs phase rule, Application of phase rule in one component system, Water system, Carbon dioxide system, Sulfur system Condensed phase rule, Two component system, Eutectic mixture, Lead silver system. Two component system: Potassium iodide -water system, Ferric chloride water system and Iron-Carbon system. Corrosion and its control: Introduction, Causes of corrosion, effects of corrosion, Types and mechanism of corrosion, Direct chemical (dry) corrosion. Electrochemical (wet) corrosion, Pilling Bedworth ratio. Comparison of Chemical and electrochemical corrosion. Types of electrochemical corrosion, Other forms of corrosion (Underground or soil corrosion, Microbial corrosion, Erosion corrosion, Intergranular ,Crevice, atmospheric corrosion), Passivity of corrosion, Factors influencing corrosion: Nature of the metal, Nature of corroding environment, Prevention of

corrosion, Use of protective measurements: Cathodic protection. Green Chemistry and Green Engineering: Weapons for mass destruction and chemistry for peaceful work., Principles of green chemistry and Green Engineering, Green Chemistry in India (examples of Microwave assisted synthesis),Traditional synthesis of adipic acid and urethane.

Recommended Books
1. 'The Engineering Chemistry' - Chitkara University Publication.
2. O. G.Palanna, 'Engineering Chemistry' by 2009, Tata McGraw- Hill publishing house.
3. B. Sivasankar, 'Engineering Chemistry' by 2008, Tata McGraw Hill Publishing House.
4. Gilbert W. Castellan 'Physical Chemistry' 3rd edition, Narosa Publishing House.

| CHP1101 | Engineering Chemistry Lab | (0-0-2), 1 Credits |

Course Learning Outcomes (CLO):

CLO1: Develop innovative methods to produce soft water for industrial use and potable water at cheaper cost.

CLO2: Substitute metals with conducting polymers and also produce cheaper biodegradable polymers to reduce environmental pollution.

CLO3: Design economically and new methods of synthesis nano materials.

CLO4: Skilled to apply their knowledge for protection of different metals from corrosion.

Water Technology: Determination of total hardness, permanent hardness and temporary hardness by Complexometric method.Determination of residual chlorine in water.Determination of dissolved oxygen in the given water sample.To determine Biological Oxygen Demand in the given water sample.Green chemistry and Green Engineering: To draw the chemical equations of Traditional and Green synthesis of Adipic acid using CHEM SKETCH. LC- Technology: To study the electro-optic behavior of liquid crystal cell.Polymers and synthesis, Preparation of Bakelite using phenol-formaldehyde.Preparation of urea –formaldehyde resin.Preparation of copper ammonia ComplexInstrumental Analysis, To determine the acid strength by using pH meter.To determine the acid strength by using conductometer, To determine the surface tension by drop number method using Stalgamometer, To determine the % moisture, volatile, ash and carbon content of coal sample by proximate method.

Recommended Books:
1. J MENDHAM,RC DENNEY, Vogel's Textbook of quantitative chemical analysis' by, Sixthedition, Pearson Publications .
2. V. Alexeyev, 'Quantitative Analysis' by, Third edition, MIR publications.
3. F G Mann,B C Saunders, 'Practical Organic Chemistry' ,Fourth edition ,Pearson Publications.
4. Gilbert W. Castellan 'Physical Chemistry' 3rd edition, Narosa Publishing House.

| PYP1101 | Engineering Physics Lab | (0-0-2) , 1 Credits |

Course Learning Outcomes (CLO):

CLO1: Students would be able to correlate practical knowledge of physics with the theoretical concepts.

CLO2: Students would achieve perfectness in experimental skills related to physics fundamentals.

CLO3: The study of practical applications will bring more confidence.

CLO4: Ability among the students to design, perform, document and analyze advanced experiments in physics.

Electrical Properties of Materials: To determine the ionization potential of mercury using a gas filled diode.Todetermine the e/m ratio of electron using Thomson method. Find out the polarizability of a dielectric substance by using dielectric constant kit. To study the Hall effect in a semiconductor. Quantum Mechanics: To determine Planck's constant by using light emitting diodes. Magnetic Materials: To find out the Susceptibility of $FeCl_3$ by Quinke's Method. Study the variation of magnetic field with distance along axis of a circular coil carrying current. To draw the B-H curve of a given magnetic material. Lasers and Optics: To determine the wavelength of light using Michelson's Interferometer. To determine the resolving power of a plane transmission grating. To measure the specific rotation of cane sugar solution using Laurent's half shade polarimeter. Study of Diffraction using Laser beam and thus to determine the wavelength/grating element..

Recommended Books:
1. The Engineering Physics' second edition by Chitkara University Publication.
2. H. K. Malik and A. K. Singh, Engineering Physics by , Tata McGraw Hill Publishing company.
3. Arthur Beiser, Concepts of Modern Physics by, Tata McGraw Hill Publishing company.,
4. D.K. Bhattacharya and P. Tandon , Engineering Physics by, Oxford University Press.

| HUL2101 | Disaster Management | (2-0-0) , 2 Credits |

Course Learning Outcomes (CLO):

CLO1: Increase the knowledge and understanding of the disaster phenomenon, its different contextual aspects, impacts and public health consequences.

CLO2: Increase the knowledge and understanding of the International Strategy for Disaster Reduction (UN-ISDR) and to increase skills and abilities for implementing the Disaster Risk Reduction (DRR) Strategy.

CLO3: Ensure skills and abilities to analyse potential effects of disasters and of the strategies and methods to deliver public health response to avert these effects.

Disasters: Classification, Causes, Impacts: Introduction to Disasters: Concepts, and definitions (Disaster, Hazard, Vulnerability,Resilience, Risks) . Impacts (including social, economic, political, environmental, health, psychosocial, etc.Differential impacts- in terms of caste, class, gender, age, location, disability). Classification of hazards/disasters and causes. Principles of disaster management: Approaches to Disaster Risk reduction: Disaster cycle - its analysis, Phases, Culture of safety, prevention, mitigation and preparedness, Community based DRR, Components of Disaster Relief: Water, Food, Sanitation, Shelter, and Health, Structural and non-structural measures. Hazard Profile (India) , Disaster Risk Management in India: Hazard and Vulnerability profile of India. Institutional arrangements (Mitigation, Response and Preparedness ,DM Act and Policy, Other related policies, plans, programmes and legislation) ,Role of Panchayati Raj Institutions/Urban Local Bodies (PRIs/ULBs), states, Centre, and other stake-holders. Disaster and Development: Inter-relationship between Disasters and Development: Factors affecting Vulnerabilities, impact of Development projects such as dams, embankments, changes in Land-use etc. urban disasters, Waste Management. Global trends in disasters & Adaptation: Global Trends, Complex emergencies, Pandemics Climate change and Adaptation, Relevance of indigenous knowledge, appropriate technology and local resources.

Recommended Books
1. Alexander, D. Natural Disasters, ULC press Ltd, London, 1993.
2. Carter, W. N. Disaster Management: A Disaster Management Handbook, Asian Development Bank, Bangkok, 1991.
3. Alexander David, Introduction in 'Confronting Catastrophe', Oxford University Press, 2000.
4. Chakrabarty, U. K. Industrial Disaster Management and Emergency Response, Asian Books Pvt. Ltd., New Delhi 2007.

GEL4101   **Environmental Sciences**   **(4-0-0), 4 Credits**

Course Learning Outcomes (CLO):

CLO1: Understand the transnational character of environmental problems and ways of addressing them, including interactions across local to global scales.

CLO2: Skilled to apply systems concepts and methodologies to analyze and understand interactions between social and environmental processes.

CLO3: Reflect critically about their roles and identities as citizens, consumers and environmental actors in a complex, interconnected world.

CLO4: Skilled to demonstrate proficiency in quantitative methods, qualitative analysis, critical thinking, and written and oral communication needed to conduct high-level work as interdisciplinary scholars and/or practitioners.

The multidisciplinary nature of environmental studiesDefinition, Scope and Importance; Need for Public Awareness. Natural Resources:Renewable and non-renewable resources Associated problems: Forest resources, use and over, exploitation, deforestation, timber extraction, mining, dams and other effects on forest and tribal people. Water resources: Use and over utilization of surface and ground water, floods, drought, conflicts over water, dams , benefits and problem. Mineral resources: Use and exploitation, environmental effects of extracting and using mineral resources. Food resources: World food problems, changes caused by agriculture and overgrazing, effects of modern agriculture, fertilizer-pesticide problems, water logging, salinity, case studies. Energy resources: Growing energy needs, renewable and non-renewable energy sources, Uses of various alternate energy sources. Land resources: Land as a resource, land degradation, man induced landslides, soil erosion and desertification. Role of an individual in conservation of natural resources Equitable use of resources for sustainable lifestyles. Ecosystems **-** Concept of an ecosystem, Structure and function of an ecosystem. Producers, consumers and decomposers.Food chains, food webs and Ecological pyramids.Energy flow in the ecosystem, Biogeochemical cycles.Ecological succession. Introduction, types, characteristic features, structure and function of the following ecosystem: a. Forest ecosystem, b. Grassland ecosystem, c. Desert ecosystem, d. Aquatic ecosystems (ponds, streams, lakes, rivers, oceans, estuaries. Biodiversity and its conservation: Introduction - Definition: genetic, species and ecosystem diversity. Value of biodiversity: consumptive use, productive use, social, ethical, aesthetic and option values. Bio-geographical classification of India.Biodiversity at global, National and local levels, India as a mega diversity nation, Hot-spots of biodiversity. Threats to biodiversity: habitat loss, poaching of wildlife, man wildlife conflicts, Endangered and endemic species of India. Conservation of biodiversity: in-situ and ex-situ conservation of biodiversity. Environmental Pollution: Definition, Cause, effects and control measures of : a. Air pollution, b. Water pollutionc. Noise pollution, d. Soil pollution e. Marine pollution, f. Thermal pollution, g. Nuclear hazards. Solid waste Management: Causes, effects and control measures of urban wastes and industrial wastes. E-waste introduction. Role of an individual in prevention of pollution. Disaster management: floods, earthquake, cyclone and landslides. Social Issues and the Environment: From Unsustainable to Sustainable development -Urban problems related to energy, Environmental ethics: Issues and possible solutions. Water conservation, rain water harvesting, and watershed management.Resettlement and rehabilitation of people; its problems and concerns. Climate change, global warming, acid rain. Ozone layer depletion, nuclear accidents and holocaust; Case Studies, Wasteland reclamation – Consumerism and waste products.Environment Protection

Act.Water (Prevention and control of Pollution) Act. Air (Prevention and Control of Pollution) Act ,Wildlife Protection Act, Forest Conservation Act. Issues involved in enforcement of environmental legislation, Public awareness. Human Population and the Environment: Population growth, variation among nations, Population explosion - Family Welfare Program. Environment and human health, HIV/AIDS,Human Rights, Value Education. Women and Child Welfare, Role of information Technology in Environment and human health.

Recommended Books
1. ErachBharucha,, Textbook of Environmental Studies for Undergraduate Courses' by First Edition, University Grants Commission, Universities Press (India) Private Limited.
2. Manish Randhawa, 'The Basics of Environmental Sciences' by First edition, ChitkaraUniversity publications.
3. Cunningham, W.P. and Saigo, B.W., 1999. Environmental science (p. 640). McGraw-Hill.
4. Bates, P.D., Lane, S.N. and Ferguson, R.I. eds., 2005. Computational fluid dynamics: applications in environmental hydraulics. John Wiley & Sons.

Year 1, Semester – 2

AML5102	Engineering Mathematics- II	(5-0-0), 5 Credits

Course Learning Outcomes (CLO):

CLO1: To analyze and correlate many real-life problems mathematically and thus find the appropriate solution for them using Fourier series and Transforms (Fourier and Laplace transform).

CLO2: Using ordinary differential equations student will be skilled to solve various practical problems in Science and Engineering.

CLO3: Possess an ability to recognize and find families of solutions for most real physical

CLO4: Processes such as heat transfer, elasticity, quantum mechanics, water flow and others, which are governed by partial differential equations subject to boundary conditions.

Pre-Requisite: Differentiation, Integration, Multiple Integral.FourierSeries: Introduction,FourierSeriesonArbitraryIntervals,Half-range cosine andsine series, Fourier Transform with properties, Parseval"s Identity. Ordinary Differential Equations: Differential equations of first order and first degree–exact, linear and Bernoulli, Clairauts, Application to orthogonal trajectories. Secondand higher order ordinary linear differential equations with constant coefficients–Complementary function -Particular integrals (standard types), Differential Operator Method, Variation of parameters,MethodofUndeterminedCoefficients. Cauchy-Euler differential equation. Simultaneous linear differential equations (two variables) with constant coefficients, Applicationto RLCcircuit. Partial Differential Equations: Formation of partial differential equations-Equations of first Order-Lagrange's linear equation-Charpit's method-Standard types of first order non-linear partial differential equations. Solution of second order linear partial differential equations in two variables with constant coefficients by finding complementary function and particular integral. Classification of PDE of second order-parabolic, elliptic and hyperbolic equations- Solution by separation of variables. Solutions of one-dimensional heat and wave equations and two-dimensional Laplace equation using Fourier series. Functions of Complex Variables: Limits, Continuity, Derivative of Complex Functions, Analytic Function, Cauchy Riemann Equation, Harmonic Functions, Conformal Mapping, Complex Integration, Cauchy's Theorem, Cauchy Integral formula, Taylors and Laurent's Expansion. Laplace Transform: Laplace Transform, Inverse transforms properties, Transforms of derivatives and integrals, Unit step function, Dirac's delta function, Applications to differential equations.

Recommended Books
1. The Engineering Mathematics", 1st Edition, Chitkara University Publication, Vol. II.
2. B V Ramana, "Higher Engineering Mathematics", Edition 2009, McGraw Hill.
3. Dr.H.C.Taneja "AdvancedEngineeringMathematics"(Vol. I &Vol.II), ,I.K.International.
4. Grewal, B.S. and Grewal, J.S., 1996. Higher engineering mathematics. 2002, Khanna Publishers, New Delhi.

ECL5101	Basics of Electronics Engineering	(4-1-0), 5 Credits

Course Learning Outcomes (CLO):

CLO1: Students would know the basics of electronics elements, their functionality and applications. They would be able to perceive the concept of logic gates and integrated circuits in electronics.

CLO2: Skilled to interpret the characteristics of various types of diodes and transistors to describe the operation of related circuits for evolving engineering solutions.

CLO3: Students would be able to apply fundamental principles of electronics together with analytic tools to evaluate and describe physical situations appropriate to address a scientific problem.

CLO4: Students would possess a skill to explore physical systems by setting up experiments, collecting and analysing data, identifying sources of uncertainty, and interpreting their results in terms of the fundamental principles and concepts of electronics.

CLO5: Skilled to apply fundamental principles of electronics together with analytic tools

Junction diode and its applications: Semi conductor theory, Theory of PN Junction diode, PN Diode Applications, Zenerdiode ,Varactor diode, Rectifiers: Half Wave,: Ripple Factor, Efficiency, Peak inverse voltage, Transformer Utilization Factor, Form Factor and peak factor, Full Wave and Bridge Rectifier : Ripple Factor, Efficiency, Peak inverse voltage, Transformer Utilization Factor, Form Factor and peak factor and Bridge Rectifiers, Light Emitting Diode, Seven Segment Display. Semiconductor Transistors: Bipolor Transistors : npn and pnp transistor, operational modes-active, saturation and cutoff mode, Introductory idea about CE, CB and CC configuration, VI characteristics of CE configuration, Transistor application as an amplifier and a switch, Junction Field Effect Transistor (JFET), SCR and UJT (Construction and Characteristics). Digital Electronics: Number Systems (Decimal, Binary, Octal and hexa decimal), Conversion from binary number system to decimal number system and vice versa, 1's Complement and 2's complement, Binary Addition, Subtraction and Multiplication. Boolean algebra - Laws of Boolean algebra, Logic Gates, Basic gates, Universal Gates and special purpose gates with their truth table, symbols, logical expression, Realization of logical expression using basic and Universal gates, Flip-Flops- Latch, Basic SR flip-flop, truth table. Integrated Circuits: Classification of ICs, Operational Amplifiers, its characteristics, IC Timer- IC 555 Timer Block Diagram, pin configuration, Basic modes of operation- bistable, monostable and astable , Waveform generation using astable configuration only.

Recommended Books

1. R. Muthusubramanian, S. Sahlivahanan 'Basic Electrical and Electronics Engineering', by McGraw Hill, First Edition, 2010.
2. N. N Bhargava, D. C Kulshreshtha, S. C Gupta; 'Basic Electronics and Linear Circuits', McGraw Hill Publications, Second Edition, 2013.

3. D. P. Kothari, I. J. Nagrath, 'Basic Electronics', McGraw Hill, Second Edition, 2014.
4. D. K. Bhattacharya, Rajnish Sharma, 'Solid State Electronic Devices', Oxford University Press, Second Edition, 2013.

ECP1101	Basics of Electronics Engineering Lab	(0-0-2) , 1 Credits

Course Learning Outcomes (CLO):

CLO1: After completing the course, students would know the basics of electronics elements, their functionality and applications and would be able to design basic electronics projects.

CLO2: They would be able to analyze and characterize the electronic circuits and have basic understanding for their implementation.

CLO3: They would possess a skill to perceive the concept of logic gates like XOR and X-NOR and integrated circuits in electronics.

CLO4 Skill of explaining the basics of electronics fundamentals

Familiarization with basic electronic components..Plot and analyze the forward and reverse characteristics of PN junction Si and Ge diodes and determine their knee and breakdown voltages.Analyze Zener diode as voltage regulator and observe the output voltage with variable input voltage and fixed load resistance for zener diodes with breakdown voltages of 6 V,8V and 12 V.Study and observe the output waveform of half-wave and full wave rectifiers on CRO and calculate the average and rms values of output voltage and current.Analyze the npn and pnp transistors in common emitter configuration and plot their input and output characteristics.Study the operation of npn and pnp transistors as a switch and an amplifier.Analyze the truth tables of various logic gates.Implement 2-input XOR gate and 2-input X-NOR gate using basic gates.Analyze S-R and D flip-flops using NAND/NOR gates.Study the operation of Inverting and non-Inverting amplifier using Op-amp IC 741 in closed-loop configuration.Study the operation of astable,monostable and bistablemultivibrators using 555 timer.

Recommended Books:

1. R. Muthusubramanian, S. Sahlivahanan 'Basic Electrical and Electronics Engineering', by McGraw Hill, First Edition, 2010.
2. N. N Bhargava, D. C Kulshreshtha, S. C Gupta; 'Basic Electronics and Linear Circuits', McGraw Hill Publications, Second Edition, 2013.
3. D. P. Kothari, I. J. Nagrath, 'Basic Electronics', McGraw Hill, Second Edition, 2014.
4. D. K. Bhattacharya, Rajnish Sharma, 'Solid State Electronic Devices', Oxford University Press, Second Edition, 2013.

CSL4103	Introduction to Programming Logic	(4-0-0), 4 Credits

Course Learning Outcomes (CLO):

CLO1: Choose the appropriate programming constructs to solve the problems.

CLO2: Demonstrate the advantages and disadvantages of specific techniques to be used.

CLO3: Skilled to differentiate between efficient and inefficient way of programming.

CLO4: Determine and demonstrate bugs in a program and recognize needed basic operations.

CLO5: Formulate new solutions for programming problems or improve existing code to program effectively

Understanding computer systems, simple program logic, program development cycle, Pseudo code statements, Flowcharts Symbols, Sentinel Values to end a program, Understanding programming and user environments, Evolution of programming models. Declaring and using variables and constants, assigning values to variables, advantages of modularization, Modularizing a program, Creating hierarchy charts, Features of good program design. Understanding unstructured spaghetti code, three basic structures, Input to a structure a program, Reasons for structuring a program, recognizing structure, Structuring and modularizing unstructured logic. Evaluating Boolean expressions to make comparisons, relational comparison operators, Logical operators-(AND, OR, NOT), Making selections with ranges, Precedence Rules. Advantages, Loop Control variables, Nested Loops, Common mistakes using loops, FOR Loop, Common Loop Applications.Understanding arrays and their memory occupancy, Manipulating arrays to replace nested decisions, using constants with arrays, Searching, Parallel Arrays, FOR Loop with arrays. Sorting (Bubble), Swapping Values, Multi-dimensional arrays(1-D, 2-D). Understanding computer files, Data hierarchy, Performing file operations on Sequential Files, Introduction to Random Access Files.

Recommended Books
1. V. Rajaraman, 'Fundamental of Computers', Prentice Hall India Pvt., Limited
2. Joyce Farrell, 'Programming Logic and Design Comprehensive', Sixth Edition, Cengage Learning.
3. Jeri R. Hanly 'Problem Solving and Program Design in C, 5/E', , Pearson Education India.
4. Reema Thareja, Programming in C, 2nd Edition, Oxford University Press.

MEL4102	Engineering Graphics	(4-0-0), 4 Credits

Course Learning Outcomes (CLO):

CLO1: Improve the technical writing, basic sketching and drawing.

CLO2: Use engineering scale effectively

CLO3: Skilled to use dimensioning effectively.

CLO4: Use development of surfaces.

CLO5: Communicate through Engineering Graphics.

Drawing Techniques: - Various types of lines, principles of dimensioning, size and location dimensions, symbols, conventions, scales (plane and diagonal) and lettering as per IS code of practice (SP-46) for general Engg. Drawing. Practice of drawing various types of lines and dimensioning exercises. Drawing exercises pertaining to Symbols, Conventions and exercises on lettering techniques free hand printing of letters and numerals in 5 mm sizes, vertical and inclined. Projection of Points and Lines: - Concept of horizontal and vertical planes. First and third angle projections; projection of points and lines, true lengths of lines and their horizontal and vertical traces. Projection of Planes: - projection of planes and their traces. Projection of Solids:- Projection of Right solids; solids of rotation and polyhedrons etc, Projection of solids with cases when (a) inclined to one ref plane and (b)inclined to both ref planes. Sectioning of solids: - Principles of sectioning, types of sectioning, and their practice on projection of solids, sectioning by auxiliary planes. Isometric projections and Orthographic projections: - Concept of isometric views; isometric scale and exercises on isometric views. Practice in orthographic projections. Development of Surfaces: - Development of surfaces of cylinders, cones, pyramids and prisms.

Recommended Books
1. P.S. Gill **"**Engineering Drawing",; Eleventh edition, S.K. Kataria& Sons.
2. R. K. Dhawan; "Engineering Drawing2014 Edition, S. Chand and Company.
3. Dhananjay A Jhole 'Engineering Drawing with introduction to Auto CAD';First Addition;Mc Graw Hill.
4. Bhatt ND; Tulsi sadan Anand. 'Engineering Drawing Charotar Book stall, 49th Addition.

MEP1102	Engineering Graphics Lab	(0-0-2) , 1 Credits

Course Learning Outcomes (CLO):

CLO1:   Improve the technical writing, basic sketching and drawing.

CLO2:   Skilled to use engineering scale effectively

Introduction of the CAD (computer aided drafting) software and its utilities in the engineering software.Study of the various toolbar options and exercises to familiarize all the drawing tools. Study the basic initial setting and viewing of the drafting software interfaces. Use of basic entities in 2D. Uses of various modify commands of the drafting software. Dimensioning in 2D and 3D entries.Study and implementation of coordinate systems.

Recommended Books
1. George Omura , Mastering Autocad 2011 and Autocad LT 2011, Wiley Pub, India
2. ShahRana; 'Engineering Drawing ',Second addition ,Pearson Publication.
3. Dhananjay A Jhole; 'Engineering Drawing with introduction to Auto CAD', FirstAddition;McGraw Hill.
4. Bhatt ND; 'Engineering Drawing Charotar Book stall, 49th Addition ;TulsisadanAnand.

MEW2101	Manufacturing Practice Lab	(0-0-4) , 2 Credits

Course Learning Outcomes (CLO):

CLO1:   The students will understand the working of engines and simple machines

CLO2:   The students will gain knowledge about different processes involved in manufacturing process that enhances employability.

Introduction to manufacturing set up and course requirement; work culture; safety requirements; fire, fire fighting& accident handling; and first aid. Hands on practice in the following works area: Carpentry Shop, Fitting Shop, Sheet Metal Shop, Machine Shop, Welding Shop, Electrical & Electronic Shop, Computer Work Bench. Carpentry Shop: Various types of timber and practice boards, defects in timber, seasoning of wood; tools, wood operation and various joints; exercises involving use of important carpentry tools to practice various operations and making joints. Fitting Shop: Introduction of fitting practice and tools used in fitting shop; exercise involving marking, cutting, fitting practice (Right Angles), male-Female mating parts practice, trapping practice.Sheet Metal Shop: Development of surfaces of various objects; sheet metal forming and joining operations, joints, soldering and brazing; exercises involving use of sheet metal forming operations for small joints. Machine Shop: Introduction to various machine tools, grinders etc; cutting tools and operations; exercises involving lathe, various tools used on lathe, drilling m/c, grinder etc. Welding Shop: Introduction to different welding methods; welding equipment; electrodes; welding joints; welding defects; exercises involving use of gas/ electric arc welding.Electrical & Electronic Shop: Electrical : Introduction to electrical wiring; Testing tools and apparatus. Electronic : Introduction to electronic components (Diode, Resistor, Transistors, Capacitors, LED's, PCB's etc) Preparation of PCBs involving soldering applied to electronic applications. Introduction to tools & test apparatus; Troubleshooting of electronic circuits. Computer Bench Work: Introduction to computer Hardware & peripherals Parts : Motherboard, Processor, Socket types, Input/output ports, Memory (primary, secondary), hard disc, CD/DVD drive, key board, mouse, SMPS. Assembling/Dissembling and Fault identification: SMPS function and power distribution, testing (using multi meter), part connectivity, error correction and detection. Introduction to advance technology and current wireless technologies (laptop component identification, Bluetooth, Wi Fi RF, IRDA etc.).

Recommended Books
1. Raghuwanshi B.S.; , A course in Workshop technology Vol I & II by DhanpatRai& Sons , New Delhi.
2. Jain R.K.; , Production Technology By Khanna Publishers, New Delhi.
3. Singh, S ; Manufacturing Practice, By S.K. Kataria& Sons, New Delhi
4. NN Bhargava; Basic Electronics by TTTI
5. PK Sinha; Computer Fundamental by BPB Publication.
6. Winn L Rosch , Computer Hardware Bible by; Bible Publication.

CSP1103	Introduction to Programming Logic Lab	(0-0-2), 1 Credits

Course Learning Outcomes (CLO):

CLO1: Skilled to understand the problem statement using principles of mathematics and engineering sciences.

CLO2: Identify the OOPs programming constructs to solve the problems by differentiating between efficient and inefficient way of programming.

CLO3: Determine the bugs in a program and recognize the need of alternate approaches.

CLO4: Acquire ability for independent and life-long learning in the broadest context of technological change.

CLO5: Provide solutions to societal, health, safety, legal, and cultural issues through contextual knowledge of professional engineering practice for employability.

An analysis is to be done on the performance of 100 students studying Introduction to programming language (ITPL) Subject. Design the logic and draw a flow chart to find the average of marks of the students in ITPL subject.Allen wants to find the circumference of a circle. He wants an application for the same. The application takes radius as input and gives diameter and circumference as output. Design the logic and draw a flowchart to represent above mentioned problem.Manager of "Indian Computers" wants an application to calculate the net pay of the employees working in an organization. The application should input three values that represent hourly pay rate, the number of hours worked this pay period, percentage of gross salary that is withheld and outputs the net pay. Design the logic and draw the flowchart for the application.The sales manager of The Couch Potato Furniture Company needs a program to determine the profit on any item sold. Input includes the wholesale price and retail price for an item. Draw the flowchart and then plan the logic for a program using modular approach to determine the item's profit.The owner of Bits and Pieces Manufacturing Company, who needs to calculate an employee's projected salary after appraisal. The input is the name of the employee, the employee's current weekly salary, and the percentage increase in appraisal expressed as a decimal. The application should run continuously for any number of employees. Design the logic and draw a flowchart for the application.The manager of the Jeter County softball team wants to compute batting averages for his players. A batting average is computed as hits divided by at-bats. The application prompts the user for a player's jersey number, the number of hits, and the number of at-bats, and then displays all the data, including the calculated batting average. The application accepts player's data continuously until 0 is entered for the jersey number. Using appropriate modules design the logic and draw a flowchart for the application. Chocolate Delights Candy Company manufactures several types of candy. Draw flowchart and design the logic for the program in such a way that it inputs candy name (for example, "chocolate-covered blueberries"), price per pound, and number of pounds sold in the average month, and displays the item's data only if it is a best-selling item.Note: Best-selling items are those that sell more than 2000 pounds per month.Draw flowchart and design the logic for the program in such a way that it accepts candy data continuously and displays a list of high priced, best-selling items. Note: High-priced items are those that sell for $10 per pound or more.Pastoral College is a small college in the Midwest. Draw the flowchart and design the logic for an application that accepts a student's data as follows: ID number, first and last name, major field of study, and grade point average. The application should display a student's data if the student's grade point average is below 2.0.The is a Literary Honor Society in the college for which the similar kind of application need to be designed that will continuously reads student data and displays every student who is an English

major with a grade point average of 3.5 or higher. The Billy Goat Fast-Food restaurant sells the following products:

Design the logic and draw the flowchart for an application that allows a user to enter ordered items. After all items have been entered, the application should display the total bill for the order.In a university the data given to the admission cell is not organized and exist in random order. An application is required that accepts first names of the students in a class and arrange them in alphabetical order. Design the logic and draw the flowchart for the above application.

The Barking Lot is a dog day care centre and they want to automate the billing process. The application should accept the following data: ID number and name of a dog's owner, breed of the dog, age, and weight of the dog and should display the bill containing all the input data as well as the weekly day care fee, which is $55 for dogs less than 15 pounds, $75 for dogs from 15 to 30 pounds inclusive, $105 for dogs from 31 to 80 pounds inclusive, and $125 for dogs over 80 pounds. Design logic and make a flowchart for dog care centre application.

Recommended Books

1. Farrell, J., 2014. Programming logic and design, comprehensive. Cengage Learning.
2. Farrell, J., 2012. An object-oriented approach to programming logic and design. Cengage Learning.
3. Gaddis, T., 2008. Starting Out with Programming Logic and Design, 2/e. Pearson Education India.
4. Farrell, J., 2012. Just Enough Programming Logic and Design. Cengage Learning.

Year 2, Semester-3

ECL4214	**Analog Electronics**	**(3-1-0), 4credits**

Course Learning Outcomes (CLO):

CLO1: Develop the ability to understand the design and working of BJT amplifiers

CLO2: Skill to design BJT based circuits and observe the amplitude and frequency responses of common amplifiers.

CLO3: Skill to design and develop the audio and power amplifiers using re and hybrid equivalent models.

CLO4:   Develop the skill to build, and troubleshoot analog circuits.

BJT introduction and operation, Common Base Configuration, Common Emitter Configuration D.C Biasing : operating point, DC analysis of BJT in CE configuration: Fixed Bias configuration, Emitter Bias Configuration, Voltage Divider Bias configuration Emitter Follower Configuration, Common Base configuration.BJT transistor modeling and small signal ac equivalent circuit The re transistor model in CE configuration: Voltage divider bias to calculate phase relationships . BJT amplifier to calculate voltage gain, input impedance and output impedance. Hybrid Equivalent model. Complete Hybrid circuit to find current gain, voltage gain, input impedance and output impedance. Introduction-Definition and Amplifier Types Series-fed and Transformer-coupled class A Amplifiers Class B Amplifier Operation and Circuits, Amplifier Distortion Class C and D AmplifiersLow Frequency Response BJT Amplifier.

Recommended Books:
1. Robert L. Boylestad and Louis Nashelsky, `Electronic Devices and Circuit Theory' by Pearson Publication, 10th Edition, 2009.
2. Albert Malvino, 'Electronics Principles by McGraw Hill, 7th Edition, 2006.
3. Peyton, A., Walsh, V. and Walsh, Y., 1993. Analog electronics with op-amps: a source book of practical circuits. Cambridge University Press.
4. Maheswari, L.K. and Anand, M.M.S., 2009. Analog electronics. PHI Learning Pvt. Ltd.

| ECL4204 | Signals & Systems | (3-1-0), 4credits |

Course Learning Outcomes (CLO):

CLO1: Categorize various types of signals and systems as continuous/ discrete.

CLO2: Apply various transforms in analysis of systems with different input signals.

CLO3: Skill to interpret the behaviour of Linear time invariant systems (Continuous & Discrete) in terms of system stability and response.

CLO4: Skilled for evaluation of several transforms in analysis of systems with different input signals

Introduction to Signals and Systems Signal – Continuous-time and discrete-time signals; commonly encountered signals;– unit impulse and unit step functions; sampling and aliasing; continuous-time and discrete-time systems; basic properties. Standard Continuous Time Signals; Classification of Continuous Time Signals and their Mathematical Operations; Continuous Time System – Mathematical Equation, Response of LTI Continuous System in Time Domain; Classification of Continuous Time Systems, Laplace Transform Introduction; Region of Convergence; Properties and Theorems of Laplace Transform; Poles and Zeros of Rational Functions of s; Inverse Laplace Transform; application Laplace transform for solving electrical circuits, Fourier Series and Fourier Transform of Continuous Time Signals Review of Fourier series, Fourier Coefficients of Signals with Symmetry; Fourier Series and Fourier Transform Analysis of some important Continuous Time Signal Waveforms; Relation between Fourier and Laplace Transform, Discrete Time Signals and Systems Discrete and Digital Signals; Standard Discrete Time Signals; Sampling of Continuous Time Signals; Classification of Discrete Time Signals and their Mathematical Operations; Discrete Time System – Mathematical Equation, Response of LTI Discrete System in Time Domain, Z Transform Introduction, Region of convergence; inverse z-transform; properties; analysis of LTI systems using the z-transform.

Recommended Books:
1. A NagoorKani, 'Signals and Systems' by, ISBN: 978-0-07-015139-0, McGraw Hill Education, First Edition, Copyright © 2010.
2. Tarun Kumar Rawat, Signals and Systems' by ISBN: 978-0-19-806679-8, Oxford University Press, First Edition, Copyright © 2010.
3. B. P. Lathi, `Principles of Linear Systems and Signals, Oxford University Press, First Edition, Copyright © 2009.
4. S Salivahanan, C Gnanapriya, 'Digital Signal Processing' by 2nd edition, Tata McGraw-Hill, ISBN:9780070669246,2, 2013.

ECL4208   **Control Systems**   **(3-1-0), 4credits**

Course Learning Outcomes (CLO):

CLO1:   The students would be able to understand operation of basic control systems employed in industries.

CLO2:   The students would be able to propose automation solutions to real world problems

CLO3:   The students would attain skill to carry out time domain and frequency domain analysis of a designed control system.

CLO4:   Skilled to solve automation solutions to real world problems

The control System, Feeback Control system, transfer function of Mechanical, translational, rotational, electrical system,   control system components potentiometer, synchro, tachometer, Block diagram Algebra, Mason's Gain formula, transfer function of Signal flow graphs, State variable approach: advantage and disadvantages, basic concepts.Classification or time responses, system time response, analysis of steady state error , Type of input and steady state error ,Steady state error for type 0,1,2 systems , Analysis of the system, effect of damping ratio on second order system, time response specifications, Concept of stability ,Routh-Hurwitz criterion, Routh's stability criterion.Root locus concepts ,construction root locii,   Frequency response, Methods in frequency response, Advantages and disadvantages in frequency response.Polar plots, Bode plots, Nyquist stability criterion, Nyquist analysis. Realization of basic compensators (lead) with advantages and disadvantages, Introduction to basic actions of controllers.

Recommended Books:

1. SamarjitGhosh, 'Control Systems' by 1st edition, Pearson Education, ISBN-81-317-0828-4.
2. K. Ogata `Modern Control Engineering' by, 4th edition, Pearson, ISBN-81-7808-579-8.
3. B.C.Kuo `Automatic Control Systems' by, 7th edition, PHI,ISBN-81-203-0968-5.
4. Isermann, R., 2013. Digital control systems. Springer Science & Business Media.

| **ECL4207** | **Digital Electronics & Logic Design** | **(3-1-0), 4credits** |

Course Learning Outcomes (CLO):

CLO1: Understand the basics of difference between analog and digital circuits and their applications.

CLO2: Skill to implement simple logical operations required for the designing of digital circuits and understand common forms of number representation.

CLO3: Reduction of Boolean expressions for the designing of minimized logical circuits.

CLO4: Skill to design and implementation of combinational circuits.

CLO5: Skill to design and implementation of sequential circuits and their application.

Introduction to Digital Concepts:Digital and Analog systems, logic levels & Pulse waveform. Logic Gates: And Gate, OR Gate, Not gate, Universal Gates, Exclusive–OR gate, Exclusive-NOR gate. Number systems: Decimal number system, Binary number system. Representation of signed numbers.Octal number system, Hexadecimal number system. Binary codes: Classification of binary codes. 8421 BCD code, Excess three code, Gray code. Error detecting codes: Parity, checksum, block parity. Error correcting codes: 7 bit Hamming code, code. Boolean algebra: Laws of Boolean algebra and De Morgan's Theorem. Minimization of Boolean expression. Boolean expression and logic diagram, converting AND/OR/Invert Logic to NAND/NOR logic. Boolean Functions and their representation: Sum of Product (SOP), Product of Sum (POS), canonical forms. Introduction to Karnaugh map. Introduction to Logic families. Combinational circuit: Arithmetic circuits Binary Adders &Subtractors (half and full), Magnitude Comparator: Multiplexer and Demultiplexer, Encoder, Decoder, Code Converters Parity bit generators and checkers. Sequential circuits: Classification of sequential circuits, Flip flops SR, JK, T, D, Race around condition and Master slave flip flops Flip flop excitation table, Conversion of flip flops. Introduction Shift Registers. Counters: Asynchronous counters, design of asynchronous counters, effects of propagation delay in ripple counters, synchronous counters. Shift register counter: Ring counter and Johnson counter. D/A Converter and A/D converters: Introduction, Digital to analog conversion, R-2R DAC, weighted resistor DAC, A/D Converter: Analog to digital conversion using Successive approximation method, Dual slope method.

Recommended Books:

1. A.Anand Kumar, Fundamentals of digital circuits, 3rd Edition, PHI.
2. Thomas L. Floyd, 10th Edition, Digital Fundamentals, Pearson Publications.
3. M. Morris Mano, Digital Design, 4.ed., Prentice Hall of India Pvt. Ltd., New Delhi, Sixth impression /Pearson Education (Singapore) Pvt. Ltd., New Delhi.
4. Donald P.Leach and Albert Paul Malvino, Digital Principles and Applications, 5th Edition, Tata McGraw Hill Publishing Company Limited, NewDelhi, 2003.

ECP1214	Analog Electronics Lab	(0-0-2), 1credit

Course Learning Outcomes (CLO):

CLO1: To be able to read and interpret electronic datasheets and diagrams.

CLO2: To be able to measure the electronics & electrical parameters of an amplifier like voltage gain, input & output impedance.

CLO3: Skill to design, construct and troubleshoot transistor based amplifier complex electronic circuits

Familiarization with Cathode Ray Oscilloscope, Function Generator and Power Supply, Study and analysis of Fixed base biasing With &without Emitter Resistor and collector to Base Biasing of BJT. Study and analysis of Self Bias Circuit and Voltage Divider Bias of BJT. To plot the frequency response of Single stage Common Emitter (CE) Amplifier and calculate its gain and bandwidth. To plot the frequency response of Single stage CB (Common Base) Amplifier and calculate its gain and bandwidth. To plot the frequency response of Single stage CC (Common Collector) Amplifier and calculate its gain and bandwidth. To calculate the gain and bandwidth of CE amplifier with feedback and without feedback BJT Class A power amplifier-To simulate and verify the efficiency of BJT Class A Power amplifier circuit. BJT Class B Push pull Power amplifier -To simulate and verify the efficiency of Transistor Class B Push pull Power amplifier. BJT Class AB power amplifier- To simulate and verify the efficiency of class AB power amplifier. BJT Class C power amplifier - To simulate and verify the efficiency of class C power amplifier. BJT Complementary Symmetry Push pull power amplifier - To simulate and verify the efficiency of Transistor Complementary Symmetry Push pull power amplifier.

Recommended Books:

1. Robert L. Boylestad and Louis Nashelsky, `Electronic Devices and Circuit Theory' by Pearson Publication, 10th Edition, 2009.
2. Albert Malvino, 'Electronics Principles by McGraw Hill, 7th Edition, 2006.
3. Peyton, A., Walsh, V. and Walsh, Y., 1993. Analog electronics with op-amps: a source book of practical circuits. Cambridge University Press.
4. Maheswari, L.K. and Anand, M.M.S., 2009. Analog electronics. PHI Learning Pvt. Ltd.

ECP1207	Digital Electronics & Logic Design Lab	(0-0-2), 1credit

Course Learning Outcomes (CLO):

CLO1: To understand the digital logic and create various systems by using these logics.

CLO2: Develop a skill to understand the design and simulation of digital logic circuits.

CLO3: To get a basic understanding of layout of electronic circuits.

CLO4: Skill to use the Multisim tool for design and simulation.

CLO5: Skill to design and implementation of sequential circuits and their application.

Logic gate is a basic building block of a digital circuit. So verify the truth tables of all the logic gates on trainer kit using TTL ICs. Also verify them using multsim. Mr. Vivek wants to add two numbers in computer but computer only understands the binary numbers i.e. 0&1.So design a circuit that adds binary equivalent of two decimal numbers using multisim.Suppose there are two binary numbers as input and subtract one binary number input from other binary number input. Design the circuit using universal gates.Considering two numbers (each two bit), Design a circuit which produces the output that compares whether the number is greater than, less than or equal to the second number.A code represents each number in the sequence of integers{0...2^N-1} as a binary string of length N in an order such that adjacent integers have code representations that differ in only one bit position. Design a convertor that has above property.

Design and verify the flip flops.

In digital logic and computing, a counter is a device which stores the number of times a particular event or process has occurred in relationship to a clock signal.

i) Design such a counter using multisim which uses a circulating shift register in which last flip flop shifts its value into the first flip flop.

ii) Also design a counter using multisim in which the inverted output of the last flip flop is connected to the input of first flip flop.

Recommended Books:
1. A.Anand Kumar, Fundamentals of digital circuits, 3rd Edition, PHI.
2. Thomas L. Floyd, 10th Edition, Digital Fundamentals, Pearson Publications.
3. M. Morris Mano, Digital Design, 4.ed., Prentice Hall of India Pvt. Ltd., New Delhi, Sixth impression /Pearson Education (Singapore) Pvt. Ltd., New Delhi.
4. Donald P.Leach and Albert Paul Malvino, Digital Principles and Applications, 5th Edition, Tata McGraw Hill Publishing Company Limited, NewDelhi, 2003.

| **ECP2203** | **Measurement & Virtual Instrumentation Lab** | **(0-0-4), 2credits** |

Course Learning Outcomes (CLO):

CLO1: The students will be skilled to design any instrumentation-based project in employment

CLO2: The students will be skilled to simulate any type of signals and check performance of any circuit based on these simulated signals.

CLO3: Skill of using Elvis instrument and perform experiment on it

CLO4: Skilled to work on interfacing hardware with software.

CLO5: Skilled for the creation of new projects.

Introduction to LabVIEW software: LabVIEW components, function pallette, control pallete, loops and structures, waveform graphs, SubVI, Debugging techniques, Icon editor, formula node.Virtual Instrumentation: Creating a virtual instrument using LabVIEW. Different types of Waveform generation and analyze the signals by measuring amplitude, frequency and phase variations using soft front panel instruments (CRO, FGEN, DMM), introduction to ELVIS-II. Measurement of instantaneous, peak to peak and average value of voltage, period and phase angle using oscilloscope and Lissajous pattern using LabVIEW.Sources of error in measurements and its statistical analysis:Measurement of resistances of resistors of same color coded values using DMM SFP and investigates errors (Gross error, systematic error and random error) in measurements and statistical analysis using waveform graph.Introduction to control system toolbox of LabVIEW:Stability test of series parallel circuit using labview, generation of Bode plot, Nyquist plot, Root locus plot of the given transfer function. Obtain the responses of the systems i.e. proportional (P), the integral (I), and the derivative (D), PID control system..Synchro set and servo system:Implementation of transmitter- receiver characteristics of a synchro set.

Recommended Books:

1. S. Sumathi and P. Surekha , LabVIEW based advanced instrumentation system by, springer.

2. Jerome, J., 2010. Virtual instrumentation using LabVIEW. PHI Learning Pvt. Ltd..

3. Klaassen, K.B., 1996. Electronic measurement and instrumentation. Cambridge University Press.

4. Oliver, B.M. ed., 1971. *Electronic measurements and instrumentation* (Vol. 12). Tata McGraw-Hill Education.

CSL4102/CSP2102   Problem Solving Techniques Using C/Lab   (2-0-8),   6credits

Course Learning Outcomes (CLO):

CLO1:   Skilled to understand the problem statement using principles of mathematics and engineering sciences.

CLO2:   Identify the C programming concepts to solve the problems by differentiating between efficient and inefficient way of programming.

CLO3:   Determine the bugs in a program and recognize the need of alternate approaches.

CLO4:   Acquire ability for independent and life-long learning in the broadest context of technological change.

CLO5:   Provide solutions to societal, health, safety, legal, and cultural issues through contextual knowledge of professional engineering practice for employability.

Structure of a c program, Compilation, Linking & Execution, Comments in C, Identifiers: Nomenclature of an Identifier, Variables, Constants, Reserved KeywordsData Types: Introduction Initialization and Declaration of Data Type, Expressions, Statements, Symbolic Constants, Memory representation of integer, character and float data types, Conversion / Type Casting.Input/ Output in C: Introduction, scanf(), printf( )Operators: Arithmetic, Relational, Logical, Assignment, Conditional, bitwise, sizeof, Precedence of operators and their associativity,Selection control Statements: if , if – else, Switch,Iteration control Statements:While, do – while, for, Nested loops, Continue, break, Introduction to complexity analysis, Functions: User defined functions, Built-in functions, Function parameter passing mechanisms: call by value, call by reference, Recursion,Pointers: Introduction to pointer, Pointer expression and pointer Arithmetic, Assignment, Value finding (dereferencing),Taking a pointer address, Adding an integer to a pointer, null pointer, generic pointer, Storage classes: auto, register, static, extern.Pre-processor directives: #define , #include , #if, #elif, #else, #endif, #undef, #ifdef, #ifndef, #error,Program Execution: Command line arguments in C, Debugging, Makefile, Static and dynamic libraries,File Handling: File pointer, open file, close file, Read data from file:fgetc(), fgets(), fscanf(),fprintf(), Writing data to a file:fputc(),fputs(),fprintf(),fwrite(), Detecting End-of-file, Functions for selecting record randomly: fseek(),ftell(),rewind(), Difference between Text Mode and Binary Mode,Arrays: Introduction, Need & Importance, One Dimensional Arrays, Two Dimensional Arrays, Initialization of arrays, inputting values, assigning Values, Multi-Dimensional Arrays: Declaration of an Array, Initialization of an Array, Passing 1-D to Function, passing 2-Darray to function, representing arrays as pointer, Arrays of pointers,Strings: Introduction,   Reading   and   writing   strings,   String   functions (Predefined):isalpah(),isdigit(),isspace(),   strcat(),   strncat(),strcpy(),strncpy(),strlen(),strncpy, Pointers and Strings,Structure –Declaring Structure, Accessing members of Structure, Copying Structure, Accessing Structure elements, Nested Structure, Array of structure, passing structure elements to a function individually, Passing entire structure to a function, Pointer to structure, Passing pointer of structure to function, Padding and data packing in structures, Unions Bit Fields in c, enum in c, Dynamic memory Allocation in c, Function pointer in C.

Recommended Books:

1. ReemaThareja, Programming in C, 2nd Edition, Oxford University Pres
2. Vikas Gupta, Computer concepts and C programming, 1st edition, DreamTech Press
3. Dennis Ritchie and Brian. W. Kernighan, The C Programming Language, 2nd edition, Prentice Hall.
4. Ritchie, D.M., Kernighan, B.W. and Lesk, M.E., 1988. The C programming language. Englewood Cliffs: Prentice Hall.

Year 2, Semester-4

| ECL4205 | Network Analysis & Synthesis | (3-1-0), 4 credits |

Course Learning Outcomes (CLO):

CLO1: Students will develop sufficient knowledge on circuit analysis techniques.

CLO2: Students will be skilled to perform time domain as well as frequency domain analysis of any electrical circuit.

CLO3: Students will be skilled to synthesize various electrical networks like two port networks and filters circuits.

CLO4: Skilled to execute all domain analysis of any electrical circuit.

The circuit, Energy Sources, Kirchhoff's Voltage Law, Voltage division, Kirchhoff's current law, Current division, Introduction, tree, Co-tree, twigs and links, incidence matrix(A), Incident matrix and KCL, Link currents-Tie set matrix, Cut set and tree branch voltage, Mesh Analysis, Mesh equations by inspection method, Super Mesh Analysis, Nodal Analysis, Nodal equations by inspection method, Super node analysis, State Equation for networks, Source transformation technique, Wye-delta transformation ,Superposition theorem for DC, Thevenin's theorem for DC and AC circuits, Nortan's theorem for DC and AC circuits, Reciprocity theorem for DC circuits, Maximum Power Transfer theorem for DC and AC circuits, Impedance diagram, phasor diagram, series circuits, parallel circuits, Steady state and Transient response ,DC Response of RL circuit, DC Response of RC circuit, DC Response of RLC circuit, Sinusoidal Response of RL circuit, Sinusoidal Response of RC circuit.Two port Network, Open circuit impedance parameters, Short circuit admittance parameters, Transmission parameters.

Recommended Books:

1. Sudhakar Sham Mohan, Network Analysis and Synthesis by Tata McGraw Hill Publication Fourth   Edition, 2004**.**
2. H Hayt, J E Kemmerly, S M Durbin, Engineering Circuit Analysis' by W Tata McGraw Hill Publication, Seventh Edition, 2006.
3. D.Roy Choudhury; Networks and Systems: New Age International, edition 2nd, 2012.
4. Anderson, B.D. and Vongpanitlerd, S., 2013. Network analysis and synthesis: a modern systems theory approach. Courier Corporation.

ECL 4317	**Digital Signal Processing**	**(3-1-0), 4credits**

Course Learning Outcomes (CLO):

CLO1: Identify different types of discrete signals, implement these signals on different systems using z transform, Discrete Fourier Transform and Fast Fourier Transform.

CLO2: Student can apply knowledge to design and filters and implement them for signal processing applications.

CLO3: Apply the knowledge to design and analyse a practical discrete-time signal system, such as a radar, image, speech, audio, bio-medical or wireless system during employment.

CLO4: Implementation of signal processing applications in employment

CLO5: Skilled to design and analyse a practical discrete-time signal system, in their employment.

Introduction to signals, DFT of discrete time signal, Properties of DFT, ,Relation between DFT and z-transform, Analysis of LTI discrete time system using DFT,─DFT as a Linear Transformation Fast Fourier Transform, Radix-2(DIT), Fast Fourier Transform, Radix-2(DIF), Computing an Inverse DFT using FFT Finite Impulse Response Filters: Frequency response of linear phase FIR filters(case 1 only), Design Techniques for FIR filters using Window method Design techniques for FIR filters using Frequency Sampling method. Infinite Impulse Response Filters: Introduction, Frequency response of Analog and digital IIR Filter─Impulse Invariant Method, Bilinear Transformation, Butterworth filters, Chebyshev Filters Realization of Digital Filters: Basic Structures for IIR Systems, ─ Direct Form I, Direct Form II, Cascade Structure, Parallel Realization of IIR System Basic Structures for FIR system. Multirate Signal Processing Introduction, Decimation and Interpolation.

Recommended Books
1. A. NagoorKani, 'Digital Signal Processing' Second edition, 2012, Tata McGraw-Hill Education private limited.
2. S.Salivahanan, 'Digital Signal Processing' third edition-2015 Tata McGraw-Hill Education private limited.
3. John G. Proakis, Dimitris G. Manolakis, 'Digital Signal Processing' by Prentice Hall of India Pvt. Ltd., 4th edition.
4. Bose, T. and Meyer, F., 2003. *Digital signal and image processing*. John Wiley & Sons, Inc.

ECL4314	**Linear Integrated Circuits**	**(3-1-0), 4credits**

Course Learning Outcomes (CLO):

CLO1: Skilled to design Op-amp based circuit to give specified gain.

CLO2: To compute component values to design different Op-amp based applications such as arithmetic building blocks, filters, waveform generators.

CLO3: Develop practical skills for building and testing circuits using analog ICs during employment.

CLO4: Able to compute component values to design different Op-amp based applications in their employment

CLO5: Skilled in practical skills for building and testing circuits using analog ICs in their employment.

Introduction to Op-Amp: Operational Amplifier, Block Diagram. Schematic symbol, Integrated Circuits, types of Integrated Circuits. Ideal Op Amp, equivalent circuit, Ideal voltage transfer curve, open loop Op Amp configurations. Unit-2: Op-Amp ideal circuits: define feedback configurations, Voltage Series Feedback Amplifier: closed loop voltage gain, difference input voltage ideally zero, input and output resistance Bandwidth, total output offset voltage, voltage follower. Voltage Shunt Feedback Amplifier: closed loop voltage gain, inverting input terminal at virtual ground, Input and output resistance Bandwidth, total output offset voltage. Current to voltage converter. Linear applications : DC and AC Amplifiers, Summing, Scaling and averaging amplifier, instrumentation Amplifier, Integrator Circuit, Differentiator Circuit, Introduction to Voltage to current converter with floating load and grounded load. Unit-3: Non-idealities and frequency response: Practical Op Amps, Frequency response: compensating networks, frequency response of internally compensated and non-compensated op amps, high frequency op amp equivalent circuit, open loop voltage gain as a function of frequency, close loop frequency response, circuit stability, slew rate: Causes of slew rate, slew rate equation. Unit-4: Active filters and Oscillators.

Recommended Books:
1. Ramakant A. Gayakwad "Op-AMPS and Linear Integrated Circuits", by, Prentice-Hall, 4th edition, 2008.
2. T.L Singal "Linear Integrated Circuits", by, PBS Education, 1st edition, 2015.
3. S. Salivahanan, V S Kanchanna Bhaaskaran, "Linear Integrated Circuits", by Tata McGraw-Hill, 1st Edition, 2008.
4. D. Roy Choudhary, Sahil B. Jain. "Linear Integrated Circuits", by, New Age Techno press, 4th edition, 2010.

| ECL4215 | Analog Communication Systems | (3-1-0), 4credits |

Course Learning Outcomes (CLO):

CLO1: The students would understand various modulation concepts and distinguish between various modulation schemes on the basis of advantages, disadvantages and applications as used in analog and digital wireless communication systems.

CLO2: The students would be skilled to analyze design aspects of generation and detection techniques of AM and FM signals as used in broadcast radio and TV transmissions.

CLO3: The students would be able to select appropriate method to convert an analog signal to digital signal with suitable line coding technique for baseband transmission systems.

CLO4: They would possess an ability to apply knowledge of various digital modulation schemes to improve performance of advanced digital cellular communication systems during employment.

Introduction to Electronic Communications System; Introduction to Electronic Signals – Transmission Media and Modes; Noise in Electronic Communications; Classifications and sources of Noise, Signal to Noise Ratio (SNR), Noise Figure: Definition, Noise Figure in cascaded stages, Friis Formula for Noise Figure; Modulation – Need and Types. Types of Analog Modulation; Principles of Amplitude Modulation; AM for a Complex Modulating Signal; Limitation of AM; DSBSC, SSB and VSB Modulation Techniques and their Comparison; Applications of AM. Principles of Angle Modulation; Theory of FM – Basic Concepts; Spectrum Analysis of FM Wave; Narrowband and Wideband FM; Theory of Phase Modulation; Relationship between FM and PM; Advantages and Disadvantages of Angle Modulation; Comparison of AM, FM and PM; Applications of FM and PM. Low-Level and High-Level AM Transmitters; AM Superheterodyne Receiver; Receiver Characteristics; AM Detectors; AGC; FM Modulators and Transmitters – Direct FM Varactor Diode, FM Reactance, PLL Direct and Armstrong Indirect FM; FM Superheterodyne receiver; Amplitude Limiter; Pre-emphasis and De-emphasis, FM Demodulators – Types; FM Detection using PLL; Two-way FM Radio Communications. Sampling Theorem for Baseband and Bandpass Signals; Introduction to Pulse Amplitude Modulation.

Recommended Books:
1. T L Singal, Analog and Digital Communications, ISBN: 978-0-07-107269-4, McGraw Hill Education, First Edition, Copyright © 2012.
2. B P Lathi and Zhi Ding, Modern Digital and Analog Communication Systems, ISBN: 978-0-19-807380-2, Oxford University Press, International 4th Edition, Copyright © 2010.
3. T. L. Singal, Electronic Communications, ISBN: 978-93-82782-16-2, Chitkara University Publications, First Edition, Copyright © 2014.
4. R P Singh and S D Sapre, Communication Systems: Analog and Digital, ISBN: 978-1-25-900460-5, McGraw Hill Education, Third Edition, Copyright © 2012.

| ECP1305 | Digital Signal Processing Lab | (0-0-2), 1credit |

Course Learning Outcomes (CLO):

CLO1: To understand and analyze the different types of signals in time domain and frequency domain.

CLO2: Skill to design and implement the characteristics of the digital filters (FIR and IIR).

CLO3: Can apply skill of programming using MATLAB to develop the computation of Transforms and convolution during employment.

CLO4: Skill of hand-on training on MATLAB programming and simulation.

CLO5: Skill of modeling FIR and IIR filters on MATLAB.

Introduction to MATLAB,Write a MATLAB program to generate unit impulse sequence, unit step sequence, ramp sequence and exponential (a)Computation of N- point DFT of a discrete time signal and plot the magnitude and phase response using direct approach. Compute linear convolution of two discrete time sequences.Compute circular convolution of two discrete time sequences.a) Computation of z transform in factored form.b) Verification of pole zero analysis using transfer function.Design and implementation of FIR filter using rectangular window.Design Butterworth low pass and high pass filter with the given specifications. Find the autocorrelation and cross correlation coefficients of discrete time signals.

Recommended Books

1. A. NagoorKani, 'Digital Signal Processing' Second edition, 2012, Tata McGraw-Hill Education private limited.
2. S.Salivahanan, 'Digital Signal Processing' third edition-2015 Tata McGraw-Hill Education private limited.
3. John G. Proakis, Dimitris G. Manolakis, 'Digital Signal Processing' by Prentice Hall of India Pvt. Ltd., 4th edition.
4. Bose, T. and Meyer, F., 2003. *Digital signal and image processing*. John Wiley & Sons, Inc.

ECP1314   **Linear Integrated Circuits Lab**   **(0-0-2), 1credit**

Course Learning Outcomes (CLO):

CLO1:   Skilled able to select an appropriate IC for a industrial and domestic applications by interpreting electronic datasheet.

CLO2:   Skilled to design an op amp based circuit such as filters, oscillators, generators, converters and can solve problems related to it during employment.

CLO3:   Skilled to troubleshoot and replace the defective parts of op amp based electronic circuits during employment.

CLO4:   Develop appropriate communication skills, particularly technical reports through the laboratory for student as entrepreneur.

CLO5:   Skill of hands-on training of designing linear integrated circuits

To investigate the application of negative feedback Operational Amplifier as Inverting and Non Inverting configuration. To observe the performance parameters of an Operational Amplifier. To observe & study frequency response of an Operational Amplifier. Measurement of Saturation limits of an Operational Amplifier. To design a Differentiator circuit and observe output with different input waveforms using Op-Amp. To design an Integrator circuit and observe output with different input waveforms using Op-Amp. To investigate an application of an OP-Amp as Schmitt trigger. To Calculate the time period and frequency of waveform generated of 555 timer using Op-Amp. To observe an application of an Op Amp as summing, scaling and averaging circuit. To understand the concept of differentiator as a building block for designing High pass Butterworth active filters using Op Amp. To understand the concept of an integrator as a building block for designing Low pass Butterworth active filters using Op Amp.

Recommended Books:

1. Ramakant A. Gayakwad "Op-AMPS and Linear Integrated Circuits", by, Prentice-Hall, 4th edition, 2008.
2. T.L Singal "Linear Integrated Circuits", by, PBS Education, 1st edition, 2015.
3. S. Salivahanan, V S Kanchanna Bhaaskaran, "Linear Integrated Circuits", by Tata McGraw-Hill, 1st Edition, 2008.
4. D. Roy Choudhary, Sahil B. Jain. "Linear Integrated Circuits", by, New Age Techno press, 4th edition, 2010.

CSL3201/CSP2201   Object Oriented Programming/lab   (2-0-8), 6credits

Course Learning Outcomes (CLO):

CLO1:   Skilled to understand the problem statement using principles of mathematics and engineering sciences.

CLO2:   Identify the C++ programming concepts to solve the problems by differentiating between efficient and inefficient way of programming.

CLO3:   Determine the bugs in a program and recognize the need of alternate approaches.

CLO4:   Acquire ability for independent and life-long learning in the broadest context of technological change.

CLO5:   Provide solutions to societal, health, safety, legal, and cultural issues through contextual knowledge of professional engineering practice for employability.

Introduction to basic concepts of object-oriented programming, Comparison between procedural programming paradigm and object-oriented programming paradigm. Functions in C++: inline functions, default arguments, function prototyping, function overloading, call by reference (call by value & call by pointer). Classes and Objects: Specifying a class, Creating class objects, Accessing class members, Access specifiers – public, private, and protected, Classes, Objects and memory, Static members, Static objects, constant member function, constant objects, friend functions, friend class. Dynamic Memory Management & pointers: Understanding pointers, Accessing address of a variable, Declaring & initializing pointers, Accessing a variable through its pointer, Pointer arithmetic, Pointer to a pointer, Pointer to a function, Dynamic memory management - new and delete Operators, Pointers and classes, Pointer to an object, Pointer to a member, this Pointer, Possible problems with the use of pointers - Dangling/wild pointers, Null pointer assignment, Memory leak and allocation failures. Constructors and Destructors: Need for constructors and destructors, Copy constructor, Dynamic constructors, Destructors, Constructors and destructors with static members Operator Overloading and Type Conversion: Defining operator overloading, Rules for overloading operators, Overloading of unary operators and various binary operators, Overloading of new and delete operators, Type conversion - Basic type to class type, Class type to basic type, class to class type. Inheritance: Introduction, Defining derived classes, Forms of inheritance (single, multilevel, multiple, hybrid & hierarchical), Ambiguity in multiple and multipath inheritance.Virtual Base Class: Overriding member functions, Order of execution of constructors and destructors, inheritance with constructor. Virtual Functions and Polymorphism: Concept of Binding - Early binding and late binding, Virtual functions, Pure virtual functions, Abstract classes, Virtual destructors & polymorphism. Exception Handling: Review of traditional error handling, Basics of exception handling, Exception handling mechanism- Throwing/Catching an exception, Re-throwing an exception, Specifying exceptions.

Recommended Books:
1. E Balagurusamy, Object Oriented Programming with C++, 4th Edition, Tata McGraw Hill.
2. Robert Lafore, Object Oriented Programming in C++, Third Edition, Galgotia 2008.
3. Herbert Schildt, The Complete Reference C++, Second edition, Tata McGraw Hill.
4. Stroustrup, Bjarne, The C++ Programming Language, Pearson Education.
5. Lippman, S.B. and Lajoie, J., C++Primer, Pearson Education.

Year 3, Semester-5

ECL4315	Microprocessor & Microcontroller	(3-1-0), 4credits

Course Learning Outcomes (CLO):

CLO1: After completing the course students will be skilled to differentiate between the real time applications of microprocessor and a microcontroller.

CLO2: The student will be able to design a memory and I/O interface aspects for an 8085-based computer systems.

CLO3: Students will develop the knowledge regarding architecture and peripheral configuration of STM32L476.

CLO4: Students will be able to write embedded C code to develop applications using I/O ports, timers and other peripherals of a microcontroller which increases employability.

CLO5: Skilled to develop applications using I/O ports, timers and other peripherals of a microcontroller in their employment

Introduction; Overview of microprocessor (8085) and micro-controller (8051), features and comparison. Intel 8085 Architecture and Programming: Functional Blocks of µP and its architecture, Programming model, Function of 8085 chip pins, Introduction to instruction set, Addressing modes, Assembly language programming for 8085.Memory and I/O interfacing, Introduction to Interrupts and Serial Communication. Architecture of 8051:Micro-controller and Embedded Processors, Criteria for choosing a micro-controller, Overview of 8051 family, block diagram, Architecture and Instruction Set, Assembly language programming of 8051, Function of 8051 chip pins and I/O port programming, Programming 8051 Timers, Interrupt Programming, Serial port programming, Interfacing with RS 232.Introduction to 8086 microprocessor and its architecture.

Recommended Books:
1. N. Senthil Kumar, M. Saravanan& S. Jeevananthan, "Microprocessors and Micro-controllers", Oxford University Press, 2010.
2. Muhammad Ali Mazidi, Janice GillispieMazidi and RolinD.Mckinlay, "The 8051 Microcontroller and Embedded System", Pearson Education, 2008.
3. Ramesh S. Gaonkar, "Microprocessor Architecture, Programming and Applications with 8085", Prentice Hall, 2002.
4. Douglas V. Hall, "Microprocessors and Interfacing", Tata McGraw-Hill, Revised Second Edition, 2006.

ECL4322	Electromagnetic Waves & Antennas	(3-1-0), 4credits

Course Learning Outcomes (CLO):

CLO1: Develop sufficient knowledge on fundamental of Electromagnetic field theory and its applications such as Vector Calculus and Co-ordinates Systems.

CLO2: Understand Maxwell's equations and apply them to solve practical electromagnetic fields problems.

CLO3: Analyses the behavior of EM Wave through different medium such as Transmission Lines and Waveguides.

CLO4: Skill to solve transmission line impedance mismatching problems in communication and power transmission using stub matching and Smith chart.

CLO5: Understand the basic parameters & properties of Antennas, Antenna Types, and Antenna Arrays for Antenna Gain and Directivity Enhancement.

Co-ordinate Systems and Vector Calculus: Cartesian Co-ordinates, Circular Cylindrical Co-ordinates, Spherical Co-ordinates.Differential Length, Area and Volume. Divergence and Curl (Cartesian Co-ordinates): Del Operator and gradient of scalar field, Divergence of vector field, Curl of vector field, Laplacian operator.Gauss and Stokes's Theorem.Maxwell's equations: Guass's Law, Ampere's Circuit Law, Magnetic Flux Density. Faraday's law, Displacement Current.Maxwell's Equations in Differential and Integral Forms. Electromagnetic wave propagation: Introduction, Wave Equation, Wave Propagation in Lossy Dielectrics. Plane waves in Lossless Dielectrics, Plane waves in Free space. Plane waves in Good Conductors, Skin Depth. Power and Poynting Vector.Reflection of plane wave at normal incidence, Reflection of plane wave at Oblique incidence. Transmission Lines: Introduction, Transmission Line Parameters, Transmission Line Equations, Characteristic impedance. Impedance transformation: Input Impedance, Standing Wave Ratio and Power, Smith Chart. Impedance matching- Quarter Wave Transformer (Matching), Single-Stub Tuner (Matching), Slotted line (Impedance measurement).: Antenna pattern, Radiation Intensity, Directive gain, Power gain. Antenna Arrays: Array of two point sources with: Equal amplitude and phase, equal amplitude and opposite phase, unequal amplitude and any phase.

Recommended Books

1. Matthew N.O. Sadiku, 'Principles of Electromagnetics' Fourth Edition International version, Oxford University Press.
2. Fawwaz T. Ulaby 'Electromagnetics for Engineers' by,Pearson Education, Inc. 2005.
3. Constantine A. Balanis, 'Antenna Theory Analysis and Design' by John Wiley & Sons, Inc., Second Edition.
4. Prasad, K.D. and Handa, D., 2003. *Antenna and wave propagation*. Satya Prakashan.

ECL4321   **Digital Communication Systems**   **(3-1-0), 4credits**

Course Learning Outcomes (CLO):

CLO1:   The students would be able to select appropriate method to convert an analog signal to digital signal with suitable line coding technique for baseband transmission systems.

CLO2:   They would possess an ability to apply knowledge of various digital modulation schemes to improve performance of advanced digital cellular communication systems during employment.

CLO3:   Skill of various schemes to improve performance of communication systems.

Introduction – Digital Representation of Signals, Advantages and Disadvantages of Digital Transmission; Review of Sampling Theorem; Pulse Code Modulation – Block Diagram, Sampling, Quantization and Encoding; PCM System parameters; DPCM and adaptive DPCM; Delta Modulation; Slope Overload and Granular Noise; Adaptive Delta Modulation; Comparison of PCM and DM Techniques, Line Coding and its Types – Desirable Properties; Unipolar, Polar and Bipolar Line Codes; Intersymbol Interference (ISI); Digital Multiplexing and TDM – Synchronous and Asynchronous; Digital Carrier Systems, Binary Digital Modulation Techniques – ASK, FSK and PSK; M-ary Digital Modulation Techniques – QPSK, OQPSK, MSK, GMSK and QAM; Coherent and Non-coherent Detection; Performance of Digital Modulation Techniques, Introduction – Random Variables, Probability Distributions, Power Spectral Density, Random Processes.

Recommended Books:

1. T L Singal, Digital Communication, ISBN: 978-93-392-1952-9, McGraw Hill Education, First Edition, Copyright © 2015.
2. Moser and Chen, A Student's Guide to Coding and Information Theory, ISBN: 978-1-107-68457-7, Cambridge University Press, First Edition, Copyright © 2012.
3. R N Mutagi, Digital Communication: Theory, Techniques, and Applications, ISBN: 978-0-19-808722-9, Oxford University Press, First Edition, Copyright © 2013.
4. University Press, First Edition, Copyright © 2012.

ECL4303	Microelectronic Circuits	(3-1-0), 4credits

Course Learning Outcomes (CLO):

CLO1: After completion of the course, students will be able to construct and apply physical model to determine the electrical characteristic and operation principle of microelectronic devices.

CLO2: Skill of designing digital as well as analog circuits using CMOS technology

CLO3: Students will apply the concept of IC fabrication to create layouts of digital circuits at entrepreneur level.

CLO4: Able to design both circuits using CMOS technology used in industry.

CLO5: Execute the concept of IC fabrication to create layouts of digital circuits during their employment

Historical Perspectives andevolution of MOS,MOS structure without and with external bias, Structure and Operation of MOS Transistor, Gradual channel approximation, channel length modulation, MOSFET Capacitances. Introduction to circuit designing, CMOS Inverter: representation, CMOS working, DC-characteristics, Logic gates designing using CMOS logic and Pseudo nMOS logic, CMOS transmission gates and complementary pass transistor logic, SR Latch circuit, SR flip-flop designing, D Flip Flop designing, CMOS dynamic circuits, Domino logic, NP domino logic, Zipper domino logic. Introduction to memory design, Operation of one Transistor DRAM Cell, Operation of 6-T SRAM cell, Overview of power consumption. Introduction, Fabrication Process Flow: Basic steps, Fabrication of nMOS transistor, silicon on Insulator, floating body voltage, SOI advantages and disadvantages, Layout Design Rules, Full custom mask layout design.

Recommended Books:
1. Sung-Mo Kang and Yusuf Leblebici, "CMOS Digital Integrated Circuits Analysis and Design", Tata McGraw Hill, 3rd Edition, 2005.
2. Richard C. Jaeger, Travis N. Blalock, "Microelectronic Circuit Design", McGraw-Hill, 4th edition, 2011.
3. Donald A Neamen , "Semiconductor Physics and Devices", McGraw- Hill, 4th edition, 2011.
4. Michael John, Sebastian Smith, "Application Specific Integrated Circuits", Pearson, 6th edition, 2009.
5. Neil H. E. Weste and Kamran Eshraghian , "Principles of CMOS VLSI design", Pearson, 3rd edition, 2005.
6. Jacob Millman, Arvin Grabel, "Microelectronics", Tata McGraw-Hill, 2nd edition, 1987.

| ECP1206 | Analog & Digital Communication Lab | (0-0-2), 1credit |

Course Learning Outcomes (CLO):

CLO1: The students would have a good understanding of both time and frequency domain representations of information and modulated signals used in analog, pulse and digital communication systems

CLO2: They would be able to evolve functional blocks of Tx and Rx for AM/FM broadcast radio, baseband PCM transmission and digital wireless communication applications.

CLO3: The students would be skilled to evaluate binary and M-ary shift keying digital modulation and demodulation techniques for digital cellular applications

CLO4: They would possess an ability to apply knowledge of various digital modulation schemes to improve performance of advanced digital cellular communication systems.

CLO5: Skill of various schemes to improve performance of communication systems.

The lab work focuses on providing practical knowledge of fundamental concepts of different types of analog, pulse and digital modulation and demodulation techniques used in analog and digital communication systems. The students are also familiarized with MATLAB software tool to simulate amplitude and frequency modulation process. Various experiments to be performed include the following: To generate and demodulate the amplitude modulation signal and plot the waveforms in time-domain and frequency-domain. To generate and demodulate the frequency modulation signal and plot the waveforms in time-domain and frequency-domain. To generate and plot natural sampling. Flat top sampling and sample & hold (PAM) waveforms. To study pulse code modulation (PCM) technique and observe analog signal to digital code conversion procedure.To study delta modulation (DM) techniques and observe the DM noise.To study and obtain modulated and demodulated waveforms of amplitude shift keying (ASK) technique. To study and obtain modulated and demodulated waveforms of frequency shift keying (FSK) technique. To study and obtain modulated and demodulated waveforms of phase shift keying (PSK) technique. To study and obtain Modulated and Demodulated waveforms of Quadrature Phase Shift Keying (QPSK) technique. To study GMSK modulation and demodulation process and observe the process.

Recommended Books:

1. T. L. Singal, 'Analog and Digital Communications' by ISBN: 978-0-07-107269-4, McGraw Hill Education Publications, First Edition Copyright @ 2012, Fifth Reprint 2015.
2. T. L. Singal, 'Electronic Communications' by ISBN: 978-93-82782-16-2, Chitkara University Publications, First edition Copyright @ 2014.
3. B. P. Lathi and Zhi Ding `Modern Digital and Analog Communication Systems' by, Oxford University Press, International 4th Edition Copyright @ 2010.
4. T. L. Singal. Digital Communication' by, ISBN: 978-93-392-1952-9, McGraw Hill Education Publications, First Edition Copyright @ 2015.

ECP2303	Microelectronic Circuits Lab	(0-0-4), 2credits

Course Learning Outcomes (CLO):

CLO1: Students will acquire hands on experience of industry oriented circuit designing tools

CLO2: Students will be skilled to design different digital and analog circuits and verify the same through simulation on cadence design tool.

CLO3: Capable of designing layouts of the designed circuit in accordance with layout design rules during employment

CLO4: Skill of using simulator.

CLO5: Skill of interfacing hardware with software.

Introduction to VLSI design techniques and VLSI design flow for Digital and Analog IC designing. Introduction to IC design flow As we know that NMOS IV curves lie in I quadrant and the curve increase linearly first and then saturates. Verify the same by designing a NMOS circuit & plotting IV characteristics on graph. Identify the best inverter circuit from all design choices. Then design its schematic such that the width of PMOS is 2.5 to 3 times greater than that of NMOS and find the noise margin byplotting VTC & performing DC analysis. The variations in design parameters of MOS have significant effect on the final design. In order to become aware of these changes, perform the parametric analysis for CMOS inverter for different values of W/L. Design a logic function using CMOS and verify its functionality. Design, simulate & calculate noise margin for the Pseudo NMOS inverter by plotting VTC & performing DC analysis. In order to understand the back end design process, perform the complete layout design of CMOS inverter. Also verify the design using DRC & LVS. Design schematic of MOS Current Mirror circuit and perform DC analysis of the designed schematic. Design & Perform the AC analysis of MOS based Amplifiers.

Recommended Books

1. Sung-Mo Kang and Yusuf Leblebici, "CMOS Digital Integrated Circuits Analysis and Design", Tata McGraw Hill, 3rd Edition, 2005.
2. Richard C. Jaeger, Travis N. Blalock, "Microelectronic Circuit Design", McGraw-Hill, 4th edition, 2011.
3. Donald A Neamen, "Semiconductor Physics and Devices", Mc Graw- Hill, 4th edition,2011
4. Neil H. E. Weste and Kamran Eshraghian, "Principles of CMOS VLSI design", Pearson, 3rd edition, 2005.

ECP1315	Microprocessor & Microcontroller Lab	(0-0-2), 1credit

Course Learning Outcomes (CLO):

CLO1: After the completion of this lab course students will be skilled to handle the technical issues during the programming and also able to evaluate possible causes of discrepancy in practical experimental observations.

CLO2: The students will be able to write a program in assembly language to perform the specific task like arithmetic and logical operations, ON/OFF procedure for an LED pattern etc.

CLO3: Student will be able to understand how to Interface the external devices to the controller

CLO4: Skilled according to the user requirements to create novel products and solutions for the real time problems as entrepreneur

CLO5: Skilled to create innovation through microprocessor circuit designing

8085-based experiments: Study of 8085 Microprocessor kit, Addition and subtraction of two 8-bit/ 16-bit numbers, Detection of Even/ Odd numbers, Arranging numbers in ascending/ descending order, Finding 1's and 2's complement of numbers, Multiplication of two 8-bit numbers. Study of 8086 microprocessor kit, addition of two 16-bit numbers with 8086. 8051-based experiments: Introduction to microcontroller kit and Keil software, Introduction to assembly language programming of 8051, simulation of the program for 8051 using keil software, addition of two 8-bit numbers, program to time the events using software delay, Blinking LEDs connected to a port, generate time delay using timers of 8051, display a message on LCD using a look-up table, Transmit and receive a message using serial port of 8051, using the external hardware interrupts of 8051. ATMEGA2560 Micro-controller based experiments: Introduction to ATMEGA2560 architecture, Firebird V platform, AVR Studio and Embedded C programming, Using AVR Bootloader to program Firebird V, A demo buzzer beep application, controlling the motors for moving Firebird V robotic platform in forward, backward, left and right directions, simple I/O operations using buzzer and bargraph LED display, implementing a line follower with real-time sensor reading display on LCD.

Recommended Books:
1. N. Senthil Kumar, M. Saravanan& S. Jeevananthan, "Microprocessors and Micro-controllers", Oxford University Press, 2010.
2. Muhammad Ali Mazidi, Janice GillispieMazidi and RolinD.Mckinlay, "The 8051 Microcontroller and Embedded System", Pearson Education, 2008.
3. Ramesh S. Gaonkar, "Microprocessor Architecture, Programming and Applications with 8085", Prentice Hall, 2002.
4. Douglas V. Hall, "Microprocessors and Interfacing", Tata McGraw-Hill, Revised Second Edition, 2006.

CSL5210	Data Structures	(4-1-0), 5credits

Course Learning Outcomes (CLO):

CLO1: After understanding the basic types for data structure, students will be able to implement different real world applications.

CLO2: Students will be able to determine time and memory complexity of basic algorithm constructs.

CLO3: Implement algorithms for the creation, insertion, deletion, and traversal of each data structure.

CLO4: Skilled to solve problems based on searching and sorting algorithms.

CLO5: Formulate new solutions for programming problems or improve existing code using learned algorithms during their employment.

Introduction:Basic Terminology, Elementary Data Organization, Data Structures and Operations, Algorithms: Complexity, Time-Space Tradeoff, Asymptotic Notations for Complexity (Ω ,θ, O).Arrays: Introduction, Representation of Linear Arrays in Memory, Traversing Linear Arrays, Inserting and Deleting (at Beginning, middle and at the end), Linear and Binary Search with their Complexity, Bubble Sort & its Complexity. Linked List: Introduction & its memory representation, Traversing a Linked List, Insertion into Linked List (sorted and unsorted Linked List), Deleting from Linked List, Operations on Doubly Linked List, Circular linked List & its applications. Stacks and Queues: Array and Linked representation of Stack, Implementation of recursive procedures using Stack, Applications: Arithmetic Expressions, Polish Notation, Transforming Infix Expressions into Postfix ExpressionsArray and Linked representation of Queue, DE Queues, Priority Queues. Sorting Techniques: Quick Sort , Merge Sort , Radix Sort, Selection Sort, Insertion Sort & their complexity. Trees: Binary trees, complete binary trees, Binary Search Trees, Data structures for representing binary trees. Insertion, deletion and searching in these trees, Tree Traversal: Pre-order, In-order, Post-order and their algorithms, Balanced Binary Trees, AVL trees, Insertion and Deletion in AVL tree Heaps: Difference between Heap and Array, Insertion and Deletion in heap, Heap-sort and its applications. Graphs: Basic terminology, directed and undirected graphs, notion of path, Representation of graphs: edge list structures, adjacency list structures, adjacency matrix, Linked List representation of Graph, Operations on Graph, Graph traversals: DFS, BFS. Hashing: Techniques, Collision and its resolving.

Recommended Books:
1. Seymour Lipschutz, Data Structures, Schaums' Outlines Indian Adapted Edition 2006, Tata McGraw-Hill.
2. Tanenbaum, Augenstein, &Langsam, Data Structures using C and C++, Prentice Hall of India, Second edition.
3. RichardGilberg, BehrouzForouzan, Data Structures, Second edition.
4. Horowitz, E., Sahni, S. and Anderson-Freed, S., 1976. Fundamentals of data structures (Vol. 20). Potomac, MD: Computer science press.

| CSP2210 | **Data Structures Lab** | (0-0-4), 2 credits |

Course Learning Outcomes (CLO):

CLO1: After understanding the basic types for data structure, students will be able to implement different real world applications.

CLO2: Students will be able to determine time and memory complexity of basic algorithm constructs.

CLO3: Implement algorithms for the creation, insertion, deletion, and traversal of each data structure.

CLO4: Skilled to solve problems based on searching and sorting algorithms.

CLO5: Formulate new solutions for programming problems or improve existing code using learned algorithms during their employment.

The lab work focuses on providing practical knowledge of algorithms and their implementation. The students solve classical problems practically by implementation of different algorithms as mentioned : Traversal, insertion, deletion and search (linear and binary)operation on a linear array, Sorting of array elements in ascending order using Bubble sort, Doubly or circular linked list: Insert a new element at the beginning ,end and in-between the given list, Delete an existing element, Search an element, Display all the elements, Stack: Push, Pop , Display Top of the stack, use of stack in converting arithmetic expression from infix notation to postfix notation and in evaluating arithmetic postfix expression, Circular queue: Insertion, deletion and traversal algorithms**,** Sorting algorithms: Insertion Sort, Quick Sort, Selection Sort, Merge Sort, Binary search tree : Insert a new element , Delete an existing element, Traversing the tree: Pre-order Traversal, In-order Traversal , Post-order Traversal, Sort the list of integers using heap tree (Heap sort) , Operations on Graph and traversal using DFS and BFS.

Recommended Books:
1. Seymour Lipschutz, Data Structures, Schaums' Outlines Indian Adapted Edition 2006, Tata McGraw-Hill.
2. Tanenbaum, Augenstein, &Langsam, Data Structures using C and C++, Prentice Hall of India, Second edition.
3. RichardGilberg, BehrouzForouzan, Data Structures, Second edition.
4. Horowitz, E., Sahni, S. and Anderson-Freed, S., 1976. Fundamentals of data structures (Vol. 20). Potomac, MD: Computer science press.

GEW1615	Introduction to Power Electronics	(2-0-0), 2 Credits

Course Learning Outcomes (CLO):

CLO1: Relate basic semiconductor physics to properties of power devices, and combine circuit mathematics and characteristics of linear and non-linear devices

CLO2: Skilled to design and Analyze power converter circuits and learn to select suitable power electronic devices by assessing the requirements of application fields

CLO3: Skilled to recognize the role power electronics play in the improvement of energy usage efficiency and the applications of power electronics in emerging areas..

An Introduction to Power Electronics by referencing how it has shaped our lives and as part of an accelerating 21st century revolution. The ways we use energy, the ways we produce it, and the ways in which we process it are changing. A look briefly at energy alternatives and sustainability complete the introductory lecture.Power Calculations w addressed by considering power and energy in switching circuits and then the effects of adding inductors and capacitors. Some basic power definitions and formula including: Average power, Energy recovery and Effective values. The Effect of Heat Sources of heat loss in power semiconductor devices: Forward conduction – this is the major loss at low frequency, Leakage during blockage state, At the gate due to gate signal – negligible and switching losses, which can be significant at higher frequency. Power Semiconductor Devices including: Types and Characteristics and the ideal switch. Such characteristics including: Losses, Ratings and Device Comparisons Device Operation in order to understand classifications of PE circuits and terminologies. These necessitate understanding the operation of pure resistive loads and inductive loads and approaches to Power Electronics devices operation in different circuit topologies .Applications of Devices including common configurations such as: DC to DC Converters, Boost (step-up) converter, Buck-Boost converter and Cuk Converter .Frequency Conversion identifying types of frequency conversion and the study the operation of H-Bridge inverter. Three phase inverter and its applications.A brief introduction to non-sinusoidal components, namely harmonics. Waveform harmonic components & THD, Harmonics at the load, Harmonics at the supply, Filtering and Electromagnetic compatibility (EMC).

Recommended Books:
1. Bimbhra, P.S. and Kaur, S., 2012. Power electronics (Vol. 2). Khanna publishers.
2. Kazmierkowski, M.P., Krishnan, R. and Blaabjerg, F. eds., 2002. Control in power electronics (Vol. 17). New York: Academic press.
3. Rashid, M.H. ed., 2017. Power electronics handbook. Butterworth-Heinemann.
4. Erickson, R.W. and Maksimovic, D., 2007. Fundamentals of power electronics. Springer Science & Business Media.

GEW1810   Introduction to Microcomputer Technology   (2-0-0), 2credits

Course Learning Outcomes (CLO):

CLO1:   Understanding of the layers of the model.

CLO2:   Students will be able to understand the different number systems.

CLO3:   Skilled to implement algorithms.

Introduction, Layer model : From highlevel programming, Language down to transistor level, Instruction set architecture (ISA), Computer arithmetics and ALU
Processor design: datapath and control unit, Basic principles of microcomputers and microcontrollers, Programming and instruction processing,computer architecture, Lecture includes assembler programming (SPIM-simulator), Pipelining, Semiconductor memories, System- and interface units, Microcontrollers Link:Simulation of the MIPS instruction set with SPIM simulator, Spim Version 8 (Windows).

Recommended Books:
1. David Patterson and John Hennessy, Computer Organization and Design, The Hardware/Software Interface, 3rd edition 2007,Morgan Kaufmann, ISBN : 978-0123706065.
2. N. Senthil Kumar, M. Saravanan& S. Jeevananthan, "Microprocessors and Micro-controllers", Oxford University Press, 2010.
3. Muhammad Ali Mazidi, Janice GillispieMazidi and RolinD.Mckinlay, "The 8051 Microcontroller and Embedded System", Pearson Education, 2008.
4. Ramesh S. Gaonkar, "Microprocessor Architecture, Programming and Applications with 8085", Prentice Hall, 2002.

Year 3, Semester-6

Department Elective-I

ECL4316	Wireless & Mobile Communication	(3-1-0), 4 credits

Course Learning Outcomes (CLO):

CLO1: The students would be able to apply the knowledge of mobile communication engineering to solve coverage and call failure problems in cell phones.

CLO2: They would be skilled to implement the cellular concept and antenna system design consideration aspects in optimizing the cellular architecture as per user needs during their employment.

CLO3: The students would possess in-depth knowledge to select and use optimum multiple access technique for interference-free communication.

CLO4: The students would possess an ability and technical skills necessary to understand digital cellular standards and architecture designs.

CLO5: The students would have acquired adequate knowledge about major aspects of 3G/4G digital cellular networks.

Radio Wave Propagation: Basic Propagation Mechanism, Ground wave Propagation, Space wave Propagation: Free Space and Two Ray Point to Point Propagation Models, Sky wave Propagation: Structural details of the Ionosphere, Ray Path, Critical Frequency, MUF, LUF, OF, Virtual Height and Skip Distance. Principles of Cellular Communication: Cellular Terminology, Cell Structure and Cluster, Frequency Reuse Concept, Cluster Size and System Capacity, Method of Locating Co-channel cells, Frequency Reuse Distance, Antennas at cell site, Mobile Antennas, Design of Omni-directional and Directional Antenna Cellular Systems, System parameters to increase cell coverage and capacity, Cell Splitting. Multiple Access Techniques: Introduction, Frequency Division Multiple Access, Time Division Multiple Access, Code Division Multiple Access, Comparison of Multiple Access Techniques. Global System for Mobile (GSM): GSM Network Architecture, Identifiers used in GSM system, GSM Channels, Frame Structure for GSM, GSM Call Procedures – Registration, Mobile to Network call, Network to Mobile call.

Recommended Books:

1. T L Singal, Wireless Communications, ISBN: 978-0-07-068178-1, McGraw Hill Education, First Edition, Copyright © 2010.
2. Andrea Goldsmith, Wireless Communications, ISBN: 978-0-52-170416-8, Cambridge University Press, 2nd Edition, Copyright © 2008.
3. UpenaDalal, Wireless Communication and Networks, ISBN: 978-0-19-809888-1, Oxford University Press, First edition, 2014.
4. William C. Y. Lee, Mobile Communications Engineering, ISBN: 978-0-07-025220-2, McGraw Hill Publication, 2nd edition, 2008.

CSL4207	Operating System	(3-1-0), 4 credits

Course Learning Outcomes (CLO):

CLO1: Identify different types of Operating System and their components.

CLO2: Design and implementation of new system calls for any open source operating system.

CLO3: Implementation of existing resource management algorithms in Linux operating system.

CLO4: Skilled to identify various system security and protection issues.

CLO5: Administer the system using various Operating systems (Windows and Ubuntu) for managing its resources.

Introduction: Concept of Operating Systems, Generations of Operating systems, Types of Operating Systems, OS Services, System Calls, Structure of an OS - Layered, Monolithic, Microkernel Operating Systems, Concept of Virtual Machine. Case study on UNIX and WINDOWS Operating System.Processes: Definition, Process Relationship, Different states of a Process, Process State transitions, Process Control Block (PCB), Context switching Thread: Definition, Various states, Benefits of threads, Types of threads, Concept of multithreads, Process Scheduling: Foundation and Scheduling objectives, Types of Schedulers, Scheduling criteria: CPU utilization, Throughput, Turnaround Time, Waiting Time, Response Time; Scheduling algorithms: Pre-emptive and Non pre-emptive, FCFS, SJF, RR; Multiprocessor scheduling: Real Time scheduling: RM and EDF. Inter-process Communication: Critical Section, Race Conditions, Mutual Exclusion, Hardware Solution, Strict Alternation, Peterson's Solution, The Producer Consumer Problem, Semaphores, Event Counters, Monitors, Message Passing, Classical IPC Problems: Reader's & Writer Problem, Dinning Philosopher Problem etc. Deadlocks: Definition, Necessary and sufficient conditions for Deadlock, Deadlock Prevention, Deadlock Avoidance: Banker's algorithm, Deadlock detection and Recovery. Memory Management: Basic concept, Logical and Physical address map, Memory allocation: Contiguous Memory allocation – Fixed and variable partition– Internal and External fragmentation and Compaction; Paging: Principle of operation – Page allocation – Hardware support for paging, Protection and sharing, Disadvantages of paging. Virtual Memory: Basics of Virtual Memory – Hardware and control structures – Locality of reference, Page fault , Working Set , Dirty page/Dirty bit – Demand paging, Page Replacement algorithms: Optimal, First in First Out (FIFO), Second Chance (SC), Not recently used (NRU) and Least Recently used (LRU). I/O Hardware: I/O devices, Device controllers, Direct memory access Principles of I/O Software: Goals of Interrupt handlers, Device drivers, Device independent I/O software, Secondary-Storage Structure: Disk structure, Disk scheduling algorithms File Management: Concept of File, Access methods, File types, File operation, Directory structure, File System structure, Allocation methods (contiguous, linked, indexed), Free-space management (bit vector, linked list, grouping), directory implementation (linear list, hash table), efficiency and performance. Disk Management: Disk structure, Disk scheduling - FCFS, SSTF, SCAN, C-SCAN, Disk reliability, Disk formatting, Boot-block, Bad blocks

Recommended Books:

1. AviSilberschatz, Peter Galvin,Greg Gagne,Operating System Concepts Essentials, 9th Edition by Wiley Asia Student Edition.
2. William Stallings, Operating Systems: Internals and Design Principles, 5th Edition, Prentice Hall of India.

3. Milenkovic, M., 1992. Operating systems: concepts and design. McGraw-Hill, Inc.
4. Madnick, S.E. and Donovan, J.J., 1974. Operating systems (Vol. 197, No. 4). New York: McGraw-Hill.

ECL4409   **Audio and Speech Processing**   **(3-1-0), 4 credits**

Course Learning Outcomes (CLO):

CLO1: Isolated words and commands recognition.

CLO2: Skilled to recognize continues speech.

CLO3: Skilled to apply Dialogue systems in various applications

CLO4: Skilled to recognize Time and frequency domain text-to-speech synthesis.

Mechanics of Speech, Speech production: Mechanism of speech production, Acoustic phonetics - Digital models for speech signals - Representations of speech waveform: Sampling speech signals, basics of quantization, delta modulation, and Differential PCM - Auditory perception: psycho acoustics; Time domain Methods For Speech Processing, Time domain parameters of Speech signal – Methods for extracting the parameters Energy, Average Magnitude, Zero crossing Rate – Silence Discrimination using ZCR and energy – Short Time Auto Correlation Function – Pitch period estimation using Auto Correlation Function. Frequency Domain Method For Speech Processing, Short Time Fourier analysis: Fourier transform and linear filtering interpretations, Sampling rates - Spectrographic displays - Pitch and formant extraction - Analysis by Synthesis - Analysis synthesis systems: Phase vocoder, Channel Vocoder - Homomorphic speech analysis: Cepstral analysis of Speech, Formant and Pitch Estimation, HomomorphicVocoders. Linear Predictive Analysis Of Speech, Basic Principles of linear predictive analysis – Auto correlation method – Covariance method – Solution of LPC equations – Cholesky method – Durbin's Recursive algorithm – Application of LPC parameters – Pitch detection using LPC parameters – Formant analysis – VELP – CELP. Application Of Speech & Audio Signal Processing, Algorithms: Dynamic time warping, K-means clustering and Vector quantization, Gaussian mixture modeling, hidden Markov modeling - Automatic Speech Recognition: Feature Extraction for ASR, Deterministic sequence recognition, Statistical Sequence recognition, Language models - Speaker identification and verification – Voice response system – Speech synthesis: basics of articulatory, source-filter, and concatenative synthesis.

Recommended Books:

1. R. Rabiner and R. W. Schaffer, "Digital Processing of Speech signals", Prentice Hall, 1978
2. Ben Gold and Nelson Morgan, "Speech and Audio Signal Processing", John Wiley and Sons Inc., Singapore, 2004.
3. Benesty, J., Sondhi, M.M. and Huang, Y. eds., 2008. Springer handbook of speech processing (Vol. 1). Berlin: Springer.
4. Deng, L. and O'Shaughnessy, D., 2003. Speech processing: a dynamic and optimization-oriented approach. CRC Press.
5. Rabiner, L.R. and Schafer, R.W., 2007. Introduction to digital speech processing (Vol. 1). Now Publishers Inc.

CLL3301	Life Skills I	(3-1-0), 4 Credits

Course Learning Outcomes (CLO):

CLO1: Recognize diverse communication styles (body language, tone of voice) and effectively increase comprehension and build rapport with others.

CLO2: Draw comparison and demonstrate communication in a clear and direct manner. To be able to understand words and language used in formal and casual communication in entrepreneurship.

CLO3: Use creative thinking skills to analyze and evaluate issues and arguments, to solve problems, or to make decisions during their employment and entrepreneurship.

CLO4: Understanding a leader's responsibilities to assess the requirements of a task, identifying the strengths within the team, utilizing the diverse skills of the group to achieve the set objective as an entrepreneur and employee.

Grammar Usage- Tenses (Present Tense, Past Tense and Future Tense),Subject Verb Agreement, Modals, Prepositions, Adjectives and Adverbs, Phrasal Verbs, Common Errors in English. Phonetics- Consonants and Vowel sounds, Pronunciation , Diction, Intonation, Word and Sentence Stress, Commonly mispronounced and confusing words Build the Language-Word mapping, Verbal Ability worksheets, Antonyms, Synonyms, Homophones, Idioms and Proverbs, Prefix- Suffix, Crossword Puzzles and Vocabulary Games. Practice the Language-Reading Comprehension Practice, Elevator Speech, Expressing an opinion- Agreement, Disagreement.

Department Elective-II

| **ECL4414** | **Analog Layout Design** | **(3-0-0), 3 credits** |

Course Learning Outcomes (CLO):

CLO1: Enhance the skills of integrated circuit design for designing layouts of complex circuits

CLO2: Students will be able to design layouts using CMOS technology and learn industry related design tools such as Cadence Virtuoso to work as IC design engineer.

CLO3: Skill of applying different matching techniques in layouts of analog circuits and apply those techniques to design high quality and noise tolerant layout

CLO4: evaluate combinational and sequential logic designs using various metrics:

CLO5: switching speed, gate count , and energy dissipation and power

Introduction to CMOS physical design, Introduction to CMOS technology, Important Processes involved in IC fabrication, Fabrication steps of CMOS inverter, Demo of GDS 3D viewer, Introduction to the layout tool, Drawing-related features and functionality of the tool, Live demo of virtuoso layout XL,DRC flow using the tool, LVS flow using the tool, Digital standard cell layouts, Introduction to standard cells, Layout optimization for minimum parasitics and area, Live demo of a NAND/NOR gate layout, Live demo of a decoder layout, Universal gates with LVS and DRC clean, Introduction to basic components, Introduction to various types of resistors & its parameters, BJTs and its parameters Introduction to various types of capacitors & its parameters, MOSFETs parameters and matching, Analog layout concepts, Need & Techniques for Matching: Common centroid, interdigitization(Differential pairs and current mirror circuits),WPE and STI effect, Live demo of a comparator layout, Live demo of OTA layout, Coupling & Shielding, Routing current/ voltage lines, Routing power/ signal lines, ESD & Latch-up, Electro-migration effects and metal width calculations,

Advanced extra topics: Layout automation using PCELLs, Layout challenges in Deep Submicron technologies Advanced DRC rules for process technologies 45 nm and lower, Challenges and Solutions for 14nm Digital Design Reference Flows, FD-SOI Technology.

Recommended Books:
1. Alan Hastings,The Art of Analog layout' by, 2001, ISBN 0-13-087061-7, Prentice Hall
2. R. Jacob Baker,CMOS circuit design, layout & simulation' by 3rd Edition, Wiley
3. Tony Chan Carusone, David A. Johns, Kenneth W. Martin,Analog Integrated Circuit Design' by,2nd Edition, ISBN 978-0-470-77010-8, Wiley
4. Maloberti, F., 2006. Analog design for CMOS VLSI systems (Vol. 646). Springer Science & Business Media.

| ECL4413 | Digital Design using FPGA | (3-0-0), 3 credits |

Course Learning Outcomes (CLO):

CLO1: The students completing this course are expected to understand the structure of various number systems and its application in digital design.

CLO2: Students will be able to design the appropriate truth table from a description of a combinational logic function

CLO3: Students will be skilled to analyze and design various combinational and sequential circuits like Comparators, Multiplexers, Encoders etc.

CLO4: Students will be skilled to design the synchronous circuits like Pulse train generator, Pseudo Random Binary Sequence generator

CLO5: Designing of Pseudo Random Binary Sequence generator

Introduction to PLD, Introduction to FPGA,FPGA Example – Altera Cyclone II, Altera QuartusII,Lab, Overview of the ISE Design ,HDL-Based Design: Overview of HDL-Based Design, Getting Started, Design Description, Design Entry ,Synthesizing the Design Schematic-Based Design: Overview of Schematic-Based Design, Getting Started, Design Description , Design Entry Behavioral Simulation: Overview of Behavioral Simulation Flow, ModelSim Setup, ISim Setup. , Getting Started, Adding an HDL Test Bench , Behavioral Simulation Using Model Sim, Behavioral Simulation Using ISim Design Implementation: Overview of Design Implementation, Getting Started, Specifying Options Creating Timing Constraints, Translating the ,Using the Constraints , Assigning I/O Locations Using the PlanAhead Tool, Mapping the Design, Using Timing Analysis to Evaluate Block Delays After Mapping, Placing and Routing the Design, Using FPGA Editor to Verify the Place and Route, Evaluating Post-Layout Timing, Creating Configuration Data, Command Line Implementation. Timing Simulation: Overview of Timing Simulation Flow, Getting Started, Timing Simulation Using ModelSim Timing Simulation Using Xilinx ISim. Configuration Using iMPACT: Overview of iMPACT Device Support, Download Cable Support ,Configuration Mode Support, Getting Started ,Using Boundary-Scan Configuration Mode , Troubleshooting Boundary-Scan Configuration , Creating an SVF File . HDL and its importance, limitations of HDL, History of HDL, Design methodology, language elements: operators, number formats, identifiers, keywords, gate level modelling, data flow, behavioural modelling Designing of combinational and sequential circuits using symbols: Combinational and Describe the basic slice resources available in Spartan-6 FPGAs, Identify the basic I/O resources available in Spartan-6 FPGAs ,List some of the dedicated hardware features of Spartan-6 FPGAs, Differentiate the Virtex-6 family of devices from the Spartan-6 family, Describe the contents of a Xilinx Targeted Design Platform kit.

Recommended Books:
1. Dubey, R., 2008. Introduction to embedded system design using field programmable gate arrays. Springer Science & Business Media.
2. Wolf, W., 2004. FPGA-based system design. Pearson Education India.
3. Ramachandran, S., 2007. Digital VLSI systems design: a design manual for implementation of projects on FPGAs and ASICs using Verilog. Springer Science & Business Media.
4. Grout, I., 2011. Digital systems design with FPGAs and CPLDs. Elsevier.

| ECL3312 | IOT & Embedded Systems | (3-0-0), 3 credits |

Course Learning Outcomes (CLO):

CLO1: Understand fundamental concepts and technologies related to embedded system and IoT based devices

CLO2: Understand the fundamentals of RTOS and application development techniques.

CLO3: Skill to write fast-executing embedded code that utilizes the CPU, memory and peripheral resources efficiently

CLO4: Understand the various communication and networking protocols used for developing IoT enabled devices during their employment.

CLO5: Applications of Embedded system design.

Introduction to IoT: IoT, Embedded system, Types of Computing platforms, Real life projects demonstration. Introduction to Arduino programing language & Sensor interfacing: Overview of ESP8266 IoT board, Software installation, Led blinking, buzzer interfacing, Digital sensor interfacing (IR sensor & Ultrasonic sensor), Temperature & humidity sensor, Light sensor interfacing, Analog sensor interfacing, Sensor design concept. Sensor Library working. Types of interfaces & Actuator interfacing: Types of interfaces: GPIO, I2C, SPI, USART, USB. Transistor as an amplifier, Transistor as a switch, Relay. Interfacing actuator (12V bulb) using transistor. Display & Bluetooth interfacing: OLED interfacing, Bluetooth interfacing: receive your sensor data on mobile application, Control your devices using smartphone.Mini project: Any application which Integrates all the modules covered during the sessions: sensor, actuator, Bluetooth, display Client- server architecture & Wifi communication: Client-server architecture, HTML basics, programming ESP8266 IoT board as a server & Desktop/Mobile as a Client. Monitor/ control your appliances remotely through Desktop/ mobile using wifi communication. Make your device configurable. Mobile Application development: Android App development using MIT App inventor: getting started with development of a simple app, App for receiving sensor data to the phone via Bluetooth, controlling appliances, getting alerts on critical situations, voice notifications. Text to speech Cloud computing: Introduction to cloud computing, Cloud architecture, Cloud computing models: Deployment Model & Service Model. Introduction to ThingSpeak Cloud platform, uploading sensor data to the cloud, Receiving sensor data from the cloud. IoT Data Communication & Protocols: Data communication layered architecture, TCP/IP Versus IoT Network stack, Request/Response versus Publish/Subscribe architecture, HTTP versus MQTT, MQTT quality of services (QoS0, QoS1, QoS2). Publishing sensor data on cloud(shiftr.io) using MQTT protocol, subscribing sensor data from the cloud using MQTT protocol. ThingSpeak cloud + MQTT: Publishing sensor data on ThingSpeak cloud using MQTT protocol, subscribing sensor data from the ThingSpeak cloud using MQTT protocol. Data analytics on cloud using MATLAB. Integrating Mobile App with cloud Mobile app development to receive data from cloud, app to send data to the cloud. Introduction to the databases, Tiny Database, Firebase database, creating authentication feature, Creating user configurable app.Mini project An application which Integrates all the modules covered during the sessions: Sensor/actuator interfacing, Cloud computing, IoT data communication, Protocols, mobile app development Introduction to Linux: Operating systems basics, Linux, Linux commands, Raspbian OS. Python programming: Software installation,

Introduction to python programming, Operators and data types: Numbers, Lists, Tuples, Strings, Set, Dictionary; Command-line interface and python files, taking user input and displaying output, control structures: if-then, for and while loops; functions. Introduction to Raspberry Pi3& peripheral interfacing: Getting started with Rpi3 board: installing operating system (Raspbian), setting up programming environment, remote login. Actuator interfacing (LED, Buzzer, Pushbutton), Sensor interfacing (DHT11, light sensor, IR Sensor). Introduction to databases: Database concepts, MySQL database, SQL Query processing, MySQL commands, installi MySQL server and client on Raspberry Pi, accessing database from command line, accessing database using python programming. Storing sensor data into database, retrieving sensor data from the database. Uploading sensor data to ThingSpeak cloud from database. IoT Networking: Establishing communication between Rpi3 and ESP8266's IoT board, Installing MQTT broker on Rpi3. Designing ESP8266's IoT board end nodes and Rpi3 as gate way node, Data aggregation and filtration at gateway node.

Recommended Books:
1. Heath, S., 2002. Embedded systems design. Elsevier.
2. Kaelbling, L.P., 1993. Learning in embedded systems. MIT press.
3. Li, Q. and Yao, C., 2003. Real-time concepts for embedded systems. CRC press.
4. Hamblen, J.O. and Van Bekkum, G.M., 2012. An embedded systems laboratory to support rapid prototyping of robotics and the internet of things. IEEE Transactions on Education, 56(1), pp.121-128.

ECL3313	VLSI Design and Verification	(3-0-0), 3 credits

Course Learning Outcomes (CLO):

CLO1: Students will get a clear understanding of VLSI design flow and different types of design styles which are used for integrated circuit design

CLO2: Students will be able to design building blocks of digital IC using different types of modelling styles used in Verilog and perform timing analysis of the blocks

CLO3: Students will acquire skills to identify the faults associated in VLSI circuits and various techniques to test the ICs during employment.

CLO4: Skilled to design building blocks of digital IC using Verilog.

Introduction-IO Statements, Operators and Control Statements, Pointers & Arrays, Pointers,1D & 2D arrays, Dynamic memory allocation, Structures and Recursion, Strings,Structures and Recursion, Basic Digital Design - Logic Gates, Boolean Expression,K-Map Simplification, Understanding Kmap simplification, boolean postulates, optimisation techniques Verilog HDL for Basic Digital designs Introduction to Verilog HDL,types of modelling, Test bench creation Combinational Circuit Design (Digital Design) Solving logical problems through digital design Verilog Design of Combinational circuit Understanding verilog constructs by solving combinational problems Advanced Combinational Design Complex Digital Design Verilog Design of Advanced Combinational circuit Understanding combinatorial loops,design hazards in a design Sequential circuit design Understading of flipflops,shif registers and counters Verilog Design of sequential CircuitsUnderstanding Blocking / non-blocking assignments, Clock/Reset Memory based design & FSM based designDesign of RAM,ROM, Understanding State Machines (Moore,Mealy) Timing Concepts Setup/Hold, Max-Frequency,Clockskew,IO timing FPGA Basics of FPGA, Handson exercises, Basic logic design- FIFO Design Multiclock FIFO Design Arbitration Design of Round Robin Arbiter Register Accessing Designing an register which access data through handshake FSM based packet structuring & clock generation with varying duty cycle Understading the importance of clock in a design Messaging interface Designing an message interface Streaming interface ,Packet Interface Designing a interface to store a variable size packet in memory, Advanced logic design-Multi-clock Designs, Synchronization Methods ,ASIC Design ASIC specific design guidelines, Design for Manufacturability, Clock/Reset Strategy, Die size estimation, Linting Common Design Mistakes, Understanding Lint Errors/Warnings and fixing them Timing Closure STA, Analyzing Timing reports, Fixing Timing issues – Design based/Tool based Area Optimisation Area reduction Methods, RAM based optimization, Using FPGA hard Macros Complex Logic Design Understading the design constructs and procedure to solve complex designs Developing a 4-port Ethernet Switch with FW interface and validating on board, Verfication-Basic design verifcation Basics of Testbench development Using Verilog Verifying FIFO Design Writing testbench module for FIFO design Verifying Round robin Arbitration design Understanding verification concepts Register Accessing Verifying the design FSM based packet structuring & clock generation with varying duty cycle Verifying Messaging interface Streaming interface Verification of Packet Interface System Verilog Language constructs, Practical examples, Assertions System Verilog Tasks and Functions ,UVM methodology Understading Scoreboard, Monitor, Agent, Interface, Transactor, Sequences, Interface Scalable Testbench Development Methodology, Assertions, Assertion for Design, Assertion for Features using System Verilog Coverage driven Verification Randomization, Coverage measurement and strategy Verifying Multi-Port Ethernet Switch Testbench Development, Physical design-Die size estimation Logic/RAM/Hard Macro area

estimation, Area/IO limited Floor Planning Floor-planning Strategy, Using the tool, Handling Hard Macros Equivalence Check Equivalence check concept, Understanding and Fixing errors, Clock/Reset Tree Synthesis ,Clock/Reset Strategy, STA, Strategies for Fixing Timing Errors using the tool,Power Estimation, Power Libraries, Running test for Power estimation, ECO flow, Estimating changes for ECO, Performing ECO changes and doing Equivalence Checks GDS II generation, Projects-Design and verification of a Media processing algorithm core Design and verification of a Network protocol core Design and verification of a Machine learning algorithm.

Recommended Books:
1. Dubey, R., 2008. Introduction to embedded system design using field programmable gate arrays. Springer Science & Business Media.
2. Wolf, W., 2004. FPGA-based system design. Pearson Education India.
3. Pucknell, D.A. and Eshraghian, K., 1994. *Basic VLSI design*. Prentice-Hall, Inc..
4. Gerez, S.H., 1998. *Algorithms for VLSI design automation*. Wiley.

HUL3303	Project Management	(3-0-0), 3 credits

Course Learning Outcomes (CLO):

CLO1: Develop, implement and evaluate various stages including planning, scheduling and Execution of projects.

CLO2: Understand risk management, administration, costing and budgeting challenges during projects improve skill as employee and entrepreneur.

CLO3: Identify project goals, constraints and performance criteria in project implementation in entrepreneurship.

CLO4: Understanding of design process.

CLO5: Application in solving the societal issues

Examining Professional Project Management-Identify Project Management Processes, Identify Professional and Social responsibilities, Identify the Interpersonal Skills Required for a Project Manager. Initiating a Project-,Examine the Project Management Context, Examine Project Selection, Prepare a Project Statement of Work, Create a Project Charter, Identify Project Stakeholders. Planning Project Work-Identify Elements of the Project Management Plan, Document Stakeholder Requirements, Create a Scope Statement, Develop a Work Breakdown Structure, Developing Project Schedules-Create an Activity List, Create a Project Schedule Network Diagram, Estimate Activity Resources, Estimate Duration for Project Activities, Develop a Project Schedule, Identify the Critical Path, Optimize the Project Schedule, Establish a Schedule Baseline, Developing Cost Estimates and Budgets-Estimate Project Costs, Estimate the Preliminary, Cost Baseline, Reconcile Funding and Costs ,Planning Project Quality, Staffing, and Communications- Create a Quality Management Plan, Document the Project Roles, Responsibilities, and Reporting Relationships, Create a Communications Management Plan, Analyzing Risks and Planning Risk Responses-Examine a Risk Management Plan, Identify Project Risks and Triggers, Perform Qualitative Risk Analysis, Perform Quantitative Risk Analysis, Develop a Risk Response Plan, Planning Project Procurement- Plan Project Procurements, Prepare a Procurement Statement of Work, Prepare a Procurement Document, Executing Project Work-Identifying the Direct and Manage Project Execution Process, Execute a Quality Assurance Plan, Acquire the Project Team, Develop the Project Team, Manage the Project Team, Distribute Project Information, Manage Stakeholder Relationships and Expectations. Managing Project Procurement, Identify the Conduct Project Procurements Process, Obtain Responses from Sellers, Determine Project Sellers. Monitoring and Controlling Project Work, Identify the Monitor and Control Project Work Process ,Develop an Integrated Change Control System,Utilize the Integrated Change Control System, Review Deliverables and Work Results ,Control the Project Scope ,Monitoring and Controlling Project Schedule and Costs, Control the Project Schedule, Control Project Costs, Monitoring and Controlling Project Performance and Quality, Perform Quality Control, Report on Project Performance ,Monitoring and Controlling Project Risk and Procurements, Monitor and Control Project Risk, Administer Project Procurements, Closing the Project, Close Project Procurements ,Close the Project or Phase Administratively.

Recommended Books:
1. Burke, R., 2013. Project management: planning and control techniques. John Wiley & Sons.
2. Newell, M. and Grashina, M., 2003. The project management question and answer book. Amacom.

3. Gray, C.F., Larson, E.W. and Desai, G.V., 2006. Project management: The managerial process.
4. Thomsett, M., 2009. The little black book of project management. Amacom.

HUL3302	Digital Marketing and Entrepreneurship	(3-0-0), 3credits

Course Learning Outcomes (CLO):

CLO1: Use confidence acquired in oral and visual presentation skills to sell their ideas

CLO2: Implement personal skills for sales and marketing and work under pressure in entrepreneurship.

CLO3: Develop, implement and evaluate strategies for setting up a business idea in entrepreneurship.

CLO4: Implement personal skills in their employment.

Principles of Digital Marketing: Basics of Marketing, What is Digital Marketing? Comparison of Traditional and Digital marketing. Statistics of Digital Marketing, Benefits of Digital marketing, Latest Digital marketing trends, Digital marketing platforms, Digital Marketing strategy for websites, Career opportunities in digital marketing, Website Designing with WordPress: Types of Websites, Basics of HTML/CSS/JavaScript, WordPress Installation on Server, Understanding the Dashboard, Changing the Default Settings, Installing and customizing themes, Content management in WP, Creating categories, pages, and posts Adding a menu, widgets to the website Installing useful plugins for site features, SEO specific plugins, Search Engine Optimization (SEO): Introduction to SEO, How Do Search engines work? Search Engine Algorithms, Google Algorithm Updates, Google Search Console, Keyword Research Process, Keyword Research Tools, Competition Analysis, On page Optimization strategies, Content development strategy, Title & Meta Tags, Semantic SEO, Rich Snippets Integration, Speed Optimization, Off Page Optimization, Link Building Techniques as per latest standards, Local SEO Strategies, Penguin & Panda update recovery process Reports and SERP Management, Click here for detailed SEO Curriculum, Search Engine Marketing (SEM): Introduction to Paid Marketing, Google Ads (Google AdWords) account and billing settings Types of Campaigns, PPC Campaign Setup, AdGroups and Keywords setup Bidding strategies & Conversion Tracking, AdRank, Quality Score Optimization, Ad Formats & Ad Extensions, Shopping Campaigns, Dynamic search campaigns, Display Ads Campaigns, Remarketing campaigns, Mobile Apps Marketing, Video Marketing, Google Ads (Google AdWords) tools MCC Account, AdWords Editor Tool, 7+ Google Ads certification exam, Social Media Marketing: Introduction to social media marketing, Facebook marketing, Facebook advertising, YouTube marketing, Twitter marketing, LinkedIn marketing, Pinterest marketing, Instagram Marketing Quora Marketing, Document Sharing Site, Click Here for SMM Training Syllabus, Email Marketing: What is Email Marketing, Benefits of email marketing, Basic terminology in email marketing, Email Marketing Softwares,Building email marketing strategy, Building subscriber lists, Designing Newsletters, Types of Campaigns, Reports and analysis.

Recommended Books:
1. Baker, M.J. and Hart, S. eds., 2016. The marketing book. Routledge.
2. Bird, D., 2007. Commonsense direct & digital marketing. Kogan Page Publishers.
3. Reynolds, P.D., 2007. Entrepreneurship in the United States: The future is now (Vol. 15). New York: Springer.
4. Lundström, A. and Stevenson, L., 2005. Entrepreneurship policy: Theory and practice (Vol. 9). New York: Springer.

ECL4311/ECP2313	**VLSI Design/VLSI Design Lab**	**(3-1-4), 6credits**

Course Learning Outcomes (CLO):

CLO1: Students will get a clear understanding of VLSI design flow and different types of design styles which are used for integrated circuit design

CLO2: Students will be able to design building blocks of digital IC using different types of modelling styles used in Verilog and perform timing analysis of the blocks

CLO3: Students will acquire skills to identify the faults associated in VLSI circuits and various techniques to test the ICs during employment.

CLO4: Skilled to design building blocks of digital IC using Verilog

Historical Perspectives, Flow of circuit design procedure, VLSI Design Flow, VLSI Design Styles, Design Quality, Introduction to Verilog, Verilog data types, system tasks, compiler directives, Modules definition and Ports declaration, Gate-Level Modeling, Rise, fall, turn-off delays, Min, Max, and typical delays. Dataflow Modeling, Behavioral Modeling, Design using Algorithmic State Machine Charts..

Recommended Books:
1. SamirPalnitkar,`Verilog HDL Guide' by Pearson, 2nd Edition,2001.
2. Sung-Mo Kang and Yusuf Leblebici, CMOS Digital Integrated Circuits Analysis and Design' by Tata McGraw Hill Publication, 3rd Edition, 2005.
3. Bushnell and Aggarwal, 'Essentials of Electronics Testing for digital memory & mixed signal VLSI Circuits' by Kluwer Academic Publishers, 1st Edition, 2002.
4. J. Bhaskar, 'Verilog HDL synthesis: A Practical Primer' by Star Glaxy Publishing, 2nd edition , 1998.

GTI4301	Numerical Ability & Logical Reasoning	(3-1-0), 4credits

Course Learning Outcomes (CLO):

CLO1: Enhance the mental and Intellectual ability and critical thinking of the students.

CLO2: Enhance the student's ability to use numerical data as a tool to make reasonable decisions and solve problems.

CLO3: Skilled to interpret, analyze and draw logical conclusions based on numerical data presented in graphs and tables.

CLO4: Draw logical conclusions

CLO5: Skilled in problem solving strategies.

VEDIC MODULE: Square and Square + Introduction with aptitude , Cube and cube root, Division, Addition and Subtraction + Basic Trick, Algebric formula base, questions +Series(No.),Rec. Numbers + Approximation, Number System Module: Number System – 1, Number System – 2,H.C.F & L.C.M – 1,H.C.F & L.C.M – 2,Average (Basic), Average(Tricks), Ratio Module: Ratio (Basic), Ratio (How to Balance a Ratio and Tricks), Ratio (Type of Question), Problem on Ages (Basic + Questions), Partnership (Basic + Questions), Allegations Part -1 (Basic Formula), Allegation (Type of Questions), Percentage Module: Introduction to Percentage, Percentage (inc. and dec.) + Population problem +Voting problem, Percentage (%Table +Questions) + Book Questions, Simple Interest (Introduction +T.E.R), Simple Interest (Type of Questions), Simple Interest (Problems), Compound Interest (Introduction to Basic), Compound Interest (Type of Questions) + Problem discussion +Installment, Profit and Loss (Basic), Profit and Loss (Type of Questions), Discount, Work and Time Module: Work and Time (Basic) Work and Time (Part – 2), Work and Time (Part – 3), Work and Wages, Pipes and Cistern (Part -1), Pipes and Cistern (Part – 2),Time Speed and Distance Module: TSD Part – 1 – Basic, TSD Part – 2 – Type of Questions, TSD Part – 3 –Problems, The Train – Part – 1 – Basic, The Train – Part – 2 – Type of Questions + Problems, Boat and stream – Part – 1, Boat and stream – Part – 2, Permutation and Combination Module: P and C Introduction ([or] and [and]) P and C Part – 2 Type of Questions, P and C Part – 3 Problems, Problems, D.I and D.S Module: D.I Simple Questions (Tables) D.I (Pie Chart) D.I (Mix Graph) Geometry Module: Introduction (Lines, Angles, Pt., Angle System), Type of Similarity and Congru, Properties of Quadrilateral and its properties, Circle and its properties Centres and their properties, Mix Questions, Coordinate Geometry, 2 D Figures, 3 D Figures, 2 D and 3 D figures(mix diagrams), Algebric Module Introduction to formula, Types of Questions, Substitute Method, Problems + Line System, Remainder thth Module, Basic Question, Wilsens and formetsthth, Cyclocitythth + Problems, Reasoning, Distance and Direction, Blood Relation (Introduction), Analogy and Venn diagram, Syllogism and Classification and Mathematical operation, Coding – Decoding, and Alphabet Test, Problem on Ages and dictionary, Series Cube and Dice and Missing number, Ranking, Clock, and Calendar, Inequalities and I/P and O/P, Puzzle, Sitting Arrangement, Statement –Argument, Statement- course of Action, Non-verbal (misc)full.

Recommended Books:

1. Copyrighted issue of book by Rishi Gurukul is distributed among students.
2. R.S.Aggarwal, Donald Quantitative Aptitude & Verbal – Nonverbal Reasoning by,Quantum Cat by Arihant Publications.
3. Carter, P., 2009. The complete book of intelligence tests: 500 exercises to improve, upgrade and enhance your mind strength. John Wiley & Sons.
4. Sinha, N.K., 2012. The Pearson Guide to Verbal Ability and Logical Reasoning for the CAT. Pearson Education India.

Open Elective-I

CSL4203	Computer Networks	(3-1-2), 5 credits

Course Learning Outcomes (CLO):

CLO1:   Understand the small networks by following the top-down approach from application to physical layer.

CLO2:   Acquire theoretical knowledge about the different network technologies

CLO3:   Understand the functioning of different layers in OSI model and TCP/IP .

CLO4:   Identify various system security and protection issues.

CLO5:   Skilled to administer the system for managing its resources.

Introduction:Data Communications,   Network criteria, Physical topology, Categories of networks, Protocols and standards, Network Models – Layered Tasks, The OSI model, Layers in the OSI model, TCP/IP protocol suite, Addressing: Physical addresses, logical addresses, port addresses, specific addresses. Transmission impairments, Data Rate limits, Performance, Transmission Media: Guided Media, Unguided Media: wireless Switching: Circuit switched networks, Datagram networks, virtual circuit Networks. Data Link Layer - framing, Character stuffing, bit stuffing, Error Detection and Correction (CRC, Hamming Code, Parity Bit, checksum) Data link protocols -simplest, stop-and-wait protocol, Sliding window protocols- 1-bit sliding window protocol, go back-n, selective repeat protocol, piggybacking, : Channel allocation, Multiple access protocols: random access (Aloha, Pure aloha, slotted aloha), controlled access (reservation, polling, token passing), Wired LANS- standard Ethernet, Wireless LANs, Bluetooth, IPv4 addresses (IP protocol, IP addresses, Subnets, NAT) , IPv6 addresses, Routing protocols: delivery, forwarding, Unicast routing protocols (optimization, intra and inter domain routing, distance vector routing, link state routing, path vector routing), Process to process delivery, Process to process delivery UDP (user datagram, checksum, UDP operation) , Process to process delivery TCP (TCP services, TCP features, TCP connection), Congestion and Congestion control. DNS, Electronic Mail and File Transfer, HTTP, WWW, TELNET.

Recommended Books:
1. B. Forouzan, 'Introduction to Data Communications and Networking' by Tata McGraw Hill, Fourth Edition, 2004
2. Tanenbaum, A.S. and Wetherall, D.J., 1996. Computer networks. Prentice-Hall international editions,
3. Wellman, B., 2001. Computer networks as social networks. Science, 293(5537), pp.2031-2034.
4. Peterson, L.L. and Davie, B.S., 2007. Computer networks: a systems approach. Elsevier.

CSA4120	Introduction to CCNA Routing and Switching	(3-1-2), 5 credits

Course Learning Outcomes (CLO):

CLO1: Understand different topologies and small networks by following the down-top approach from physical layer to application layer.

CLO2: Formulate functioning of different protocols (e.g. IP, TCP, UDP, WWW, http, email, DNS) of layered networking model.

CLO3: Analyze basics concepts of routing, switching, and advanced technologies.

CLO4: Students will be able to design simple networks using the application-driven paradigm helps in their employment

CLO5: Skilled to simulate and design network.

Introduction: uses of Computer networks, Network hardware, Network Software Configuring network operating system: IOS boot camp, basic device configuration, address schemes, Seven-Layer OSI architecture of ISO, Concepts of Layer Protocols and Layer interfaces TCP/IP reference model, comparison of OSI and TCP/IP reference models, Physical Layer: Transmission Media, Wireless Transmission, Data Link Layer: data link layer protocols- Media access control ,Ethernet protocols, Ethernet MAC address, LAN switches- working, switch forwarding methods, Address resolution protocol (ARP). Network layer: network layer protocols i.e IPv4 and IPv6, routing(routing tables) , routers, configuration of a routerIP addressing: IPv4 Network Addresses-structure and characterstics, IPv6 network addresses, connectivity verification, Subnetting IP networks: Subnetting an IPv4 Network, Addressing Schemes, Design Considerations for IPv6Transport Layer: transport layer protocols-TCP and UDP, communicationprocess of TCP and UDP, comparison of TCP and UDP Application Layer: Introduction, application layer protocols, well known application layer protocols and services- web and mail protocols(HTTP, HTTPS, email, SMTP, POP, IMAP), IP addressing services (DNS, DHCP), File sharing services(FTP, SMB) Building small Networks: Network Design: Protocols and devices used, Network Security.

Recommended Books:
1. Todd Lammle, 'CCNA Cisco Certified Network Associate Study Guide', by Wiley, 6th edition
2. Andrew S. Tanenbaum, 'Computer Networks' by Pearson Education, Fourth Edition.
3. CCNA Routing and Switching 200-125 official cert guide
4. James Kurose and K.W. Ross, 'Computer Networking: A Top-Down Approach', by Pearson.
5. Peterson, L.L. and Davie, B.S., 2007. Computer networks: a systems approach. Elsevier.

| CSA3211 | Data Analytics | (3-1-2), 5 credits |

Course Learning Outcomes (CLO):

CLO1: Apply knowledge of dispersion on grouped and ungrouped data cases.

CLO2: Evaluate discrete and continuous probability distributions to various business problems in entrepreneurship.

CLO3: Perform Test of Hypothesis as well as calculate confidence interval for a population parameter.

CLO4: Calculate confidence interval for a population parameter.

CLO5: Application and its utilization.

Introduction to SAS and SAS Programs, What is Analysis and Analytics, What is SAS and Why SAS Overview of SAS Submitting a SAS Program, SAS Program Syntax, What is Analysis and Analytics, What is SAS and Why SAS Overview of SAS, Submitting a SAS Program, SAS Program Syntax, Sorting and Grouping, Reporting Data, Using SAS Format, Reading SAS Datasets, Reading Excel Data, Reading Raw Files, Reading Database Data, Creating Summary Reports, Combining Datasets, Creating Summary Reports, Combining Datasets, Accumulating total, Creating Accumulating total for a group of data, Transposing Data, Debugging Data, Reading Formatted input, Transposing Data, Debugging Data, Reading Formatted input, Do Loop Processing, Conditional Do Loop Processing, SAS Array Processing, Using SAS Arrays, Overview of SAS Foundation, Purpose of the Macro Facility, Program Flow, Introduction to Macro Variables, Automatic Macro Variables, User Defined Macro Variables, Macro Variable Reference, Introduction to Macro Variables, Automatic Macro Variables, User Defined Macro Variables, Macro Variable Reference, Creating Macro Variables in the Data Step, Indirect Reference to Macro Variables ,Creating Macro Variables in SQL, Macro Programs, Conditional Processing, Parameter Validation, Iterative Processing, Introduction to SQL, Overview of SQL Procedure, Specifying Columns, Specifying Rows, Presenting Data, Summarizing Data, Inner Join, Outer Join, Complex SQL Joins, The Union, Outer Union, Except, Intersect Operator, Creating Tables and Views.

Recommended Books:

1. Cody, R.P. and Smith, J.K., 1987. Applied statistics and the SAS programming language. North-Holland Publishing Co..
2. Burlew, M.M., 2014. SAS macro programming made easy. SAS institute..
3. Spector, P.E., 2001. SAS programming for researchers and social scientists. Sage.Jackson, S.
4. Wicklin, R., 2013. Simulating data with SAS. SAS Institute.

CSL5349	Essential Programming Concepts	(3-1-2), 5 credits

Course Learning Outcomes (CLO):

CLO1:   Students will gain an in-depth knowledge about overall syntax and semantics

CLO2:   Students will be skilled to use an IDE to compile, load, save, and debug a program

CLO3:   Students will develop technical thinking and problem solving ability to find an appropriate solution for a problem.

CLO4:   Students will be able to demonstrate the ability to create test cases to determine that a solution produces expected outputs for given inputs

CLO5:   Incorporation of programming in real time applications

Module-1: Structure of a c program, Writing C program, Compilation, Linking & Execution, Using comments , Identifiers: Nomenclature of an Identifier, Variables, Constants, Reserved Keywords Data Types: Introduction , Initialization and Declaration of Data Type, Expressions, Statements, Symbolic Constants, Type Conversion / Type Casting, Input Output in C: Introduction, scanf(), printf( ), Operators : Operations: Arithmetic, Relational, Logical, Assignment, Conditional, sizeof , Precedence. Decision Control Construct :Conditional Statements: if , if – else, Nested if –Else, switch, conditional operator, Looping: Types of Loops: while, do – while, for ,Nested loops, Continue, break. Functions: User defined functions, Recursion , Storage classes, Arrays: Introduction, Need & Importance, Types of Arrays: One Dimensional Arrays, Two Dimensional Arrays, Initialization of arrays ,inputingvalues ,assigning Values, Multi-Dimensional Arrays , Declaration of an Array , Initialization of an Array, Passing 1d to Function, passing two dimensional array to function, Sparse Matrix, Strings : Reading and writing   strings   String   functions   (Predefined),   isalpah(),isdigit(),isspace() strcat(),strncat(),strcpy(),strncpy(),strlen(),strncpy, Implementing user defined functions for Strcpy,strlen,strcmp,strlwr,strupr,strcat, Pointers, Introduction to pointer :Pointer expression and pointer arithmetic Assignment, Value finding (dereferencing),Taking a pointer address, Adding an integer to a pointer, Incrementing a pointer, File Handling : File pointer, open file, close file Read data from file, fgetc(), fgets(), fscanf(), fprintf()   , writing data from a file, fputc(),fputs(),fprintf(),fwrite() ,Difference between Text Mode, Binary Mode, Detecting End-of-file , Accepting command line arguments, Functions for selecting record randomly fseek(), ftell(), rewind(), Difference between Text Mode, Binary Mode, Detecting End-of-file Accepting command line arguments ,Functions for selecting record randomly fseek(), ftell(),rewind(). PreProcessor Directive And Revision, Structure : Declaring Structure Accessing members of Structure, Copying Structure Accessing Structure elements, Nested Structure Array of structure, passing structure elements to a function individually Passing entire structure to a function. Union: Union Accessing member of Union Unions Inside structure ,Pointer to structure ,Passing pointer of structure to function, Pointers and strings Passing pointer to a function, Representing arrays as pointer, Arrays of pointers, Null pointers, Generic pointer ,Dynamic Allocation of Arrays, Allocating block of memory ,Releasing the used block, To Alter the size of allocated memory ,Allocating memory to single dimensional array, Allocating memory to two dimensional array, PreProcessor Directive And Revision.Module-2: Introduction to object-oriented programming: Properties of Object –Oriented Programming, Advanced C++ fundamentals: bool data type, namespaces, Relation among structures, unions and Classes, Concept of Public, protected and

Private, Concept of Constructors, types of constructors: Default, Parameterized and Copy Constructors, Destructors. Classes and Functions: Friend Functions, Friend Classes, Inline Functions , Const Objects and const Member Functions, Static Class Members, Scope Resolution operator, nested classes, local classes, passing object to function, returning object, Object assignment. Arrays, Pointers and References: Arrays of Objects of Class, Pointers as Data Members and class variables, The Size of a Class Object , Passing array of objects to functions, this pointer, References, Passing reference to objects and returning references. Dynamic Memory Management in C++: Dynamic memory management - new and delete Operators, Allocating objects and arrays at runtime, Possible problems with the use of pointers - Dangling/wild pointers, Null pointer assignment, Memory leak and allocation failures Function Overloading: Function Overloading, Overloading Constructor, Copy Constructors, Default Function Arguments. Operator Overloading: Creating a member operator function, Overloading the Assignment Operators, unary and binary Operators, Operator overloading using friend function, Type Conversions , Overloading Operators new and delete, and some special operators ([ ],( ),->,comma) Virtual Functions and Polymorphism: Understanding Polymorphism, Base Class Pointer, Virtual Function, Pure Virtual Functions, Abstract Classes, Virtual Destructors, Early vs Late binding. Inheritance: Defining derived classes, Types of inheritance, Changing the Access Specification of Inherited Members, Multiple Inheritance, Multilevel inheritance, Inherited Member Ambiguity, Virtual Base Classes, Constructors in derived classes, Nesting of classes. Templates: Introduction to Generic Functions and Classes, Overloading a generic function and function template, applications of function and class template. Exception Handling: Understanding type of Exceptions, Throwing mechanism, Catching mechanism, Rethrowing an exception, Applying exception handling. C++ Console I/O and File I/O: Stream Classes, Input/ Output using Overloaded operators >> and << and Member functions of I/O stream classes, Formatting Output, Formatting using ios class functions and flags, Formatting using manipulators. File Streams, different methods of file data
reading and writing, Accessing records randomly. Standard Template Library: Introduction to the STL Architecture, STL Components, Containers, Algorithms, Iterators, Applications of Container Classes, Using the vector Container, Accessing Elements in a vector Container and Operations on a vector Container.

Recommended Books:
1. King, K.N., 1996. C programming: a modern approach. WW Norton & Co., Inc..
2. Oualline, S., 1997. Practical C programming. " O'Reilly Media, Inc.".
3. Dos Reis, G. and Stroustrup, B., 2006. Specifying C++ concepts. ACM SIGPLAN Notices, 41(1), pp.295-308.
4. Horstmann, C.S., 1997. Computing concepts with C++ essentials. John Wiley & Sons, Inc.

CSL4318	Advanced Programming Concepts	(3-1-2), 5 credits

Course Learning Outcomes (CLO):

CLO1: Students will gain an in-depth knowledge about overall syntax and semantics of C programs

CLO2: Students will be skilled to use an IDE to compile, load, save, and debug a C program

CLO3: Students will develop technical thinking and problem solving ability to find an appropriate solution for a problem.

CLO4: Students will be able to demonstrate the ability to create test cases to determine that a solution produces expected outputs for given inputs

CLO5: Incorporation of programming in real time applications

Structure of a c program, Writing C program,Compilation, Linking & Execution Using comments Identifiers: Nomenclature of an Identifier, Variables, Constants Reserved Keywords Data Types: Introduction   Initialization and Declaration of Data Type, Expressions, Statements, Symbolic Constants, Type Conversion / Type Casting, Input Output in C: Introduction, scanf(), printf( ) Operators   Operators Operations: Arithmetic, Relational, Logical, Assignment, Conditional, sizeof,Precedence Decision Control Construct Conditional Statements: if , if – else, Nested if – Else switch, conditional operator Looping Types of Loops: while, do – while, for Nested loops Continue,break Functions Userdefined functions Recursion Storage classes Arrays: Introduction, Need & Importance Types of Arrays: One Dimensional Arrays ,Two Dimensional Arrays, Initialization of arrays, inputing values, assigning Values Multi-Dimensional Arrays Declaration of an Array Initialization of an Array Passing 1d to Function, passing two dimensional array to function.Sparse Matrix Strings Reading and writing strings String functions (Predefined) isalpah(),isdigit(),isspace() strcat(),strncat(),strcpy(),strncpy(),strlen(),strncpy Implementing user defined functions for Strcpy,strlen,strcmp,strlwr,strupr,strcat Arrays of strings. Pointers Introduction to pointer Pointer expression and pointer arthimetic Assignment, Value finding (dereferencing),Taking a pointer address, Adding an integer to a pointer, Incrementing a pointer Pointers and strings Passing pointer to a function, Representing arrays as pointer,Arrays of pointers,Null pointers,Generic pointerDynamic Allocation of ArraysAllocating block of memoryReleasing the used blockTo Alter the size of allocated memoryAllocating memory to single dimensional arrayAllocating memory to two dimensional arrayStructure –IDeclaring StructureAccessing members of StructureCopying StructureAccessing Structure elementsNested StructureArray of structure,passing structure elements to a function individually. Passing entire structure to a functionUnionAccessing member of UnionInside structurePointer to structurePassing pointer of structure to functionFile HandlingFile pointer,openfile,close fileRead data from filefgetc(),fgets(),fscanf(),fprintf()writing data from a filefputc(),fputs(),fprintf(),fwrite()Difference between Text Mode, Binary Mode,Detecting End-of-fileAccepting command line argumentsFunctions for selecting record randomlyfseek(),ftell(),rewind()Difference between Text Mode, Binary Mode,Detecting End-of-fileAccepting command line argumentsFunctions for selecting record randomlyfseek(),ftell(),rewind()PreProcessor Directive And Revision.

Recommended Books

1. V. Rajaraman, 'Fundamental of Computers', Prentice Hall India Pvt., Limited
2. Joyce Farrell, 'Programming Logic and Design Comprehensive', Sixth Edition, Cengage Learning.
3. Jeri R. Hanly 'Problem Solving and Program Design in C, 5/E', , Pearson Education India.
4. Reema Thareja, Programming in C, 2nd Edition, Oxford University Press

CSL4320	Advanced Object Oriented Programming	(3-1-2), 5 credits

Course Learning Outcomes (CLO):

CLO1: Students will gain an in-depth knowledge about overall syntax and semantics of C++ programs

CLO2: Students will be skilled to use an IDE to compile, load, save, and debug a C++ program

CLO3: Students will develop technical thinking and problem solving ability to find an appropriate solution for a problem.

CLO4: Students will be able to demonstrate the ability to create test cases to determine that a solution produces expected outputs for given inputs

CLO5: Incorporation of programming in real time applications

C++ Fundamentals: iostream Namespace Use of c in and c out Comments Declaring Variables Scope Arithmetic operators and operator Precedence Increment ,Decrement operators Control Structure(Selection And looping) Relational operator Logical operators if.else Switch While For Do.while Break Continue Arrays Declaring arrays Initialisation Accessing an arrays Multi-Dimensioned arrays User defined functions(i)Reference variable Call by value Call by reference Functions(ii)Static variable, global variable Function overloading Default arguments Classes And Objects Defining data members ,member functions Defining member functions outside class Making it inline Classes And Objects IINesting of member functions Private member function Static data members Static member functions Arrays of objects Const member functions Friend functions and Friend classes Constructor And Destructor Parameterized constructor Default constructor Constructor with default arguments Declaring pointers Use of new and delete Copy constructor Destructor Operator Overloading Overloading unary operator Overloading binary operator Type conversion String Object Creating String Objects Manipulating String Objects Relationsal operators String Characterstic Inheritance (I)Base Class And Derived Class Public, private and protected data members Constructor And Destructor In derived class Public ,private And protected Inheritance Inheritance(ii) Polymorphisim Invoking Base class Objects with Derived class Aiming derived class pointers at Base class Objects Derived class member function call via base class Pointers Virtual Functions Abstract class And Pure Virtual Functions Dynamic Binding Templates Function templates Class templates Non Type Paramaters and default type for class type Exception Handling When to use Exception Handling Rethrowing an Exception Stack Unwinding Processing new failure Class auto_ptr and Dynamic Memory Allocation Exception Handling Basics of exception handling Throwing Mechanism Catching Mechanism Rethrowing an exception File Handling Creating A Sequential File Reading data From Sequential File Random Aceess file Introduction To STL Components of STL Containers Algorthim Interators Application of Container Classes Shared Pointersm Auto_ptr Memory Leak in C++Function pointers Pointers vs references.

Recommended Books:
1. E Balagurusamy, Object Oriented Programming with C++, 4th Edition, Tata McGraw Hill.
2. Robert Lafore, Object Oriented Programming in C++, Third Edition, Galgotia 2008.
3. Herbert Schildt, The Complete Reference C++, Second edition, Tata McGraw Hill.
4. Stroustrup, Bjarne, The C++ Programming Language, Pearson Education.

| CLP2305 | **Industry Interface** | **(0-0-4), 2 credits** |

Course Learning Outcomes (CLO):

CLO1: Recognize diverse communication styles (body language, tone of voice) and effectively increase comprehension and build rapport with others.

CLO2: Draw comparison and demonstrate communication in a clear and direct manner. To be able to understand words and language used in formal and casual communication in entrepreneurship.

CLO3: Use creative thinking skills to analyze and evaluate issues and arguments, to solve problems, or to make decisions during their employment and entrepreneurship.

CLO4: Understanding a leader's responsibilities to assess the requirements of a task, identifying the strengths within the team, utilizing the diverse skills of the group to achieve the set objective as an entrepreneur and employee.

Self Awareness, Stages of learning, SWOT analysis, Goal setting, Grooming & Body Language, Power dressing, Work Ethics, Values & Moral, Interpersonal Skills & Empathy, Leadership skills, Presentation Skills, Report writing, Team work, Interview Skills – Acing the Interview, Stress Interviews, Panel Interviews, Cracking Group Discussions, Stress Management ,Anger management , Critical Thinking, Time Management, Conflict Resolution, Resume Making

HUL2401	Cyber Security	(2-0-0), 2credits

Course Learning Outcomes (CLO):

CLO1: Acquire Information and risk models including confidentiality, integrity and availability

CLO2: Acquire skill to identify the Threats and attacks and exploit vulnerabilities

CLO3: Gain suffiicent knowledge on Cyber security architecture and operations and acquire ability to handle the threats during employment

Introduction to Security: Security principles, threats and attack techniques Basics of Cryptography: Cryptographic mechanisms ,Classical Encryption Techniques Symmetric and Asymmetric cryptography (basics) Introduction to cybercrime, cybercrime and information security, Classifications of cybercrimes Cybercrime and the Indian ITA 2000, Cyber offenses: Introduction, How criminals plan the attacks? Botnets- The fuel for cybercrime. Phishing, Password cracking, key loggers and sql injection, attacks on wireless networks. Cost of cybercrimes and IPR issues: lessons for organization, web threats for organization, security and privacy implications from cloud computing, social media marketing: security risks and perils for organizations, social computing and the associated challenges for organizations, protecting people's privacy in the organization, organizational guidelines for internet usage, safe computing guidelines and computer usage policy, incident handling: an essential component of cyber security. Forensics: Best practices for organizations, Media and Asset Protection, Importance of endpoint security in organizations, cybercrime and cyber terrorism: social, political, ethical and psychological dimensions, introduction ,intellectual property in the cyberspace ,the ethical dimensions of cybercrimes ,the Psychology, mindset and skills of hackers and other cybercriminals. Cybercrime: Illustrations, Examples and mini cases, Illustrations of financial frauds in cyber domain, digital signature related crime scenarios.

Recommended Books:
1. Nina Godbole, Sunit Belapure, Cyber Security, Wiley India Pvt. Ltd.; 2011
2. Dieter Gollmann, John Wiley & Sons, ISBN: 470-86293-9; 2006
3. William Stallings, Network Security Essentials, 4th Edition, Pearson Publication
4. Bruce Schneier, Applied Cryptography, Wiley & Sons; Edition 2001.

Year 4, Semester-7
Department Elective-III

CSL3308	Artificial Intelligence and Expert System	(3-0-0), 3 credits

Course Learning Outcomes (CLO):

CLO1: Students will be skilled to apply problem solving techniques associated with artificial intelligence

CLO2: Apply predicate logic and fuzzy logic to represent system in artificial intelligence.

CLO3: Skilled to solve techniques associated with artificial intelligence

CLO4: Skilled to represent fuzzy logic to represent system in artificial intelligence

CLO5: Skilled to solve intelligence expert system.

Introduction: Artificial Intelligence and its applications, Artificial Intelligence Techniques, criteria of success. Intelligent Agents: Intelligent Agents, Nature and structure of Agents, Learning Agents. Problems, Problem Spaces &Search: Defining problem as a space, State space search, production system, control strategies, heuristic search, problem characteristics, production system, characteristics, issues in the design of search programs. Heuristic Search Techniques: Generate and test, Hill climbing, best first search, A* search, Constraint satisfaction problem, Means-end analysis. Knowledge representation: Representations & Mappings, Approaches to knowledge representation, procedural vs declarative knowledge, Forward vs. Backward reasoning, Matching, conflict resolution.

Predicate Logic: Logic representation, Propositional logic - statements, variables, symbols, connective, truth value, contingencies, tautologies, contradictions, antecedent, consequent, argument; Predicate logic - expressions, quantifiers, formula; Representing "IsA" and "Instance" relationships, computable functions and predicates; Resolution. Symbolic reasoning under uncertainty: Introduction to Non-monotonic reasoning, Logic for Non-monotonic reasoning, Depth first search, Breadth first search. Statistical Reasoning: Probability and Baye's theorem, Bayesian networks, Fuzzy logic. Natural Language processing: Introduction, Syntactic processing, Semantic and pragmatic analysis.Expert system Introduction, Knowledge acquisition, Knowledge base, working memory, Inference engine, Expert system shells, Explanation, Application of expert systems.

Recommended Books:
1. Kevin Night and Elaine Rich, Nair B., "Artificial Intelligence (SIE)", McGraw Hill- 2008.
2. Ela Kumar, Artificial Intelligence I. K. International Pvt Ltd.
3. Stuart Russel and Peter Norvig "AI – A Modern Approach", 2nd Edition, Pearson Education 2007.
4. Peter Jackson, "Introduction to Expert Systems", 3rd Edition, Pearson Education, 2007.
5. Deepak Khemani "Artificial Intelligence", Tata McGraw Hill Education 2013.

ECL4404	Digital Image Processing	(3-0-2), 4 credits
ECP1404	Digital Image Processing lab	

Course Learning Outcomes (CLO):

CLO1: After the completion of the course student will be able to understand the fundamental concepts of a digital image processing system like Image formation, Image sampling and quantization

CLO2: Students will develop the knowledge to analyze the different images in the frequency domain using various transforms

CLO3: Students will be skilled to realize the importance of filters for the images and also they will be able to differentiate between the different types of filters.

CLO4: Applications of image processing in recognition

CLO5: Applications in real-time problem solving

Fundamental steps in Digital Image Processing, Components of digital image processing system, elements of visual perception, Structure of the human eye, Image formation in the eye, Simple image formation model, Image Sampling and Quantization, Basic relationship between pixels, Linear and Non-Linear operations, Gray level transformations, Piecewise linear transformation, Histogram processing, enhancement using Arithmetic/ logic operations, Basics of spatial filtering, Smoothing and sharpening spatial filters, Use of first order and second order derivative in enhancement, Fourier Transform and the Frequency Domain, Basis of Filtering in Frequency Domain, Filters – Low-pass, High-pass; Correspondence Between Filtering in Spatial and Frequency Domain; Smoothing Frequency Domain Filters – Gaussian Lowpass Filters; Sharpening Frequency Domain, Filters – Gaussian High pass Filters; Homomorphic Filtering, Basic Morphological Operations, Dilation, erosion, Opening & Closing, morphological Algorithms – Boundary Extraction, Region Filling, Extraction of Connected Components, Convex Hull, Thinning, Thickening, Introduction to Image Segmentation, Detection of discontinuties, Pixel-Based Approach, Multi-level Thresholding, Local Thresholding, Region-based Approach, Edge and Line Detection: Edge Detection, Edge Operators, Thresholding, Basic global thresholding, Adaptive thresholding, Region based segmentation, region growing, splitting and merging,A Model of Restoration Process, Noise Models, Restoration in the presence of Noise only-Spatial, Filtering – Mean Filters: Arithmetic Mean filter, Geometric Mean Filter, Order Statistic Filters – Median Filter, Max and Min filters; Periodic Noise Reduction by Frequency Domain Filtering – Bandpass Filters; Inverse Filter, Minimum Mean-square Error Restoration.

Recommended Books:

1. Rafael C. Gonzalez & Richard E. Woods, Digital Image Processing, Pearson Education (2nd edition)
2. Anil K. Jain, Fundamentals of Digital Image Processing, Prentice Hall, Pearson Education (ISBN 81-297-0083-2)
3. Andrew Alasdar, Introduction to Digital image processing by McPublication: New Delhi: Cengage Learning. 2009 .Date: 2009
4. Jayaraman, S, Esakkirajan&Veerkumar, Digital image processing by TPublication: New Delhi: Tata McGraw Hill. 2010. Date: 2010
5. Annadurai, S. and Shanmugalakshmi,Fundamentals of digital image processing by R.Publication: New Delhi Pearson 2010.

| ECL4407 | Optical Communication | (3-0-0), 3 credits |

Course Learning Outcomes (CLO):

CLO1: Understand the fundamentals, advantages and advances in optical communication system

CLO2: Acquire a detailed understanding of types, basic properties and transmission characteristics of optical fibers

CLO3: Skilled to understand configuration and architecture of advanced optical communication, advanced system techniques and nonlinear optical effects and their applications

CLO4: Gain the knowledge of working and analysis of optical amplifiers and important devices/components of the optical communications system

CLO5: Understanding of important devices/ components of the optical communications system.

Introduction: Historical development, optical power basics, need of optical power communications, General system of optical communication system, Advantages and limitations of optical fiber communication. Basics of transmission of optical fibers: Review of optical ray theory, Light propagation in optical fiber: Total internal reflection, acceptance angle, Numerical aperture, skew rays, optical fibers structures: step index fiber, graded index fiber, propagation mode. Fiber characteristics: Introduction, attenuation, Material absorption, linear scattering losses, non-linear scattering losses, fiber bend loss, Dispersion, intermodal and intra modal dispersion. Optical sources: Light emitting diode, LED structures, LED characteristics, Basic concepts of laser, absorption and emission of radiation, population inversion, types of lasers Optical Amplifiers: Semiconductor optical amplifier & traveling wave amplifier (TWA), Gain of SOA and TWA. ERBIUM-Doped Fiber Amplifier (EDFA's), Gain and Noise in EDFA.

Recommended Books:
1. John M Senior; Optical Fiber Communications by Pearson Education, Third Edition.
2. Djafar K. Mynbaev & Lowell L, Fiber-Optics Communications Technology by Scheiner Prentice Hall, 2006..
3. R.P. Khare, Fiber Optics and Optoelctronics by Oxford publication, First edition.
4. Eldada, L., 2004. Optical communication components. Review of Scientific Instruments, 75(3), pp.575-593.

Department Elective-IV

ECL4319	Information Theory and Coding	(3-0-0), 3 credits

Course Learning Outcomes (CLO):

CLO1: Design the channel performance using Information theory:

CLO2: Comprehend various error control code properties

CLO3: Apply linear block codes for error detection and correction

CLO4: Skilled to apply convolution codes for performance analysis & cyclic codes for error detection and correction.

CLO5: Skilled to apply different codes for performance analysis for error and correction.

Information Theory-Introduction; Discrete and Continuous Messages – Message Sources, Amount of Information; Average Information and Entropy; Characteristics of a Discrete Memoryless Channel; Mutual Information; Shannon's Channel-Coding Theorem; Channel Capacity. Source Coding-Introduction; Basics of Source Encoding - Classification of Source Codes, Kraft-McMillan Inequality, Source-Coding Theorem; Source Coding Techniques – Shannon-Fano Source Code, Huffman Source Code, Lempel-Ziv Code. Error-Control Channel Coding-Types of Errors and Error-Control Codes; Hamming Codes; Cyclic Codes; BCH Codes; Hadamard Codes; LDPC Codes; Convolution Coding and Decoding; Burst-Error Correction Techniques – Interleaving, RS Codes, Turbo Codes. Spread-Spectrum Communications-Introduction, Principles of Spread-Spectrum Modulation; Spread-Spectrum Techniques – Frequency Hopping Spread-Spectrum (FHSS), FHSS with BFSK or M-ary FSK, Performance of FHSS System, Direct Sequence Spread-Spectrum (DSSS), Comparison of FHSS and DSSS, Salient Features of Spread-Spectrum Systems.

Recommended Books:
1. T L Singal, Digital Communication, ISBN: 978-93-392-1952-9, McGraw Hill Education, First Edition, Copyright © 2015.
2. Moser and Chen, A Student's Guide to Coding and Information Theory, ISBN: 978-1-107-68457-7, Cambridge University Press, First Edition, Copyright © 2012.
3. Gravano, An Introduction to Error Control Codes, ISBN: 978-0-199-23678-7, Oxford University Press, 1st edition, 2007.
4. Richard B. Wells, Applied Coding and Information Theory for Engineers, Pearson Education, 1st edition, 2009.

ECL4406	Mechatronics	(2-0-2), 3 credits
ECL1406	Mechatronics lab	

Course Learning Outcomes (CLO):

CLO1: Understand key elements of Mechatronics system, representation into block diagram

CLO2: Understand concept of transfer function, reduction and analysis

CLO3: Understand principles of sensors, its characteristics, interfacing with DAQ microcontroller

CLO4: Skilled to understand the system modeling and analysis in time domain and frequency domain.

Introduction to Mechatronics System: 9 Key elements – Mechatronics Design process –Design Parameters – Traditional and Mechatronics designs – Advanced approaches in Mechatronics - Industrial design and ergonomics, safety. System Modelling: Introduction-model categories-fields of application-model development-model verification-model validation-model simulation-design of mixed systems-electro mechanics design-model transformation- domain-independent description forms-simulator coupling. Real Time Interfacing: Introduction-selection of interfacing standards Elements of Data Acquisition & control Systems- Over view of I/O process, General purpose I/O card and its installation, Data conversion process, Application Software- Lab view Environment and its applications, Vim-Sim Environment & its applications -Man machine interface. Case Studies On Mechatronic System: Introduction, Fuzzy based Washing machine, pH control system, Autofocus Camera exposure control, Motion control using D.C. Motors & Solenoids, Engine management systems, Controlling temperature of a hot/cold reservoir using PID, Control of pick and place robot, Part identification and tracking using RFID, Online surface measurement using image processing. Micro Mechatronic System: Introduction, System principle, Component design, System design, Scaling laws, Micro actuation, Micro robot, Micro pump, Applications of micro mechatronic components.

Recommended Books:
1. Devdas shetty, Richard A. Kolk, Mechatronics System Design, 2nd Edition, Cengage Learning
2. George pelz, Mechatronic Systems: Modeling and simulation with HDL's, John Wiley and sons Ltd, 2003
3. Bishop, Robert H, Mechatronics Hand book, CRC Press, 2002
4. Bradley, D. Dawson, N.C. Burd and A.J. Loader, Mechatronics: Electronics in Products and Processes, CRC Press 1991, First Indian print 2010
5. De Silva, Mechatronics: A Foundation Course, Taylor & Francis, Indian Reprint, 2013

| **ECL4411** | **Wireless Sensor Networks** | **(3-0-0), 3 credits** |

Course Learning Outcomes (CLO):

CLO1: The students would be skilled to formulate network architecture and operating environment

CLO2: They would possess an ability to design solutions for wireless transmission technology and protocols

CLO3: The students would possess in-depth knowledge about optimization techniques for efficient operation in modern applications including healthcare helps in their employability.

CLO4: Efficient operation in modern applications including healthcare

CLO5: Application in modern technology

Introduction & Applications of Wireless Sensor Networks: Introduction, basic Overview of the Technology, Applications of Wireless Sensor Networks. Architecture: Single node architecture, Hardware components, Sensor Node Technology, Sensor Taxonomy, WN operating environment, WN Trends, Network architecture, sensor network scenarios, optimization goals and figures of merit, Gateway concepts. Networking Sensors: Physical layer, Wireless channel and communication fundamentals, frequency allocation, modulation and demodulation, wave propagation effects and noise, Wireless Transmission Technology and Systems, Radio technology primer, available wireless technologies, Medium Access Control Protocols for Wireless Sensor Networks, Fundamentals of MAC protocols, MAC protocols for WSNs, Sensor MAC case study, IEEE 802.15.4 LR-WPANs Standard Case Study, Naming & Addressing, Fundamentals, Address and name management in wireless sensor networks, assignment of MAC addresses, Routing protocols for Wireless Sensor Networks, routing challenges and design issues in wireless sensor networks, Routing strategies in wireless sensor networks, Flooding and its variants, Sensor protocols for information via negotiation, low energy adaptive clustering hierarchy, Power efficient gathering in sensor information systems, directed diffusion, Geographical routing.

Recommended Books:

1. KazemSohraby, Daniel Minoli, TaiebZnati, Wireless Sensor networks: Technology, Protocols & Applications' by, Wiley India Pvt Ltd.
2. Holger Karl & Andreas Willig, ' Protocols & Architectures for Wireless Sensor Networks' by John Wiley,2005.
3. 'Introduction to Networks Companion Guide', by Cisco Networking Academy.
4. Raghavendra, C.S., Sivalingam, K.M. and Znati, T. eds., 2006. Wireless sensor networks. Springer.

Department Elective-V

ECL4403	Microwave and Satellite Communication	(3-0-0), 3 Credits

Course Learning Outcomes (CLO):

CLO1: Students will gain complete knowledge about the significance, types and characteristics of various microwave solid state devices

CLO2: Analyze mathematically the operation and working of various tubes or sources for the transmission of the microwave frequencies

CLO3: Students will gain the basic understanding about the principles and working of RADAR.

CLO4: Students will acquire basic understanding of satellite communication and various design links in satellite communication

CLO5: Skilled in understanding the important applications of the satellite communications system.

Introduction to microwave , Klystrons, Reflex Klystrons, Magnetrons and TWT, Classification of solid state microwave devices, Microwave transistors, Diode (Tunnel, Varactor, PIN), Transferred electron devices (Gunn Diode), Avalanche transit time effect, Analysis of MW components using s-parameters, junctions, directional coupler , bends and corner, MW posts, S. S. Tuner, attenuators, phase shifter, ferrite devices (isolator , circulator , gyrator), cavity resonator, matched termination, Introduction to radar communication, Basic principle: block diagram and operation of radar, Radar range equation, PRFs and range ambiguities, application of radar, Doppler radar (Doppler determine of velocity, CW radar and its limitation, FMCW radar, basic principle and operation of MTI radar, delay line cancellers, blind speed, Origin of Satellite Communication, Technical characteristics of a satellite communications, Advantages of Satellite Communication, Active & Passive satellite, Introduction to Communication Satellite Link Design, General link design equation, system noise temperature, C/N & G/T ratio, atmospheric & ionospheric effects on link design, complete link design, interference effects on complete link design, earth station parameters.

Recommended Books:
1. M.Kulkarni, Microwave and Radar Engineering , Umesh publication , 3rd edition
2. Satellite Communication, Timothy Pratt, Charles W. Bostian , Jeremy E.Allnutt , 2nd edition
3. Dr.DC Agarwal Satellite Communication, , Khanna publishers, 5th edition
4. Elbert, B.R., 2008. Introduction to satellite communication. Artech House.

ECP1403	Microwave and Satellite Communication Lab	(0-0-2), 1 Credits

Course Learning Outcomes (CLO):

CLO1: Students will gain complete knowledge about the significance, types and characteristics of various microwave solid state devices

CLO2: Analyze mathematically the operation and working of various tubes or sources for the transmission of the microwave frequencies

CLO3: Students will gain the basic understanding about the principles and working of RADAR.

CLO4: Students will acquire basic understanding of satellite communication and various design links in satellite communication

CLO5: Skilled in understanding of important applications of the satellite communications system.

Study of microwave components and instruments, Measurement of klystron characteristics, To study the frequency and wavelength of propagating wave in a rectangular wave guide, Measurement of VSWR and standing wave ratio, Measurement of Directivity and coupling coefficient of a directional coupler, Calibration of the attenuation constant of an attenuator To Study the characteristics of various microwave Tee's. Determination of the radiation characteristics and gain of an antenna. Measurement of crystal characteristics and proof of the square law characteristics of the diode.

Recommended Books:
1. M.Kulkarni, Microwave and Radar Engineering , Umesh publication , 3rd edition
2. Satellite Communication, Timothy Pratt, Charles W. Bostian , Jeremy E.Allnutt , 2nd edition
3. Dr.DC Agarwal Satellite Communication, , Khanna publishers, 5th edition
4. Elbert, B.R., 2008. Introduction to satellite communication. Artech House.

ECL4412/ECP1412   Advance Wireless Communication/Lab   (3-0-2), 4credits

Course Learning Outcomes (CLO):

CLO1:   The students would be able to demonstrate knowledge and understanding on existing digital cellular systems and standards across the world.

CLO2:   The students would have an ability to recognize the need of 3G/4G cellular networks and evolve its architecture.

CLO3:   The students would possess the capability for evolving technological path for higher user performance in cell phone technology during their employment.

CLO4:   Efficient operation in modern applications including healthcare

CLO5:   Application in modern technology.

Architecture of 2G, Function of MSC, Functions of HLR and VLR, formats for IMSI and MSISDN, Authentication Centre functions, How to make calls 2.5 G Architecture and functions, SGSN, 3G Architecture and functions, 2.5 G TDMA,GPRS Technology, WCS and WPS Connectivity diagram, Call flows, EDGE Technology.

Recommended Books:
1. T L Singal, Wireless Communications, ISBN: 978-0-07-068178-1, McGraw Hill Education, First Edition, Copyright © 2010.
2. Andrea Goldsmith, Wireless Communications, ISBN: 978-0-52-170416-8, Cambridge University Press, 2nd Edition, Copyright © 2008.
3. UpenaDalal, Wireless Communication and Networks, ISBN: 978-0-19-809888-1, Oxford University Press, First edition, 2014.
4. William C. Y. Lee, Mobile Communications Engineering, ISBN: 978-0-07-025220-2, McGraw Hill Publication, 2nd edition, 2008.

| ECL4401 | Embedded System Design | (3-0-0), 3 credits |

Course Learning Outcomes (CLO):

CLO1: The students would be able to understand fundamental concepts and technologies related to embedded system and IoT based devices.

CLO2: The students would be able to understand the fundamentals of RTOS and application development techniques

CLO3: The students would be able to understand the various communication and networking protocols used for developing IoT enabled devices for employment.

CLO4: Skilled to understand the application development techniques.

Embedded system, Processor embedded into a system, embedded hardware units and devices in the system, embedded software in the system, Examples of embedded systems, embedded systems on chip (SoC) and use of VLSI circuit design technology, Design process in embedded system. Processor and memory organization, Introduction to advanced architectures, Instruction level parallelism, Basic processor Architecture (Intel x86), Performance metrics, Real world interfacing, Interrupts: Basics, Interrupt request, Role of Interrupt handler, Interrupt vector table, Context switching during Interrupts, Nesting of Interrupts, Shared-Data problem, Device Driver Programming. Atomic and Critical Section of the code, Interrupt latency, Solving shared-data problem with and without disabling Interrupts Software Architectures: Round-robin architecture without and with Interrupts, Function-Queue-Scheduling architecture, Real-Time Operating System(RTOS): Basic concepts: Task and task states, Role of scheduler, Preemptive and Non-preemptive RTOS, Task control block, Concept of Reentrancy, Concept of Shared-Data problem and Semaphores, Semaphore types: binary, counting and mutex, Problem of priority inversion and priority inheritance protocol.

Recommended Books:
1. Raj kamal, Embedded System Design- architecture, programming and design, Second edition, Tata Mc-Graw hill.
2. David E Simon, An Embedded Software Primer first edition, Pearson .
3. Gajski, D.D., Abdi, S., Gerstlauer, A. and Schirner, G., 2009. Embedded system design: modeling, synthesis and verification. Springer Science & Business Media.
4. Chattopadhyay, S., 2013. Embedded System Design. PHI Learning Pvt. Ltd.

ECP2401	Embedded System Design Lab	(0-0-4), 2 credits

Course Learning Outcomes (CLO):

CLO1: The students would be able to understand fundamental concepts and technologies related to embedded system and IoT based devices.

CLO2: The students would be able to understand the fundamentals of RTOS and application development techniques

CLO3: The students would be able to understand the various communication and networking protocols used for developing IoT enabled devices for employment.

CLO4: Skilled to understand the application development techniques.

1. a) Understanding PIN out description, compiler usage and API basics.
   b) Write a program to give the on-board LED in the first blinky program a delay of 0.1-1.1 seconds.
   c) Using the four on-board mbed LEDs, write a program that will use a potentiometer Input on pin 20 to continuously control how many LEDs are on.
2. a) Understanding Analog and Digital functions of mbed API
   b) Reading and Logging data from analog input mbed pin 20 and transfer serially to hyper terminal software or Tera Term software.
   c) Logging the analog data (approx. 100 samples) to Local file system using file system object in .CSV format.
   d) Generate a sawtooth voltage and analyse it using CRO.
3. a) Create a PWM signal which we can see on an oscilloscope. The code will generate a 100pulse with 50% duty cycle.
   b) Change the duty cycle to some different values, say 0.2 (20%) and 0.8 (80%) and check the correct display is seen on the 'scope,
   c) Use a pulse width modulation signal to increase and decrease the brightness of the on-board LED. The program requires the use of a host terminal application to communicate the brightness value to the mbed, using the 'u' and 'd' keys.
4. a) Interfacing 7 segment display to mbed prototype board, Program mbed using digital output to display number 5.
   b) Write a testing code to display count from 0 to 9.

Recommended Books:
1. Raj kamal, Embedded System Design- architecture, programming and design, Second edition, Tata Mc-Graw hill.
2. David E Simon, An Embedded Software Primer first edition, Pearson .
3. Gajski, D.D., Abdi, S., Gerstlauer, A. and Schirner, G., 2009. Embedded system design: modeling, synthesis and verification. Springer Science & Business Media.
4. Chattopadhyay, S., 2013. Embedded System Design. PHI Learning Pvt. Ltd.

HUL3301	Human Rights & Values	(3-0-0), 3credits

Course Learning Outcomes (CLO):

CLO1:   After completing the course students will be able to Identify constitutional or national values, social, professional, religious and aesthetic values.

CLO2:   Students will be able to link value education towards professional and employment ethics.

CLO3:   Students will be able to understand about national issues and international cooperation.

CLO4:   Students will be able to follow personal development and creation of a positive personality.

Concept of human values and value education Aim of education and value education; Concept of Human values; types of values; Components of value education.Personal development Self analysis, gender equality, physically challenged, intellectually challenged. Respect to - age, experience, maturity, family members, neighbours, co-workers. Character formation towards positive personality Truthfulness, sacrifice, sincerity, self control, Tolerance Value education towards national and global development national values - Democracy, socialism, secularism, equality, justice, liberty, freedom and fraternity Social Values - Pity , self control, universal brotherhood. Professional Values - Knowledge thirst, sincerity in profession, ethics, regularity, punctuality and faith. Religious Values - Tolerance, wisdom, character. National Integration and international understanding, Impact of global development on ethics and values Modern Challenges of Adolescent Emotions and behaviour; Comparison and competition; positive and negative thoughts., Adolescent Emotions, arrogance, anger, selfishness, defiance. Therapeutic measures Control of the mind through physical exercise, meditation (Objectives, types, effect on body, mind and soul) and Yog-sadhna. Human rights – general Concept of Human Rights – Indian and International Perspectives; Evolution of Human Rights; Definitions under Indian and International documents Human rights Right to Life and Liberty Right to Equality Right against Exploitation Cultural and Educational Rights Economic Rights Political Rights Social Rights Human rights of women and children Social Practice and Constitutional Safeguards Female Foeticide and Infanticide Physical assault and harassment Domestic violence Conditions of Working Women Institutions for implementation Violation by State Violation by Individual Nuclear Weapons and terrorism Safeguards.

Recommended Books:
1. Col KK Sharma, Human Value Education and Human Rights, Chitkara Business School, 2014
2. R. S. Naagarazan, Professional Ethics and Human Values, New Age Publishers, 2006
3. Grose, D. N, A text book of value education, Dominant Publishers and Distributors, 2005.
4. Beitz, C.R., 2009. The idea of human rights. OUP Oxford.

Appendix-A

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
AML5101	**Engineering Mathematics – I**	**CLO1:**	Define vector space, subspace, basis, dimension and spanning set of a vector space.	H											
		CLO2:	Compute eigen values, eigenvectors, inverse and rank of matrices.	H	M										
		CLO3:	Explain geometry of a complex plane and state properties of analytic functions.	M											
		CLO4:	Skilled to calculate the Taylor and Laurent series of a function of complex variable about a given point.	M											
PYL5101	**Engineering Physics**	**CLO1:**	Apply the knowledge of physics through fundamental concepts together with analytical tools in everyday life.	H											
		CLO2:	Analyze a physical problem, and suggest appropriate possible solution based on the physics concepts.			H									
		CLO3:	Explore physical systems by setting up experiments, collecting and analyzing data, identifying sources of uncertainty, and interpreting their results in terms of the fundamental principles and concepts of physics		H										
		CLO4:	Skilled to evaluate and analyze				H								

Course Code	Title of the Course		Course Learning Outcomes(CLO)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
			scientific measurement and error analysis.												
		CLO5:	Apply the fundamental concepts of physics to related engineering problems.		H										
EEL4103	Basics of Electrical Engineering	CLO1:	Students would know the basics of DC circuits, Series and parallel connections, Kirchhoff's current and voltage laws, mesh and nodal analysis. They would be able to compute various electrical engineering concepts based on real time applications.	H	H										
		CLO2:	Students would possess an ability to analyze and characterize the RL, RC & RLC circuits and have basic understanding of their implementation and also able to compute parameters related to these circuits like impedance and power. They would also learn phenomenon like resonance	H	H		M								
		CLO3:	Students would be skilled to apply and clarify fundamental principles of magnetic effects, magnetism and their functionality for electrical equipment's.						M	H					
		CLO4:	Students would possess the skill to conduct experiments, understand the principle, construction and working of Transformers, DC motors and Induction motors.				M						H		
EEP1103	Basics of	CLO1:	After completing the course, students would know the basic	H		M									

Course Code	Title of the Course	Course Learning Outcomes(CLO)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
	Electrical Engineering Lab	components of electrical elements, equipment's and their functionality with applications. With the knowledge of the basic components, students would be able to make basic electrical projects												
		CLO2: They would possess an ability to analyze and characterize the electrical equipment's and instrument's basics for their implementation.				M								
		CLO3: They would be skilled to measure power and power factor of ac circuits and understand three-phase star and delta connections with and without applying loads to calculate 3-phase power.	M	M				M						
		CLO4: Possess skill to perceive the concept of Fuse/MCB characteristics for different fault currents. Students will be familiarized with appearance and functioning of the MCB and fuse used in their homes.						M						
CHL4101	Engineering Chemistry	CLO1: Develop innovative methods to produce soft water for industrial use and potable water at cheaper cost.	L											
		CLO2: Substitute metals with conducting polymers and also produce cheaper biodegradable polymers to reduce environmental pollution.				M		M	H	H				
		CLO3: Design economically and new methods of synthesis nano materials.				M		M	H	H				

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
		CLO4:	Skilled to apply their knowledge for protection of different metals from corrosion.			M									
		CLO5:	Have the knowledge of converting solar energy into most needy electrical energy efficiently and economically to reduce the environmental pollution.				M		M	H	H				
CHP1101	Engineering Chemistry Lab	CLO1:	Develop innovative methods to produce soft water for industrial use and potable water at cheaper cost.	L											
		CLO2:	Substitute metals with conducting polymers and also produce cheaper biodegradable polymers to reduce environmental pollution.				M		M	H	H				
		CLO3:	Design economically and new methods of synthesis nano materials.				M		M	H	H				
		CLO4:	Skilled to apply their knowledge for protection of different metals from corrosion.			M									
PYP1101	Engineering Physics Lab	CLO1:	Students would be able to correlate practical knowledge of physics with the theoretical concepts.			M									
		CLO2:	Students would achieve perfectness in experimental skills related to									H			

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
			physics fundamentals.												
		CLO3:	The study of practical applications will bring more confidence.										M		
		CLO4:	Ability among the students to design, perform, document and analyze advanced experiments in physics.											M	
HUL2101	**Disaster Management**	**CLO1:**	Increase the knowledge and understanding of the disaster phenomenon, its different contextual aspects, impacts and public health consequences.						H	L					
		CLO2:	Increase the knowledge and understanding of the International Strategy for Disaster Reduction (UN-ISDR) and to increase skills and abilities for implementing the Disaster Risk Reduction (DRR) Strategy.		M				H	L					
		CLO3:	Ensure skills and abilities to analyse potential effects of disasters and of the strategies and methods to deliver public health response to avert these effects.		M				H	L					
GEL4101	**Environmental Sciences**	**CLO1:**	Understand the transnational character of environmental problems and ways of addressing them, including interactions across local to global scales.		M				H	L					
		CLO2:	Skilled to apply systems concepts and methodologies to analyze and understand interactions between		M				H	L					

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
			social and environmental processes.												
		CLO3:	Reflect critically about their roles and identities as citizens, consumers and environmental actors in a complex, interconnected world.		M				H	L					
		CLO4:	Skilled to demonstrate proficiency in quantitative methods, qualitative analysis, critical thinking, and written and oral communication needed to conduct high-level work as interdisciplinary scholars and/or practitioners.										H		
AML5102	Engineering Mathematics - II	CLO1:	To analyze and correlate many real-life problems mathematically and thus find the appropriate solution for them using Fourier series and Transforms (Fourier and Laplace transform).	H											
		CLO2:	Using ordinary differential equations student will be skilled to solve various practical problems in Science and Engineering.	H	M										
		CLO3:	Possess an ability to recognize and find families of solutions for most real physical	M											
		CLO4:	Processes such as heat transfer, elasticity, quantum mechanics, water flow and others, which are governed by partial differential equations subject to boundary conditions.	M											

Course Code	Title of the Course	Course Learning Outcomes(CLO)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
ECL5101	Basics of Electronics Engineering	CLO1: Students would know the basics of electronics elements, their functionality and applications. They would be able to perceive the concept of logic gates and integrated circuits in electronics.	H	M										
		CLO2: Skilled to interpret the characteristics of various types of diodes and transistors to describe the operation of related circuits for evolving engineering solutions.		H										
		CLO3: Students would be able to apply fundamental principles of electronics together with analytic tools to evaluate and describe physical situations appropriate to address a scientific problem.				M			H					
		CLO4: Students would possess a skill to explore physical systems by setting up experiments, collecting and analysing data, identifying sources of uncertainty, and interpreting their results in terms of the fundamental principles and concepts of electronics.						M		H				
		CLO5: Skilled to apply fundamental principles of electronics together with analytic tools		M		M								
ECP1101	Basics of Electronics Engineering Lab	CLO1: After completing the course, students would know the basics of electronics elements, their functionality and applications and would be able to design basic electronics projects.	H	M	H									

Course Code	Title of the Course	Course Learning Outcomes(CLO)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
		CLO2: They would be able to analyze and characterize the electronic circuits and have basic understanding for their implementation.		H		M								
		CLO3: They would possess a skill to perceive the concept of logic gates like XOR and X-NOR and integrated circuits in electronics.	H	M										
		CLO4: Skill of explaining the basics of electronics fundamentals	L											H
CSL4103	**Introduction to Programming Logic**	**CLO1:** Choose the appropriate programming constructs to solve the problems.		H										
		CLO2: Demonstrate the advantages and disadvantages of specific techniques to be used.					H							
		CLO3: Skilled to differentiate between efficient and inefficient way of programming.			M									
		CLO4: Determine and demonstrate bugs in a program and recognize needed basic operations.		M										
		CLO5: Formulate new solutions for programming problems or improve existing code to program effectively						H						
MEL4102	**Engineering Graphics**	**CLO1:** Improve the technical writing, basic sketching and drawing.	H											
		CLO2: Use engineering scale effectively								M				

Course Code	Title of the Course	Course Learning Outcomes(CLO)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
		CLO3: Skilled to use dimensioning effectively.								M				
		CLO4: Use development of surfaces.									M			
		CLO5: Communicate through Engineering Graphics.												L
MEP1102	**Engineering Graphics Lab**	**CLO1:** Improve the technical writing, basic sketching and drawing.												H
		CLO2: Skilled to use engineering scale effectively	L											H
MEW2101	**Manufacturing Practice Lab**	**CLO1:** The students will understand the working of engines and simple machines			L		H							
		CLO2: The students will gain knowledge about different processes involved in manufacturing process that enhances employability.		M					M					
CSP1103	**Introduction to Programming Logic Lab**	**CLO1:** Skilled to understand the problem statement using principles of mathematics and engineering sciences.	M											
		CLO2: Identify the OOPs programming constructs to solve the problems by differentiating between efficient and inefficient way of programming.		M										
		CLO3: Determine the bugs in a program and recognize the need of alternate approaches.		M										

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
		CLO4:	Acquire ability for independent and life-long learning in the broadest context of technological change.												H
		CLO5:	Provide solutions to societal, health, safety, legal, and cultural issues through contextual knowledge of professional engineering practice for employability.						H	H					
ECL4214	Analog Electronics	CLO1:	Develop the ability to understand the design and working of BJT amplifiers	M		M									
		CLO2:	Skill to design BJT based circuits and observe the amplitude and frequency responses of common amplifiers.		M	H									
		CLO3:	Skill to design and develop the audio and power amplifiers using re and hybrid equivalent models.			H									
		CLO4:	Develop the skill to build, and troubleshoot analog circuits.	H											
ECL4204	Signals & Systems	CLO1:	Categorize various types of signals and systems as continuous/ discrete.	L	L										
		CLO2:	Apply various transforms in analysis of systems with different input signals.		M										
		CLO3:	Skill to interpret the behaviour of Linear time invariant systems (Continuous & Discrete) in terms of system stability and response.			L									

Course Code	Title of the Course	Course Learning Outcomes(CLO)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
		CLO4: Skilled for evaluation of several transforms in analysis of systems with different input signals		M										
ECL4208	**Control Systems**	**CLO1:** The students would be able to understand operation of basic control systems employed in industries.	M											
		CLO2: The students would be able to propose automation solutions to real world problems					H							L
		CLO3: The students would attain skill to carry out time domain and frequency domain analysis of a designed control system.												L
		CLO4: Skilled to solve automation solutions to real world problems	M											
ECL4207	**Digital Electronics & Logic Design**	**CLO1:** Understand the basics of difference between analog and digital circuits and their applications.	H											
		CLO2: Skill to implement simple logical operations required for the designing of digital circuits and understand common forms of number representation.		M										
		CLO3: Reduction of Boolean expressions for the designing of minimized logical circuits.											H	
		CLO4: Skill to design and implementation of combinational circuits.				H								

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
		CLO5:	Skill to design and implementation of sequential circuits and their application.												L
ECP1214	**Analog Electronics Lab**	**CLO1:**	To be able to read and interpret electronic datasheets and diagrams.											M	
		CLO2:	To be able to measure the electronics & electrical parameters of an amplifier like voltage gain, input & output impedance.	H											
		CLO3:	Skill to design, construct and troubleshoot transistor based amplifier complex electronic circuits		H										
ECP1207	**Digital Electronics & Logic Design Lab**	**CLO1:**	To understand the digital logic and create various systems by using these logics.	H											
		CLO2:	Develop a skill to understand the design and simulation of digital logic circuits.		M										
		CLO3:	To get a basic understanding of layout of electronic circuits.			M									H
		CLO4:	Skill to use the Multisim tool for design and simulation.				H								
		CLO5:	Skill to design and implementation of sequential circuits and their application.				L								
ECP2203	**Measurement & Virtual Instrumentat**	**CLO1:**	The students will be skilled to design any instrumentation-based project in employment	L											

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
	ion Lab	CLO2:	The students will be skilled to simulate any type of signals and check performance of any circuit based on these simulated signals.		M										
		CLO3:	Skill of using Elvis instrument and perform experiment on it					H							
		CLO4:	Skilled to work on interfacing hardware with software.		M										
		CLO5:	Skilled for the creation of new projects.			H									H
CSL4102/ CSP2102	Problem Solving Techniques using C/Lab	CLO1:	Skilled to understand the problem statement using principles of mathematics and engineering sciences.	M											
		CLO2:	Identify the C programming concepts to solve the problems by differentiating between efficient and inefficient way of programming.				M								
		CLO3:	Determine the bugs in a program and recognize the need of alternate approaches.											M	
		CLO4:	Acquire ability for independent and life-long learning in the broadest context of technological change.											H	
		CLO5:	Provide solutions to societal, health, safety, legal, and cultural issues through contextual knowledge of professional engineering practice for employability.	L											
ECL4205	Network Analysis &	CLO1:	Students will develop sufficient knowledge on circuit analysis techniques.		H										

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
	Synthesis	CLO2:	Students will be skilled to perform time domain as well as frequency domain analysis of any electrical circuit.				M								
		CLO3:	Students will be skilled to synthesize various electrical networks like two port networks and filters circuits.												L
		CLO4:	Skilled to execute all domain analysis of any electrical circuit.			L	M								
ECL 4317	Digital Signal Processing	CLO1:	Identify different types of discrete signals, implement these signals on different systems using z transform, Discrete Fourier Transform and Fast Fourier Transform.	M											
		CLO2:	Student can apply knowledge to design and filters and implement them for signal processing applications.		M		L								
		CLO3:	Apply the knowledge to design and analyse a practical discrete-time signal system, such as a radar, image, speech, audio, bio-medical or wireless system during employment.			H									H
		CLO4:	Implementation of signal processing applications in employment		L		M								
		CLO5:	Skilled to design and analyse a practical discrete-time signal system, in their employment.			H									
ECL4314	Linear	CLO1:	Skilled to design Op-amp based	L		M									

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
	Integrated Circuits		circuit to give specified gain.												
		CLO2:	To compute component values to design different Op-amp based applications such as arithmetic building blocks, filters, waveform generators.	M		M									
		CLO3:	Develop practical skills for building and testing circuits using analog ICs during employment.		M	H	M								
		CLO4:	Able to compute component values to design different Op-amp based applications in their employment	M	L	M	M								
		CLO5:	Skilled in practical skills for building and testing circuits using analog ICs in their employment.												H
ECL4215	**Analog Communication Systems**	**CLO1:**	The students would understand various modulation concepts and distinguish between various modulation schemes on the basis of advantages, disadvantages and applications as used in analog and digital wireless communication systems.	M	H	M	M	H					M		
		CLO2:	The students would be skilled to analyze design aspects of generation and detection techniques of AM and FM signals as used in broadcast radio and TV transmissions.			H	M		M	L			L		
		CLO3:	The students would be able to select appropriate method to convert an analog signal to digital signal with suitable line coding technique for		M	H	M	L							

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
			baseband transmission systems.												
		CLO4:	They would possess an ability to apply knowledge of various digital modulation schemes to improve performance of advanced digital cellular communication systems during employment.		M		M								
ECP1305	**Digital Signal Processing Lab**	**CLO1:**	To understand and analyze the different types of signals in time domain and frequency domain.	M											
		CLO2:	Skill to design and implement the characteristics of the digital filters (FIR and IIR).			M									
		CLO3:	Can apply skill of programming using MATLAB to develop the computation of Transforms and convolution during employment.		L		M								
		CLO4:	Skill of hand-on training on MATLAB programming and simulation.		L										
		CLO5:	Skill of modeling FIR and IIR filters on MATLAB.			M		M							
ECP1314	**Linear Integrated Circuits Lab**	**CLO1:**	Skilled able to select an appropriate IC for a industrial and domestic applications by interpreting electronic datasheet.	L											

Course Code	Title of the Course	Course Learning Outcomes(CLO)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
		CLO2: Skilled to design an op amp based circuit such as filters, oscillators, generators, converters and can solve problems related to it during employment.			M									
		CLO3: Skilled to troubleshoot and replace the defective parts of op amp based electronic circuits during employment.		M		M								
		CLO4: Develop appropriate communication skills, particularly technical reports through the laboratory for student as entrepreneur.										M		
		CLO5: Skill of hands-on training of designing linear integrated circuits												H
CSL3201/ CSP2201	**Object Oriented Programming/lab**	**CLO1:** Skilled to understand the problem statement using principles of mathematics and engineering sciences.	L											
		CLO2: Identify the C++ programming concepts to solve the problems by differentiating between efficient and inefficient way of programming.		M										
		CLO3: Determine the bugs in a program and recognize the need of alternate approaches.		M		M								
		CLO4: Acquire ability for independent and life-long learning in the broadest context of technological change.												M
		CLO5: Provide solutions to societal, health, safety, legal, and cultural issues						H	M	M	M			

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			through contextual knowledge of professional engineering practice for employability.												
ECL4315	**Microprocess or & Microcontrol ler**	**CLO1:**	After completing the course students will be skilled to differentiate between the real time applications of microprocessor and a microcontroller.												H
		CLO2:	The student will be able to design a memory and I/O interface aspects for an 8085-based computer systems.			H	M								
		CLO3:	Students will develop the knowledge regarding architecture and peripheral configuration of STM32L476.			M		M							
		CLO4:	Students will be able to write embedded C code to develop applications using I/O ports, timers and other peripherals of a microcontroller which increases employability.												H
		CLO5:	Skilled to develop applications using I/O ports, timers and other peripherals of a microcontroller in their employment			H									
ECL4322	**Electromagn etic Waves & Antennas**	**CLO1:**	Develop sufficient knowledge on fundamental of Electromagnetic field theory and its applications such as Vector Calculus and Co-ordinates Systems.			H									

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		CLO2: Understand Maxwell's equations and apply them to solve practical electromagnetic fields problems.	L											
		CLO3: Analyses the behavior of EM Wave through different medium such as Transmission Lines and Waveguides.		M		M								
		CLO4: Skill to solve transmission line impedance mismatching problems in communication and power transmission using stub matching and Smith chart.		M		M								
		CLO5: Understand the basic parameters & properties of Antennas, Antenna Types, and Antenna Arrays for Antenna Gain and Directivity Enhancement.												H
ECL4321	**Digital Communication Systems**	**CLO1:** The students would be able to select appropriate method to convert an analog signal to digital signal with suitable line coding technique for baseband transmission systems.	L											
		CLO2: They would possess an ability to apply knowledge of various digital modulation schemes to improve performance of advanced digital cellular communication systems during employment.		M		M								
		CLO3: Skill of various schemes to improve performance of communication systems.			M									

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ECL4303	Microelectronic Circuits	CLO1:	After completion of the course, students will be able to construct and apply physical model to determine the electrical characteristic and operation principle of microelectronic devices.		M		M								
		CLO2:	Skill of designing digital as well as analog circuits using CMOS technology			H		M							
		CLO3:	Students will apply the concept of IC fabrication to create layouts of digital circuits at entrepreneur level.						M						
		CLO4:	Able to design both circuits using CMOS technology used in industry.			H									
		CLO5:	Execute the concept of IC fabrication to create layouts of digital circuits during their employment			M									H
ECP1206	Analog & Digital Communication Lab	CLO1:	The students would have a good understanding of both time and frequency domain representations of information and modulated signals used in analog, pulse and digital communication systems	L											
		CLO2:	They would be able to evolve functional blocks of Tx and Rx for AM/FM broadcast radio, baseband PCM transmission and digital wireless communication applications.		M		M								
		CLO3:	The students would be skilled to evaluate binary and M-ary shift keying digital modulation and				M								

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
			demodulation techniques for digital cellular applications												
		CLO4:	They would possess an ability to apply knowledge of various digital modulation schemes to improve performance of advanced digital cellular communication systems.		M		H								
		CLO5:	Skill of various schemes to improve performance of communication systems.												H
ECP2303	Microelectronic Circuits Lab	CLO1:	Students will acquire hands on experience of industry oriented circuit designing tools			H									
		CLO2:	Students will be skilled to design different digital and analog circuits and verify the same through simulation on cadence design tool.			H									
		CLO3:	Capable of designing layouts of the designed circuit in accordance with layout design rules during employment		M		M								
		CLO4:	Skill of using simulator.					M							
		CLO5:	Skill of interfacing hardware with software.					M							
ECP1315	Microprocessor & Microcontroller Lab	CLO1:	After the completion of this lab course students will be skilled to handle the technical issues during the programming and also able to evaluate possible causes of discrepancy in practical experimental observations.				H								

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		CLO2: The students will be able to write a program in assembly language to perform the specific task like arithmetic and logical operations, ON/OFF procedure for an LED pattern etc.		M		M								
		CLO3: Student will be able to understand how to Interface the external devices to the controller		M										
		CLO4: Skilled according to the user requirements to create novel products and solutions for the real time problems as entrepreneur			H									
		CLO5: Skilled to create innovation through microprocessor circuit designing			H									H
CSL5210	**Data Structures**	**CLO1:** After understanding the basic types for data structure, students will be able to implement different real world applications.		M										
		CLO2: Students will be able to determine time and memory complexity of basic algorithm constructs.		M		M								
		CLO3: Implement algorithms for the creation, insertion, deletion, and traversal of each data structure.				H								
		CLO4: Skilled to solve problems based on searching and sorting algorithms.		H		M								
		CLO5: Formulate new solutions for programming problems or improve			M									H

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
			existing code using learned algorithms during their employment.												
CSP2210	Data Structures Lab	CLO1:	After understanding the basic types for data structure, students will be able to implement different real world applications.		M										
		CLO2:	Students will be able to determine time and memory complexity of basic algorithm constructs.		M		M								
		CLO3:	Implement algorithms for the creation, insertion, deletion, and traversal of each data structure.				M		M						
		CLO4:	Skilled to solve problems based on searching and sorting algorithms.		M										H
		CLO5:	Formulate new solutions for programming problems or improve existing code using learned algorithms during their employment.		H		H								
GEW1615	Introduction to Power Electronics	CLO1:	Relate basic semiconductor physics to properties of power devices, and combine circuit mathematics and characteristics of linear and non-linear devices	L											
		CLO2:	Skilled to design and Analyze power converter circuits and learn to select suitable power electronic devices by assessing the requirements of			H									

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			application fields												
		CLO3:	Skilled to recognize the role power electronics play in the improvement of energy usage efficiency and the applications of power electronics in emerging areas.		M		M								
GEW1810	Introduction to Microcomputer Technology				M										
		CLO1:	Understanding of the layers of the model.												
		CLO2:	Students will be able to understand the different number systems.		M		M								
		CLO3:	Skilled to implement algorithms.			H	M	M							
ECL4316	Wireless & Mobile Communication	CLO1:	The students would be able to apply the knowledge of mobile communication engineering to solve coverage and call failure problems in cell phones.		M										
		CLO2:	They would be skilled to implement the cellular concept and antenna system design consideration aspects in optimizing the cellular architecture as per user needs during their employment.					M							H
		CLO3:	The students would possess in-depth knowledge to select and use optimum multiple access technique for interference-free communication.		M		H		M						

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		CLO4: The students would possess an ability and technical skills necessary to understand digital cellular standards and architecture designs.		M										
		CLO5: The students would have acquired adequate knowledge about major aspects of 3G/4G digital cellular networks.					M	M	M					
CSL4207	**Operating System**	**CLO1:** Identify different types of Operating System and their components.		M										
		CLO2: Design and implementation of new system calls for any open source operating system.			H									
		CLO3: Implementation of existing resource management algorithms in Linux operating system.				M								
		CLO4: Skilled to identify various system security and protection issues.				H								
		CLO5: Administer the system using various Operating systems (Windows and Ubuntu) for managing its resources.				M	M	M						
ECL4409	**Audio and Speech Processing**	**CLO1:** Isolated words and commands ognition		M		M								
		CLO2: Skilled to recognize continues speech..		M		M								
		CLO3: Skilled to apply Dialogue systems in various applications		M		H								

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
		CLO4:	Skilled to recognize Time and frequency domain text-to-speech synthesis.		M		H								H
CLL3301	Life Skills I	CLO1:	Recognize diverse communication styles (body language, tone of voice) and effectively increase comprehension and build rapport with others.			M									
		CLO2:	Draw comparison and demonstrate communication in a clear and direct manner. To be able to understand words and language used in formal and casual communication in entrepreneurship.			M									
		CLO3:	Use creative thinking skills to analyze and evaluate issues and arguments, to solve problems, or to make decisions during their employment and entrepreneurship.		H		H								
		CLO4:	Understanding a leader's responsibilities to assess the requirements of a task, identifying the strengths within the team, utilizing the diverse skills of the group to achieve the set objective as an entrepreneur and employee.												H
ECL4414	Analog Layout	CLO1:	Enhance the skills of integrated circuit design for designing layouts of complex circuits			H									

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	Design	**CLO2:**	Students will be able to design layouts using CMOS technology and learn industry related design tools such as Cadence Virtuoso to work as IC design engineer.			H									
		CLO3:	Skill of applying different matching techniques in layouts of analog circuits and apply those techniques to design high quality and noise tolerant layout		H										
		CLO4:	evaluate combinational and sequential logic designs using various metrics:				H								
		CLO5:	switching speed, gate count , and energy dissipation and power	L											
ECL4413	**Digital Design using FPGA**	**CLO1:**	The students completing this course are expected to understand the structure of various number systems and its application in digital design.		M										
		CLO2:	Students will be able to design the appropriate truth table from a description of a combinational logic function			M									
		CLO3:	Students will be skilled to analyze and design various combinational and sequential circuits like Comparators, Multiplexers, Encoders etc.		H										
		CLO4:	Students will be skilled to design the synchronous circuits like Pulse train generator, Pseudo Random Binary Sequence generator			H									

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
		CLO5:	Designing of Pseudo Random Binary Sequence generator			H									
ECL3312	**IOT & Embedded Systems**	**CLO1:**	Understand fundamental concepts and technologies related to embedded system and IoT based devices	L											
		CLO2:	Understand the fundamentals of RTOS and application development techniques.	L	L										
		CLO3:	Skill to write fast-executing embedded code that utilizes the CPU, memory and peripheral resources efficiently										M		
		CLO4:	Understand the various communication and networking protocols used for developing IoT enabled devices during their employment.		L										
		CLO5:	Applications of Embedded system design.	H											L
ECL3313	**VLSI Design and Verification**	**CLO1:**	Students will get a clear understanding of VLSI design flow and different types of design styles which are used for integrated circuit design					H							
		CLO2:	Students will be able to design building blocks of digital IC using different types of modelling styles used in Verilog and perform timing analysis of the blocks					H							

Course Code	Title of the Course	Course Learning Outcomes(CLO)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
		CLO3: Students will acquire skills to identify the faults associated in VLSI circuits and various techniques to test the ICs during employment.			H		H							
		CLO4: Skilled to design building blocks of digital IC using Verilog.			H		H							
HUL3303	**Project Management**	**CLO1:** Develop, implement and evaluate various stages including planning, scheduling and Execution of projects.		M		M								
		CLO2: Understand risk management, administration, costing and budgeting challenges during projects improve skill as employee and entrepreneur.				M								
		CLO3: Identify project goals, constraints and performance criteria in project implementation in entrepreneurship.		M		M								
		CLO4: Understanding of design process.			H									
		CLO5: Application in solving the societal issues				H								
HUL3302	**Digital Marketing and Entrepreneurship**													H
		CLO1: Use confidence acquired in oral and visual presentation skills to sell their ideas												
		CLO2: Implement personal skills for sales and marketing and work under pressure in entrepreneurship.										M		M

Course Code	Title of the Course	Course Learning Outcomes(CLO)		PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
		CLO3:	Develop, implement and evaluate strategies for setting up a business idea in entrepreneurship.			M	H								
		CLO4:	Implement personal skills in their employment.										M		M
ECL4311/ ECP2313	VLSI Design/VLSI Design Lab	CLO1:	Students will get a clear understanding of VLSI design flow and different types of design styles which are used for integrated circuit design		M										
		CLO2:	Students will be able to design building blocks of digital IC using different types of modelling styles used in Verilog and perform timing analysis of the blocks					M							
		CLO3:	Students will acquire skills to identify the faults associated in VLSI circuits and various techniques to test the ICs during employment.												M
		CLO4:	Skilled to design building blocks of digital IC using Verilog.		M										
GTI4301	Numerical Ability & Logical Reasoning	CLO1:	Enhance the mental and Intellectual ability and critical thinking of the students.	M											
		CLO2:	Enhance the student's ability to use numerical data as a tool to make reasonable decisions and solve problems.												H
		CLO3:	Skilled to interpret, analyze and draw logical conclusions		M										H

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		based on numerical data presented in graphs and tables.												
		CLO4: Draw logical conclusions												H
		CLO5: Skilled in problem solving strategies												H
CSL4203	**Computer Networks**	**CLO1:** Understand the small networks by following the top-down approach from application to physical layer.	H			M								
		CLO2: Acquire theoretical knowledge about the different network technologies	M		H									
		CLO3: Understand the functioning of different layers in OSI model and TCP/IP .		H				M						
		CLO4: Identify various system security and protection issues.		M										
		CLO5: Skilled to administer the system for managing its resources.											L	
CSA4120	**Introduction to CCNA Routing and Switching**	**CLO1:** Understand different topologies and small networks by following the down-top approach from physical layer to application layer.	H			M								
		CLO2: Formulate functioning of different protocols (e.g. IP, TCP, UDP, WWW, http, email, DNS) of layered networking model.	M		H									
		CLO3: Analyze basics concepts of routing, switching, and advanced technologies.		H				M						

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		CLO4: Students will be able to design simple networks using the application-driven paradigm helps in their employment		M										
		CLO5: Skilled to simulate and design network.											L	
CSA3211	**Data Analytics**	**CLO1:** Apply knowledge of dispersion on grouped and ungrouped data cases.	H			M								
		CLO2: Evaluate discrete and continuous probability distributions to various business problems in entrepreneurship.		H			M							
		CLO3: Perform Test of Hypothesis as well as calculate confidence interval for a population parameter.		H				L						
		CLO4: Calculate confidence interval for a population parameter.		M										
		CLO5: Application and its utilization.					L							
CSL5349	**Essential Programming Concepts**	**CLO1:** Students will gain an in-depth knowledge about overall syntax and semantics		M										
		CLO2: Students will be skilled to use an IDE to compile, load, save, and debug a program				M								
		CLO3: Students will develop technical thinking and problem solving ability to find an appropriate solution for a problem.		M		M								
		CLO4: Students will be able to demonstrate the ability to create test cases to			M									

Course Code	Title of the Course	Course Learning Outcomes(CLO)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
		determine that a solution produces expected outputs for given inputs												
		CLO5: Incorporation of programming in real time applications.				M								
CSL4318	**Advanced Programming Concepts**	**CLO1:** Students will gain an in-depth knowledge about overall syntax and semantics of C programs		M										
		CLO2: Students will be skilled to use an IDE to compile, load, save, and debug a C program				M								
		CLO3: Students will develop technical thinking and problem solving ability to find an appropriate solution for a problem.		M		M								
		CLO4: Students will be able to demonstrate the ability to create test cases to determine that a solution produces expected outputs for given inputs			M									
		CLO5: Incorporation of programming in real time applications.				M								
CSL4320	**Advanced Object Oriented Programming**	**CLO1:** Students will gain an in-depth knowledge about overall syntax and semantics of C++ programs		M										
		CLO2: Students will be skilled to use an IDE to compile, load, save, and debug a C++ program				M								
		CLO3: Students will develop technical thinking and problem solving ability to find an appropriate solution for a problem.		M		M								

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		CLO4:	Students will be able to demonstrate the ability to create test cases to determine that a solution produces expected outputs for given inputs			M									
		CLO5:	Incorporation of programming in real time applications				M								
CLP2305	Industry Interface	CLO1:	Recognize diverse communication styles (body language, tone of voice) and effectively increase comprehension and build rapport with others.										M		
		CLO2:	Draw comparison and demonstrate communication in a clear and direct manner. To be able to understand words and language used in formal and casual communication in entrepreneurship.		M		M								
		CLO3:	Use creative thinking skills to analyze and evaluate issues and arguments, to solve problems, or to make decisions during their employment and entrepreneurship.		H		H								
		CLO4:	Understanding a leader's responsibilities to assess the requirements of a task, identifying the strengths within the team, utilizing the diverse skills of the group to achieve the set objective as an entrepreneur and employee.												H
HUL2401	Cyber Security	CLO1:	Acquire Information and risk models including confidentiality, integrity and availability		M		M								

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		CLO2:	Acquire skill to identify the Threats and attacks and exploit vulnerabilities		M		M								
		CLO3:	Gain suffiicent knowledge on Cyber security architecture and operations and acquire ability to handle the threats during employment												H
CSL3308	Artificial Intelligence and Expert System	CLO1:	Students will be skilled to apply problem solving techniques associated with artificial intelligence		H		H								
		CLO2:	Apply predicate logic and fuzzy logic to represent system in artificial intelligence.		H										
		CLO3:	Skilled to solve techniques associated with artificial intelligence		H		H								
		CLO4:	Skilled to represent fuzzy logic to represent system in artificial intelligence				H								
		CLO5:	Skilled to solve intelligence expert system.				M								
ECL4404/ ECP1404	Digital Image Processing/Lab	CLO1:	After the completion of the course student will be able to understand the fundamental concepts of a digital image processing system like Image formation, Image sampling and quantization	L											
		CLO2:	Students will develop the knowledge to analyze the different images in the frequency domain using various		M	H									

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			transforms												
		CLO3:	Students will be skilled to realize the importance of filters for the images and also they will be able to differentiate between the different types of filters.			M	H								
		CLO4:	Applications of image processing in recognition			M	M	H							
		CLO5:	Applications in real-time problem solving.			M	M	H							
ECL4407	Optical Communication	CLO1:	Understand the fundamentals, advantages and advances in optical communication system		M										
		CLO2:	Acquire a detailed understanding of types, basic properties and transmission characteristics of optical fibers		M		M								
		CLO3:	Skilled to understand configuration and architecture of advanced optical communication, advanced system techniques and nonlinear optical effects and their applications				M								
		CLO4:	Gain the knowledge of working and analysis of optical amplifiers and important devices/components of the optical communications system		H		M								
		CLO5:	Understanding of important devices/ components of the optical												H

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			communications system.												
ECL4319	**Information Theory and Coding**	**CLO1:**	Design the channel performance using Information theory:			M									
		CLO2:	Comprehend various error control code properties		M										
		CLO3:	Apply linear block codes for error detection and correction		M		M								
		CLO4:	Skilled to apply convolution codes for performance analysis & cyclic codes for error detection and correction.				H								
		CLO5:	Skilled to apply different codes for performance analysis for error and correction.				H								
ECL4406/ ECL1406	**Mechatronics/Lab**	**CLO1:**	Understand key elements of Mechatronics system, representation into block diagram	L											
		CLO2:	Understand concept of transfer function, reduction and analysis	M											
		CLO3:	Understand principles of sensors, its characteristics, interfacing with DAQ microcontroller		M										
		CLO4:	Skilled to understand the system modeling and analysis in time domain and frequency domain.		M		M								
ECL4411	**Wireless**		The students would be skilled to formulate network architecture and		L										

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	Sensor Networks	**CLO1:**	operating environment												
		CLO2:	They would possess an ability to design solutions for wireless transmission technology and protocols			M									
		CLO3:	The students would possess in-depth knowledge about optimization techniques for efficient operation in modern applications including healthcare helps in their employability.				H		M						
		CLO4:	Efficient operation in modern applications including healthcare						H						
		CLO5:	Application in modern technology.												H
ECL4403	**Microwave and Satellite Communication**	**CLO1:**	Students will gain complete knowledge about the significance, types and characteristics of various microwave solid state devices		M										
		CLO2:	Analyze mathematically the operation and working of various tubes or sources for the transmission of the microwave frequencies	H											
		CLO3:	Students will gain the basic understanding about the principles and working of RADAR.		M		M								
		CLO4:	Students will acquire basic understanding of satellite communication and various design links in satellite communication		M		M								

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		CLO5:	Skilled in understanding of important applications of the satellite communications system.		M		M								
ECP1403	**Microwave and Satellite Communication Lab**	**CLO1:**	Students will gain complete knowledge about the significance, types and characteristics of various microwave solid state devices		M										
		CLO2:	Analyze mathematically the operation and working of various tubes or sources for the transmission of the microwave frequencies	H											
		CLO3:	Students will gain the basic understanding about the principles and working of RADAR.		M		M								
		CLO4:	Students will acquire basic understanding of satellite communication and various design links in satellite communication		M		M								
		CLO5:	Skilled to understanding of important applications of the satellite communications system.		M		M								
ECL4412/ ECP1412	**Advance Wireless Communication/Lab**	**CLO1:**	The students would be able to demonstrate knowledge and understanding on existing digital cellular systems and standards across the world.						L						
		CLO2:	The students would have an ability to recognize the need of 3G/4G cellular networks and evolve its architecture.							M			H		

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		CLO3: The students would possess the capability for evolving technological path for higher user performance in cell phone technology during their employment.							M					L
		CLO4: Efficient operation in modern applications including healthcare		M										
		CLO5: Application in modern technology.				L								
ECL4401	**Embedded System Design**	**CLO1:** The students would be able to understand fundamental concepts and technologies related to embedded system and IoT based devices.		H										
		CLO2: The students would be able to understand the fundamentals of RTOS and application development techniques				M								
		CLO3: The students would be able to understand the various communication and networking protocols used for developing IoT enabled devices for employment.					L							
		CLO4: Skilled to understand the application development techniques.				L								
ECP2401	**Embedded System Design Lab**	**CLO1:** The students would be able to understand fundamental concepts and technologies related to embedded system and IoT based devices.		H										
		CLO2: The students would be able to understand the fundamentals of				M								

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			RTOS and application development techniques												
		CLO3:	The students would be able to understand the various communication and networking protocols used for developing IoT enabled devices for employment.					L							
		CLO4:	Skilled to understand the application development techniques.				L								
HUL3301	Human Rights & Values	CLO1:	After completing the course students will be able to Identify constitutional or national values, social, professional, religious and aesthetic values.						H	L	H				
		CLO2:	Students will be able to link value education towards professional and employment ethics.								H				
		CLO3:	Students will be able to understand about national issues and international cooperation.											H	
		CLO4:	Students will be able to follow personal development and creation of a positive personality.												H